

A B S T R A C T

An array substrate includes m pixel columns in each of which n pixels PX are arranged in an effective display portion DSP_{eff} , and dummy pixel columns obtained by arranging dummy pixels outside the effective display portion DSP_{eff} . Each pixel and each dummy pixel include a switching element placed at the intersection of each scanning line and each signal line. One switching element is connected per row to each signal line. A switching element in the N th row of the M th pixel column and a switching element in the $(N + 1)$ th row of the $(M + 1)$ th pixel column are connected to the same signal line, and video signals having opposite polarities are supplied to adjacent signal lines.



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