

A WAFER PROTECTIVE CASSETTE

BACKGROUND OF THE INVENTION

(1) Technical Field

The present invention relates to a wafer cassette which holds a plurality of semiconductor substrates and more particularly to a cassette for containing and supporting a plurality of thin substrates such as semiconductor wafers and for storing and handling the fragile wafers, each of which are loaded and unloaded numerous times during the forming of integrated circuits therein.

(2) Description of the Prior Art

A cassette has been conventionally used for storing and supporting a plurality of fragile semiconductor substrates. The stored substrates, each of which are loaded and unloaded many times during the manufacturing process, both manually and automatically, are susceptible to damage. Cassettes are typically molded as a one-piece container is described with reference to Figs. 1, 2 and 3, of the prior art. The cassette 120 is made in the form of an open container having only four sides with a wide opening at the top for inserting and removing substrates, and a narrower opening, relative to the outside dimensions of a substrate at the bottom of the cassette. This allows maximum exposure to process fluids while securely containing each substrate. The four sides include two side panels 121, 122 and two end panels 123, 124. Each side panel has a straight upper

portion and a curved lower portion with grooves that have an elongated V shaped cross-section 125 formed within its interior surfaces. Each curved portion converges inward forming the narrower opening for the bottom; this is best illustrated in Fig.1. In this conventional cassette 120 having the aforementioned configuration, a plurality of substrates 10 are contained in parallel with each other while guided and retained in the elongated V grooves 125.

The substrates are usually loaded or unloaded when the cassette is in a vertical position as shown in Fig. 3 illustrating a cross-sectional side view of a cassette 120 showing a common problem of the substrates 10 protruding out beyond the front opening 130 of the cassette. When the cassette is placed in a vertical position, any vibration that may be caused by a myriad of disturbances can easily cause substrates to slide out from inside the cassette to a position of exposure to damage by interfering with substrate handling hardware. This has always been a problem with conventional cassettes when handling fragile substrates. During automatic handling of substrates, by robots and the like, the fragile substrates are vulnerable to breakage during insertion or removal because of this protrusion problem.

BACKGROUND OF THE RELATED ART

The present invention is concerned with substrate damage during transportation. A plurality of substrates contained in a cassette is held in place only if the cassette is placed in a horizontal position. The substrates are reasonably secure as provided by grooves that have an elongated V shaped cross-section. The V grooves are formed on opposing inner sides of the cassette and provide a means to hold and support a plurality of

substrates at various process steps throughout manufacturing. If the cassette is placed in a vertical position, any source of vibration, caused by a myriad of disturbances, can effect the position of substrates within the cassette. The substrates may jut out and be exposed to damage by interfering with handling hardware, as shown in Fig. 2. This has always been a problem with conventional cassettes when handling fragile substrates.

Typically, substrates are arranged within the cassette outside of a semiconductor process operation. The cassette is thereafter transported, with its compliment of substrates inside, to a processing station. Some processing stations are designed to process the substrates while in the cassettes. Therefore, a list of primary goals for providing a universal cassette design is as follows: a) safely contain the substrates in a stacked and space saving array. b) Allow as much access to each substrate without inhibiting processing fluids from making contact with all surfaces on a substrate. c) Have a reasonable spacing between substrates for accessing each for removal and insertion. d) Choose a cassette material that is compatible with processing chemicals and with its contents while maintaining dimensional integrity between cassettes.

Accordingly, a primary object of the present invention is to provide a curbing member that substantially reduces a substrate movement from jutting forward when a cassette is placed in a vertical position and bumped.

Another object of the present invention is to provide a channel design that allows freedom of movement for the substrate, one that does not induce stresses by overly constraining. This is common in the prior art when the substrate moves against the inner apex of the groove promoting breakage or chipping of the highly stressed edges.

Still another object of the present invention is to provide a cassette design that does not change the overall cassette dimensions or its spacing between substrates.

SUMMARY OF THE INVENTION

Accordingly, the present invention provides a solution to eliminate the abovementioned problems. To achieve the foregoing objects, in accordance with a first object of the present invention, there is provided a substrate cassette that embodies grooves, each with a curbing member that prevents substrates from jutting forward when a cassette is placed in a vertical position and bumped.

The present invention relates to a wafer cassette which holds a plurality of semiconductor substrates and more particularly to a cassette for containing and supporting a plurality of thin substrates such as semiconductor wafers and for storing and handling the fragile wafers, each of which are loaded and unloaded numerous times during the forming of integrated circuits therein.

The cassette is molded in one piece made in the form of an open container having only four sides with a wide opening at the top for inserting and removing substrates, and a narrower opening, relative to the outside dimensions of a substrate, at the bottom of the cassette. This allows maximum exposure to process fluids while securely containing each of a plurality of substrates. The four sides include two side panels and two end panels. Each side panel has a straight upper portion and a curved lower portion the curved portions converge inward forming the narrower opening for the bottom. A plurality of substrate supporting channels is molded into each interior panel surface. The channels

are provided on each of the side panels of the cassette in parallel and in alignment with the supporting channels on the opposite side. The supporting channels have a slight open draft angle of about three degrees. Each channel has a molded curbing member disposed preventing a substrate from jutting forward when a cassette is placed in a vertical position and bumped.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a top perspective view of a cassette showing a saw tooth groove pattern of the prior art.

FIG. 2 is a schematic side cross-sectional view of a plurality of wafers in a cassette of the prior art illustrating the problem of wafers protruding out of the cassette.

FIG. 3 is an cross-section drawing of an end view of a cassette showing a substrate retained by curbing members, of the invention.

FIG. 4 is a sectional fragmented view of the cassette shown in FIG. 3, of the invention.

FIG. 5 is a perspective illustration of the cassette of the invention.

FIG. 6 is a perspective illustration showing a left side panel of the invention.

FIG. 7 is a development layout of the construction of the curbing member of the invention.

FIG. 8 is an illustration of the invention showing an enlarged fragmented front view of the invention.

FIG. 9 is a front view illustration of a cassette of the invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

The present invention is concerned with substrate damage, in a cassette because of movement during transportation. Refer now to FIGS. 1 and 2 of the prior art. A plurality of substrates 10 contained in a cassette 120 are held in place only if the cassette is placed in a horizontal position, the substrates are reasonably secure by their weight and by grooves 125 that have an elongated V shaped cross-sections. The grooves are formed on opposing inner sides of the cassette and provide a means to hold and support a plurality of substrates at various process steps throughout manufacturing. When a cassette is placed in a vertical position, as illustrated in FIG. 2, vibration caused by a myriad of disturbances, may cause substrates to project out from a position within the cassette 120 into a position 130 of interference with substrate handling hardware. This has always been a problem with conventional cassettes when handling the fragile substrates.

Typically, substrates are arranged within the cassette outside of a semiconductor process operation. The cassette is thereafter transported, with its complement of substrates inside, to a processing station. Some processing stations are designed to process the substrates while in the cassettes. The primary goals for a universal cassette

design are as follows: a) safely contain the substrates in a stacked and space saving array. b) Allow access to each substrate without inhibiting process fluids from making contact with all surfaces on a substrate. c) Have a repeatable and precise spacing between substrates for accessing each for removal and insertion. d) Choose a cassette material that is compatible with process chemicals and temperatures while maintaining dimensional integrity between cassettes. e) Provide proper drainage of process fluids by having no fluid traps.

In the following particular embodiment of the substrate cassette, of the present invention, is described in detail by making reference to the accompanying drawings.

Referring now to FIGS. 3-8, of the invention, the substrate cassette 20 has a conventional form factor, that is, an open container having an upper entrance 29 and a lower cassette support opening 28 formed by two side panels 21 and 22 and two end panels 23 and 24. A preferred embodiment of the invention includes a first side panel 21 having an inner surface 38 thereof, a train 31 of substrate supporting "U" shaped channels 40 molded with a left surface 41, a right surface 42 and a bottom surface 43, the left and right side surfaces are within about two degrees of being perpendicular to the bottom surface that provides a slight open angle for withdrawal from molding tooling. A second side panel 22 opposite and in alignment to the first side panel 21, also with an inner surface 39 and a matching train 37 of substrate supporting channels 40.

Referring to FIGS. 5 and 6 an arcuate curbing member 30 is disposed on a left surface 41 in each of the U shaped substrate supporting channels 40, including a top end having a thinner sloped portion 36 facing the upper open entrance of cassette 20 letting a

substrate pass down through the thinner sloped profile and under a stepped bottom end. The stepped end is perpendicular to the inner surface 38 and prevents the substrate from jutting forward, see FIG. 8. The placement of the arcuate curbing member 30 relative to the substrate 10 is shown in a construction layout in FIG.7. The arcuate curvatures 32 and 33 of the curbing member 30 are of the same radii as the periphery of substrate 10. The location of the stepped bottom end is placed so that the substrate does not jut out beyond the top edge of the cassette. The edge of the sloped profile is determined to permit the substrate freedom to move to the limit in the X direction which is offset (the difference of the X offset and the wafer diameter). The inner surfaces 41, 42, 43 of the substrate supporting channels 40 are perpendicular to each other and planar.

FIG.9 illustrates a front view of a cassette 20 in a vertical position showing substrate 10 in a cassette and behind the curbing members 30, all of which are horizontal on the supporting channel planar surfaces. The space 35 between substrates is shown to be consistently uniform for the following reason. Since the supporting channel serves as a horizontal reference plane, substrate over-constraint as posed by the V slot shown in FIGS. 1, of the prior art is not a factor. The planar surface allows a substrate to be horizontal by definition, therefore, exposure to stress fracturing or chipping is reduced when bumped as in the case when restrained by the converging apex of a V shaped slot. The supporting channel design and its arcuate curbing member is applicable to all substrate sizes.

While the invention has been particularly shown and described with reference to the preferred embodiment, it will be understood by those skilled in the art that various

changes in form and details may be made without departing from the spirit and scope of the inventions.

What is claimed is:

1. A substrate carrier for securing semiconductor substrates, comprising:
 - a cassette having
 - an upper substrate entrance.
 - a lower substrate support opening.
 - 5 a first side panel having an inner surface thereof, a train of parallel substrate support channels, said support channels having a bottom surface, a left side surface and a right side surface, said left and right side surfaces are perpendicular to said bottom surface.
 - a second side panel opposite said first side panel, having, on an inner
 - 10 surface a matching train of parallel substrate support channels;
 - an arcuate curbing member. disposed on a left side surface in each of said channels, and including
 - a top end having a sloped segment facing said entrance of said cassette permitting a substrate to slide by a necked portion of said substrate
 - 15 support channel, provided by said sloped segment, into a stepped bottom, therein securing said substrate.
2. The substrate carrier according to claim 1 wherein said upper
- entrance and said lower cassette support opening are formed by said first and
- 20 second side panels while fixed apart by two end panels.

3. The substrate carrier according to claim 1 wherein said substrate support channel surfaces are planar.

25 4. The substrate carrier according to claim 1 wherein said shape of curbing member restricts said substrates from jutting towards said substrate entrance.

 5. The substrate carrier according to claim 1 wherein all the substrate
30 support channels defined by the substrate size are spaced from immediately adjacent substrates at equal distances.

 6. The substrate carrier according to claim 1 wherein said substrate support channels are configured to hold substrates in a vertical orientation.

35 7. The substrate carrier according to claim 1 wherein said parallel and perpendicular surfaces of said substrate support channels provides stress free containment of a fragile substrate by providing substrate freedom of movement within the cassette while preventing said substrate from jutting forward past said
40 curbing member.

 8. The substrate carrier according to claim 7 wherein said substrate support channels having a shape of three perpendicular surfaces prevents substrate chipping and breaking of highly stressed substrate edges.

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9. The substrate carrier according to claim 1 wherein said stepped bottom of said arcuate curbing member is placed so that said substrate does not jut out beyond the top edge of said cassette.

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10. The substrate carrier according to claim 1 wherein an edge of said sloped profile is determined to permit the substrate freedom to move laterally to the limit provided by an offset dimension which is the difference of the lateral offset and the substrate diameter.

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11. A wafer carrier for securing semiconductor wafers, comprising:

a cassette having

an upper wafer entrance.

a lower wafer support opening.

a first side panel having an inner surface thereof, a train of parallel

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wafer support channels, said support channels having a bottom surface, a left side surface and a right side surface, said left and right side surfaces are perpendicular to said bottom surface.

a second side panel opposite said first side panel, having, on an inner surface a matching train of parallel wafer support channels;

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an arcuate curbing member. disposed on a left side surface in each of said channels, and including

a top end having a sloped segment facing said entrance of said cassette

70 permitting a wafer to slide by a necked portion of said wafer
support channel, provided by said sloped segment, into a stepped
bottom, therein securing said wafer.

75 12. The wafer carrier according to claim 11 wherein said upper
entrance and said lower cassette support opening are formed by said first and
second side panels while fixed apart by two end panels.

13. The wafer carrier according to claim 11 wherein said wafer support
channel surfaces are planar.

80 14. The wafer carrier according to claim 11 wherein said shape of
curbing member restricts said wafers from jutting towards said wafer
entrance.

85 15. The wafer carrier according to claim 11 wherein all the wafer support
channels defined by the wafer size are spaced from immediately adjacent wafers
at equal distances.

16. The wafer carrier according to claim 11 wherein said wafer support
channels are configured to hold wafers in a vertical orientation.

90 17. The wafer carrier according to claim 11 wherein said parallel and

perpendicular surfaces of said wafer support channels provides stress free containment of a fragile wafer by providing wafer freedom of movement within the cassette while preventing said wafer from jutting forward past said curbing member.

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18. The wafer carrier according to claim 17 wherein said wafer support channels having a shape of three perpendicular surfaces prevents wafer chipping and breaking of highly stressed wafer edges.

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19. The wafer carrier according to claim 11 wherein said stepped bottom of said arcuate curbing member is placed so that said wafer does not jut out beyond the top edge of said cassette.

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20. The wafer carrier according to claim 11 wherein an edge of said sloped profile is determined to permit the wafer freedom to move laterally to the limit provided by an offset dimension which is the difference of the lateral offset and the wafer diameter.

21. A method for protecting semiconductor wafers in a wafer carrier, 110 comprising the steps of:

providing a cassette having

an upper wafer entrance.

a lower wafer support opening.

115 a first side panel having an inner surface thereof, a train of parallel
wafer support channels, said support channels having a bottom
surface, a left side surface and a right side surface, said left and right
side surfaces are perpendicular to said bottom surface.
a second side panel opposite said first side panel, having, on an inner
surface a matching train of parallel wafer support channels;
120 an arcuate curbing member. disposed on a left side surface in each of said
channels, and including
a top end having a sloped segment facing said entrance of said cassette
permitting a wafer to slide by a necked portion of said wafer
support channel, provided by said sloped segment, into a stepped
125 bottom, therein securing said wafer.

22. A method according to claim 21 wherein said upper entrance and said
lower cassette support opening providing liquid chemical access to all surfaces of
contained wafers .

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23. A method according to claim 21 wherein said wafer support
channel surfaces are planar thereby permitting unrestrained freedom of the wafers
during wet processes and during handling.

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24. A method according to claim 21 wherein said shape of curbing
member restricts said wafers from jutting towards said wafer entrance during

handling.

25. A method according to claim 21 wherein all the wafer support
140 channels defined by the wafer size are spaced from immediately adjacent wafers
at equal distances.

26. A method according to claim 21 wherein said wafer support
channels are configured to hold wafers in a vertical orientation.

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27. A method according to claim 21 wherein said parallel and
perpendicular surfaces of said wafer support channels provides stress free
containment of a fragile wafer by providing wafer freedom of movement within
the cassette while preventing said wafer from jutting forward past said curbing
150 member.

28. A method according to claim 7 wherein said wafer support
channels having a shape of three perpendicular surfaces prevents wafer
chipping and breaking of highly stressed wafer edges.

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29. A method according to claim 21 wherein said stepped bottom of said
arcuate curbing member is placed so that said wafer does not jut out beyond the
top edge of said cassette.

30. A method according to claim 21 wherein an edge of said sloped

160 profile is determined to permit the wafer freedom to move laterally to the limit provided by an offset dimension which is the difference of the lateral offset and the wafer diameter.