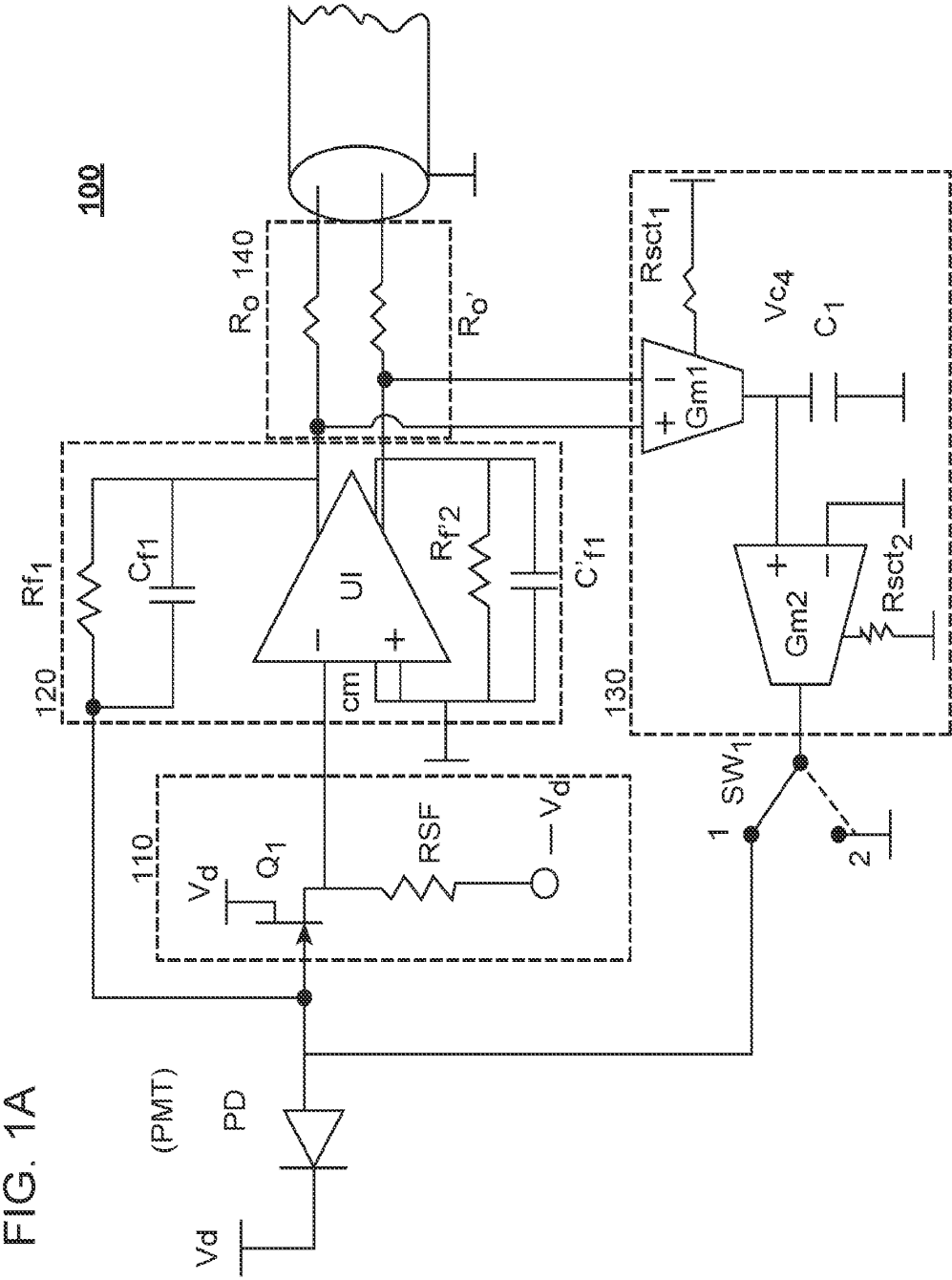
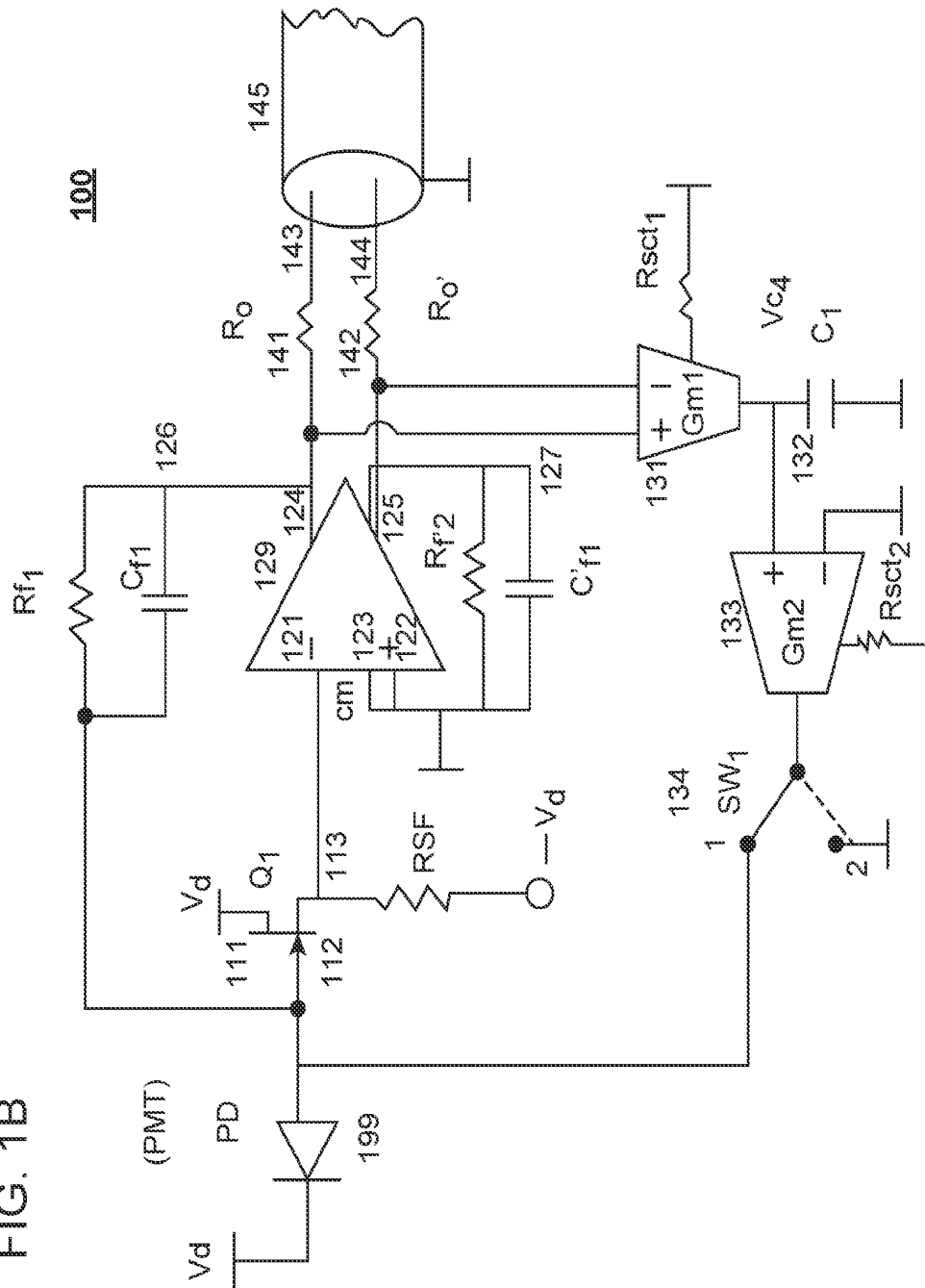
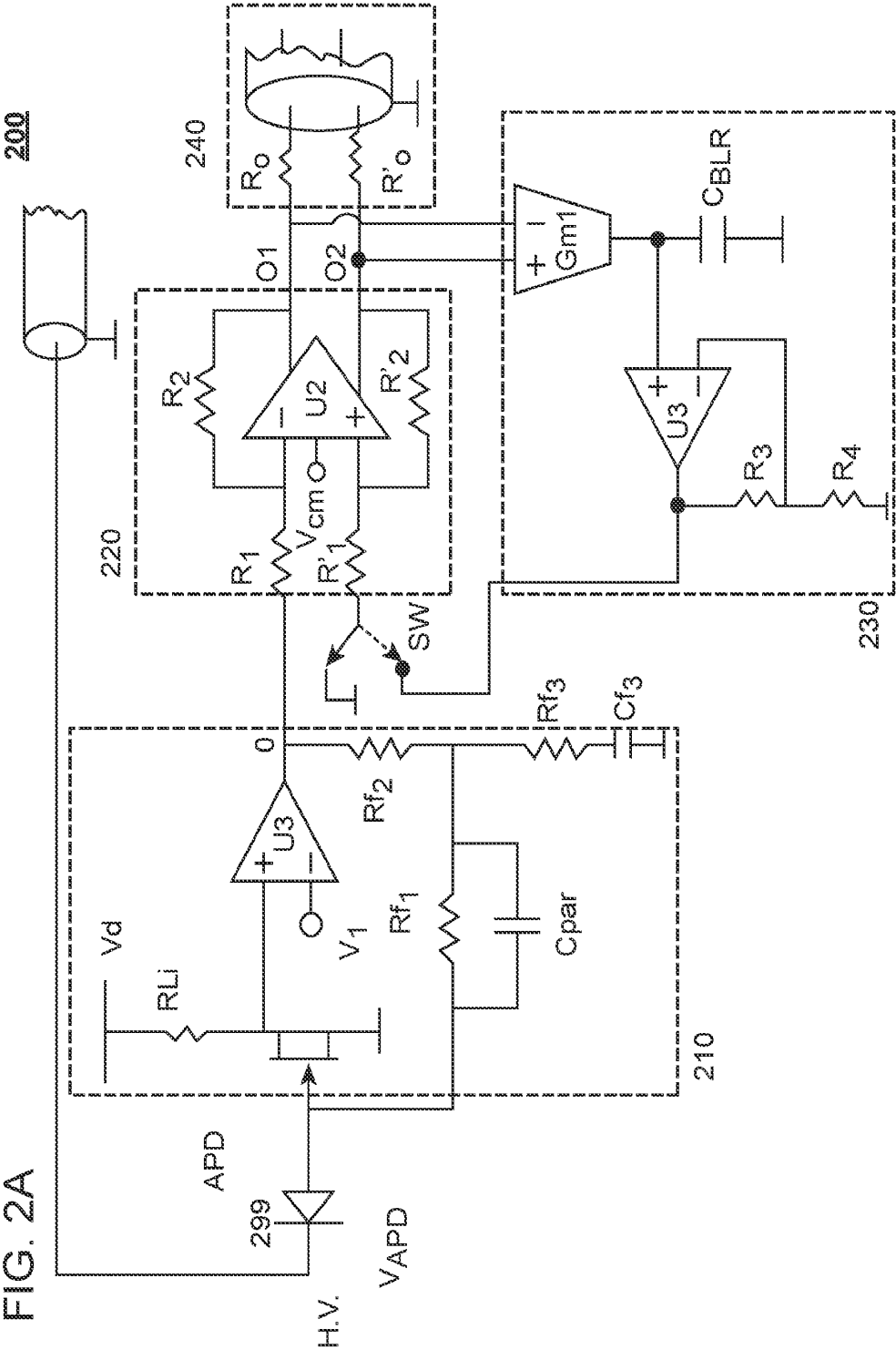
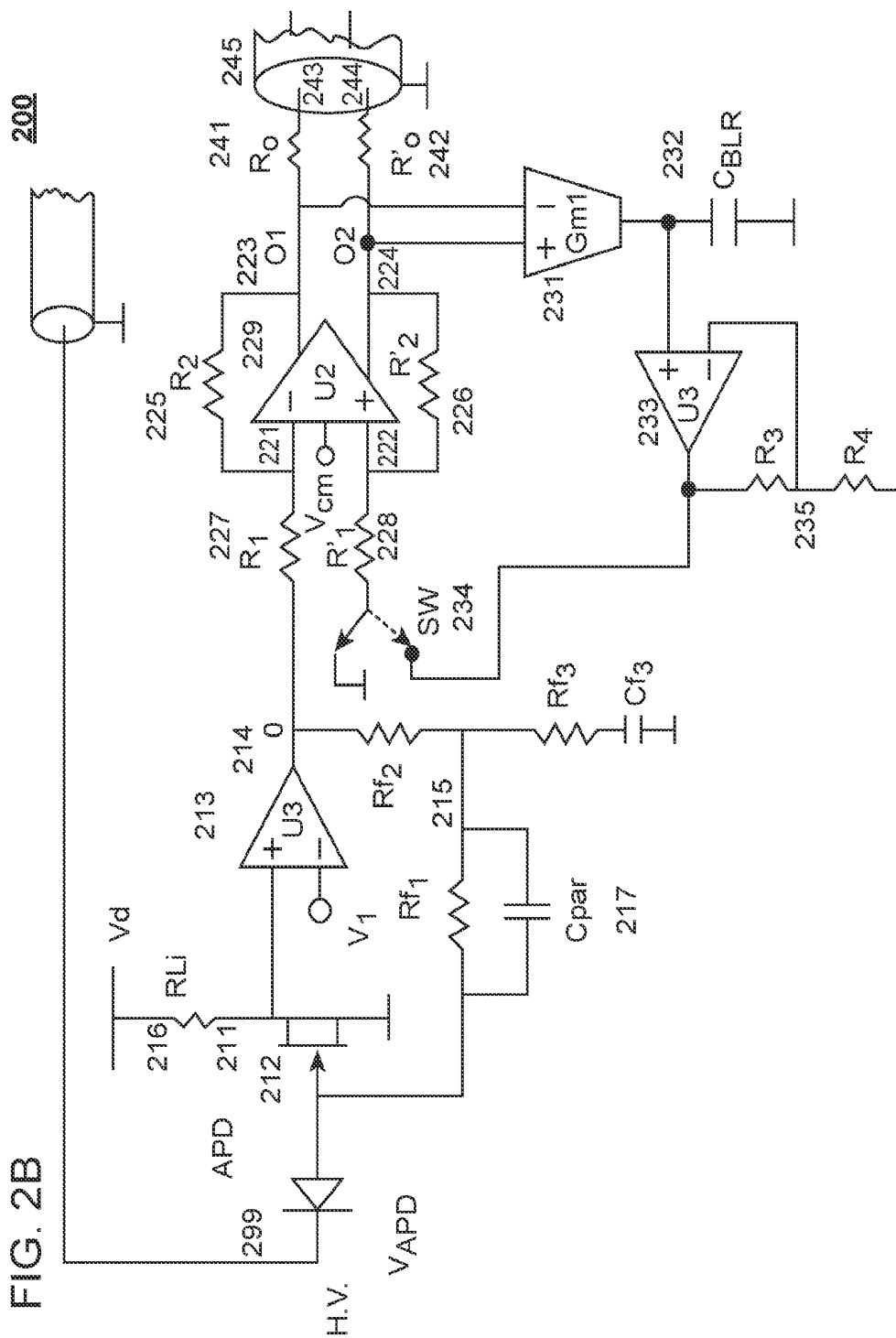




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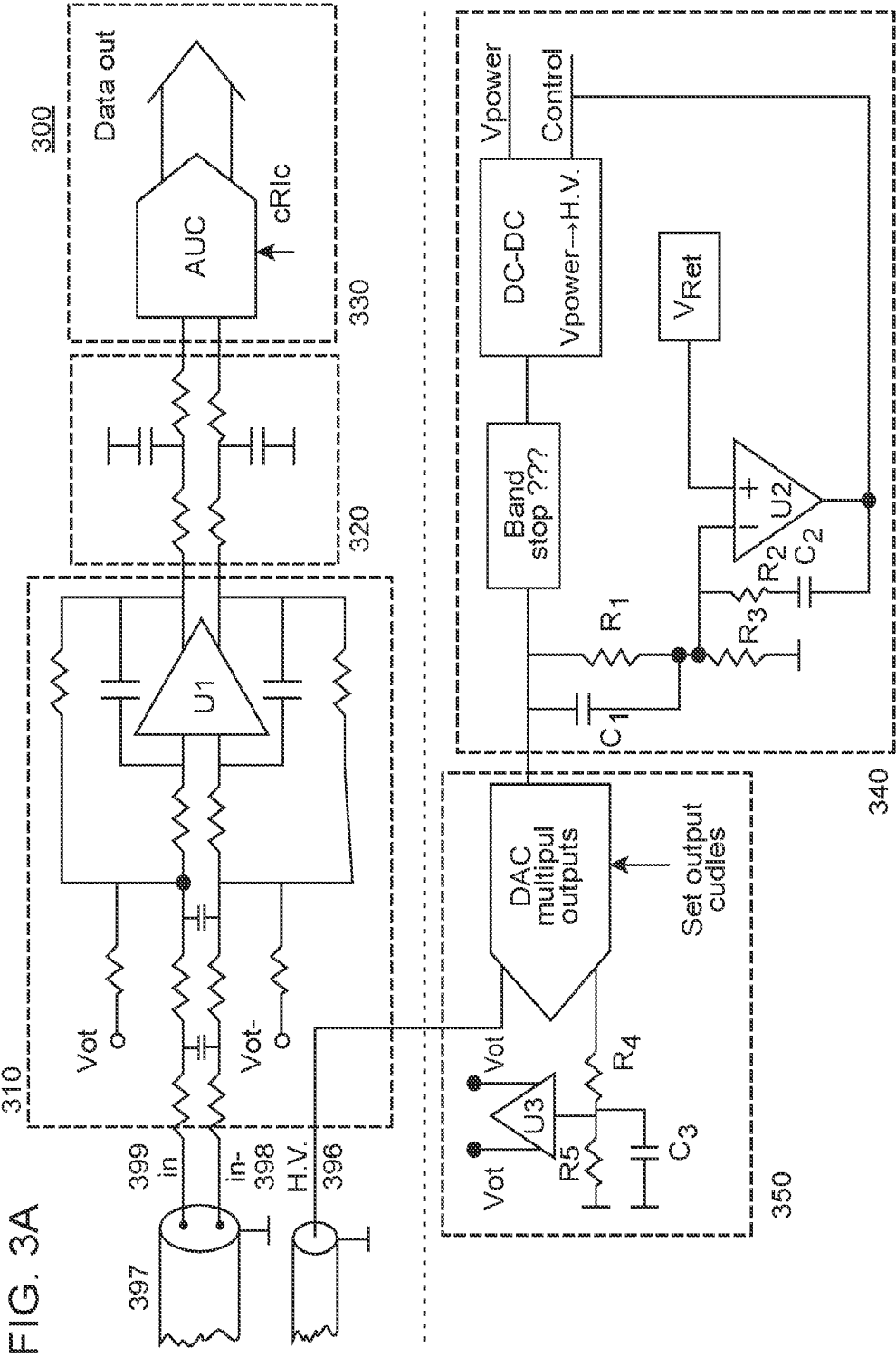
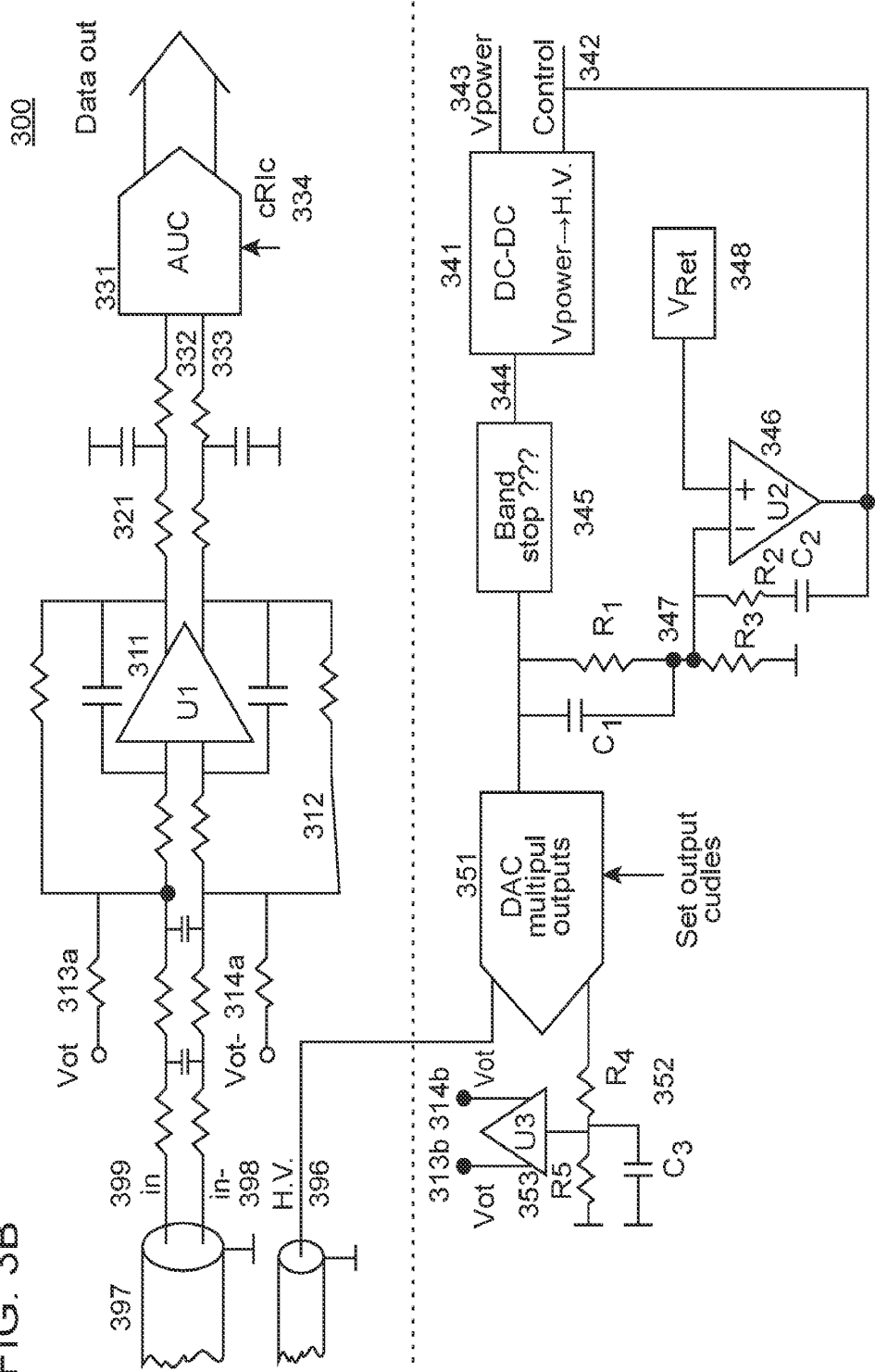


FIG. 3B



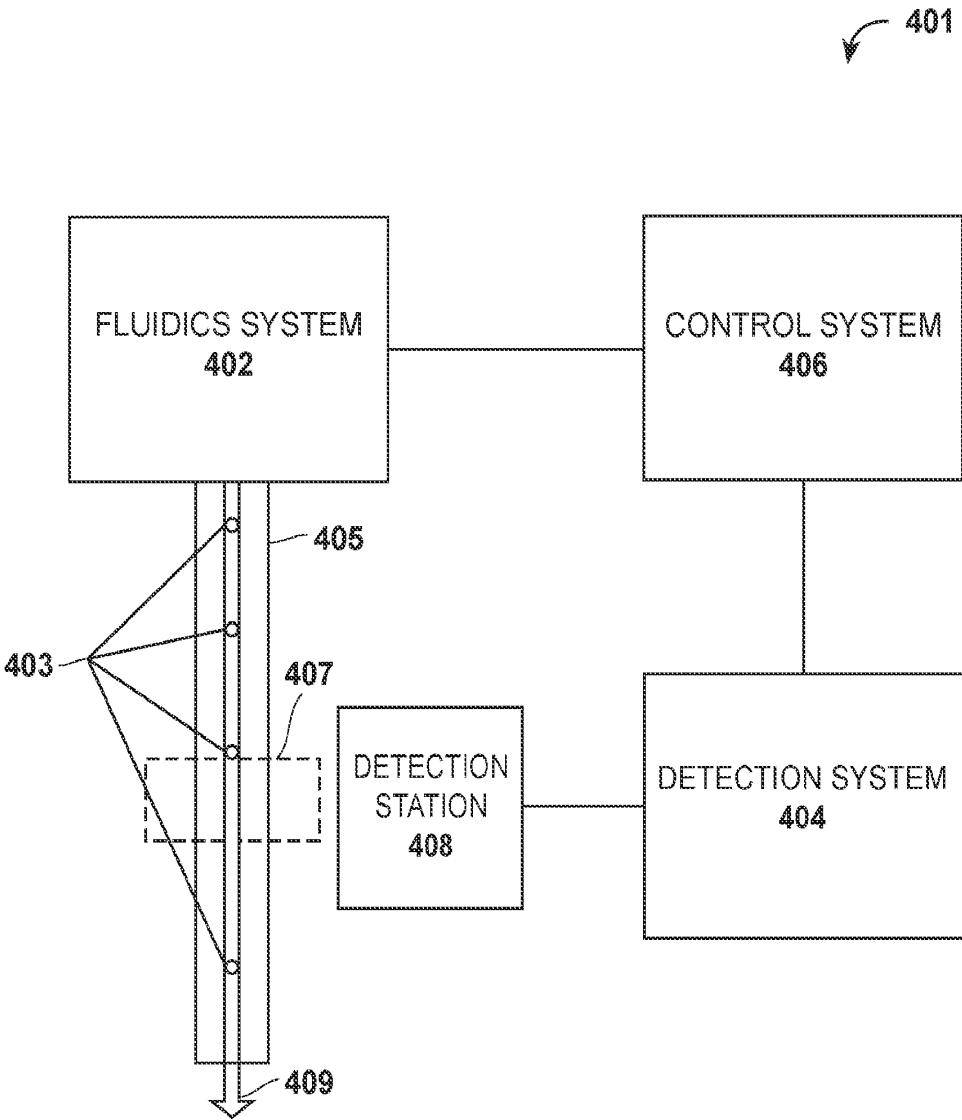


FIG. 4A

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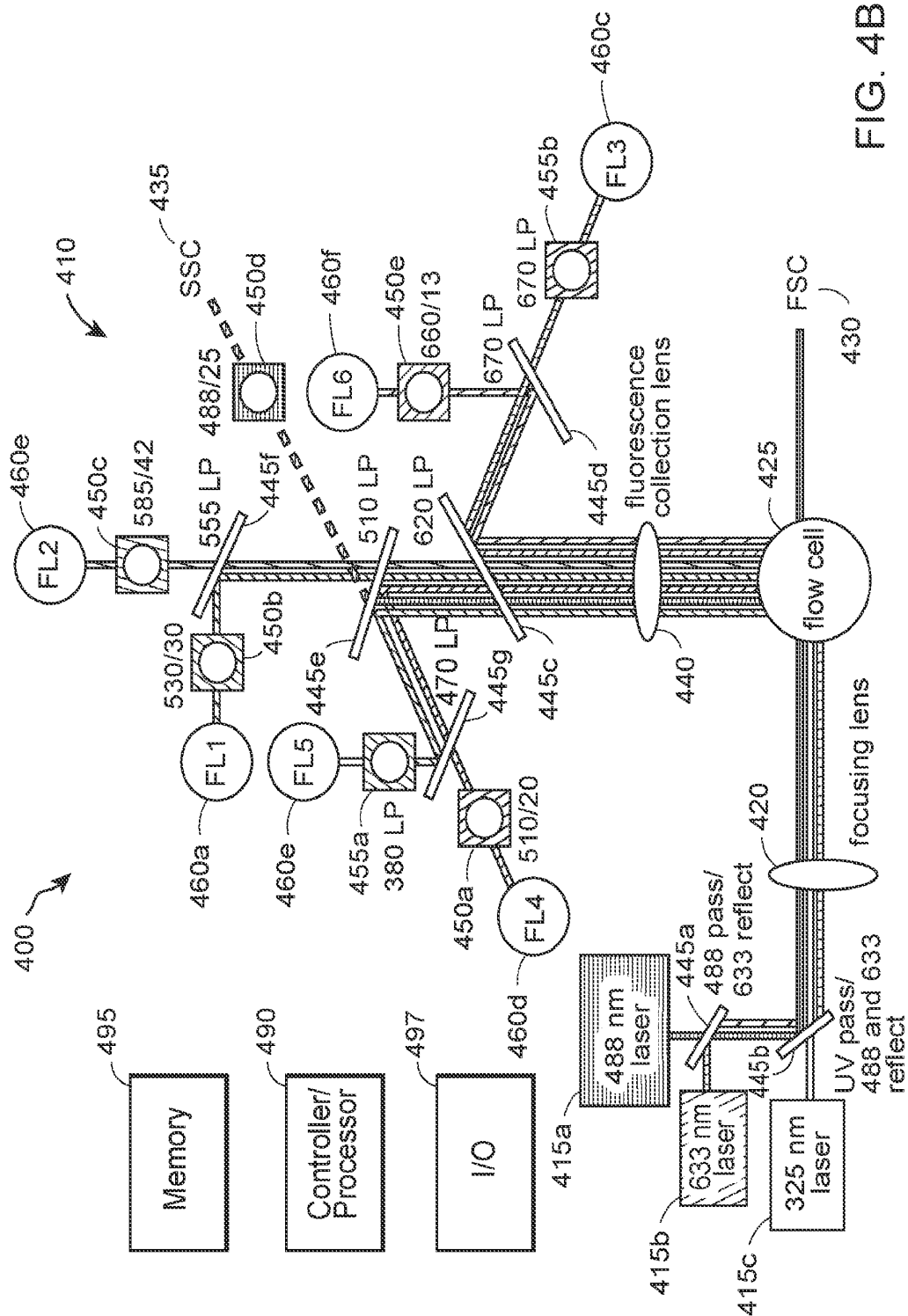
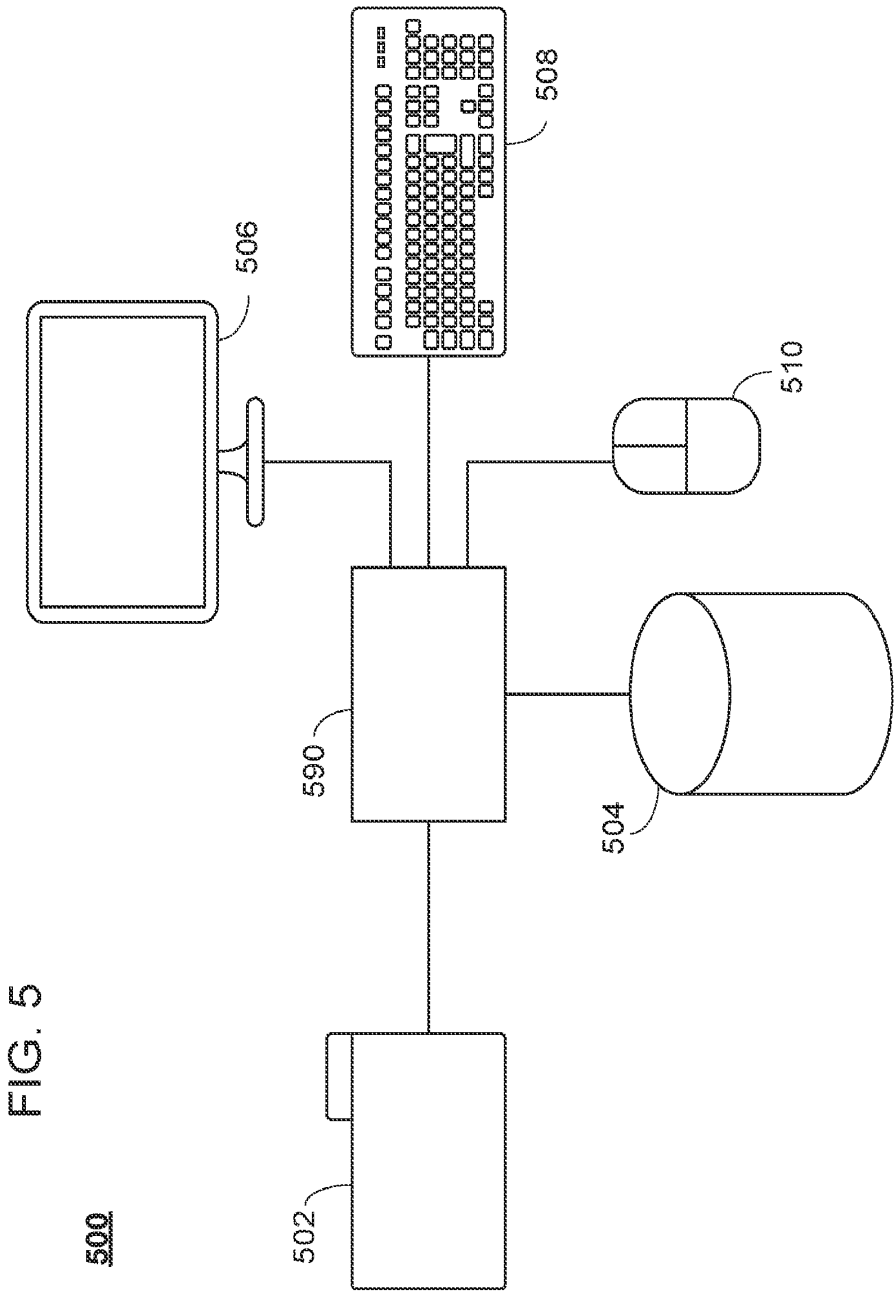


FIG. 4B

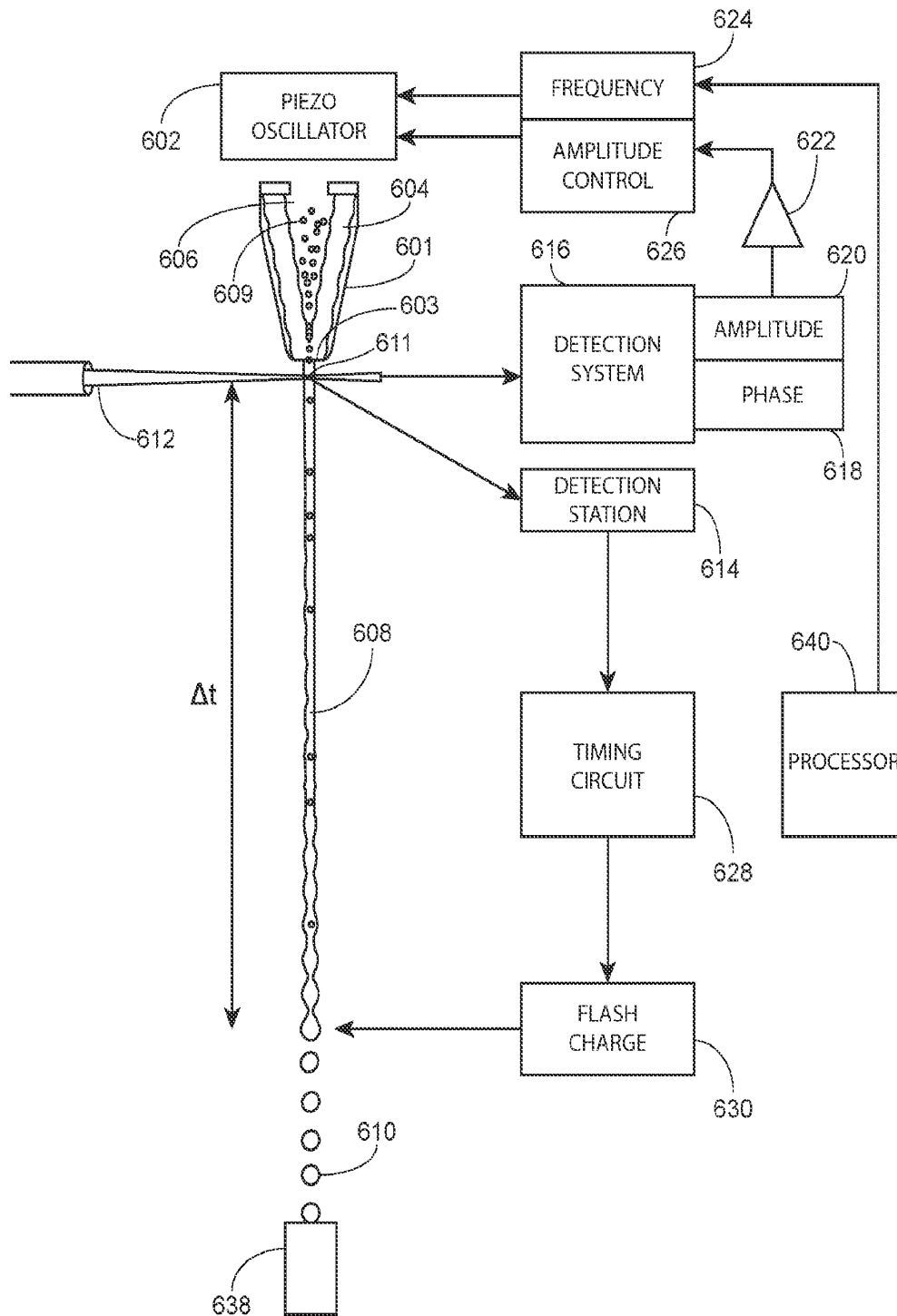
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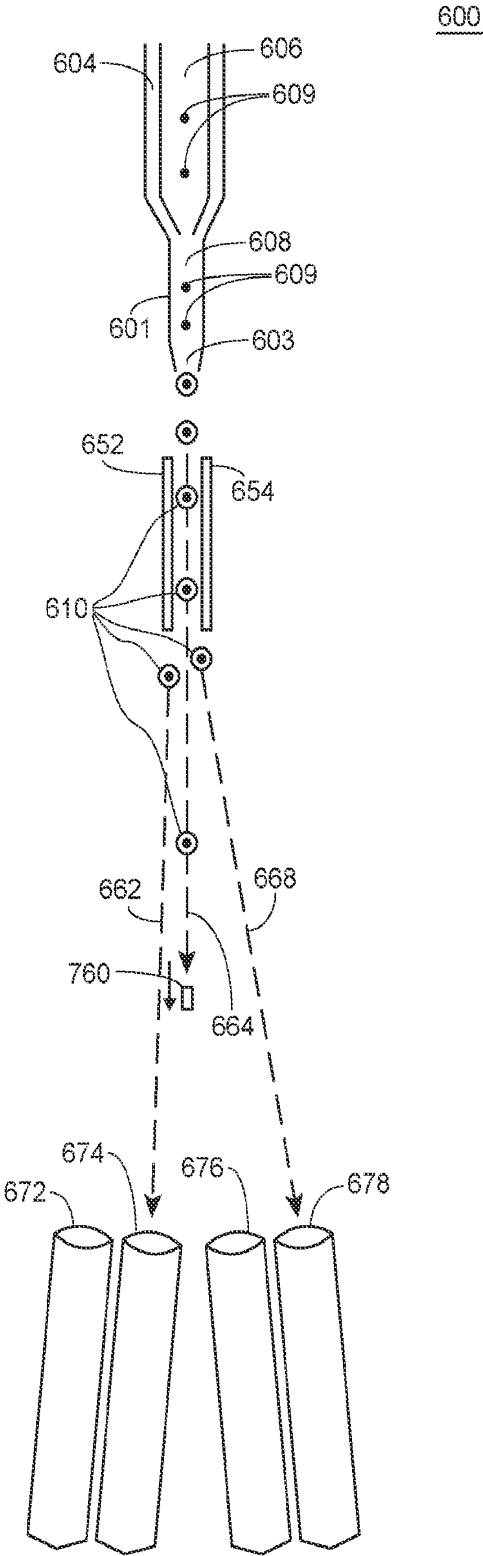
FIG. 6A

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FIG. 6B



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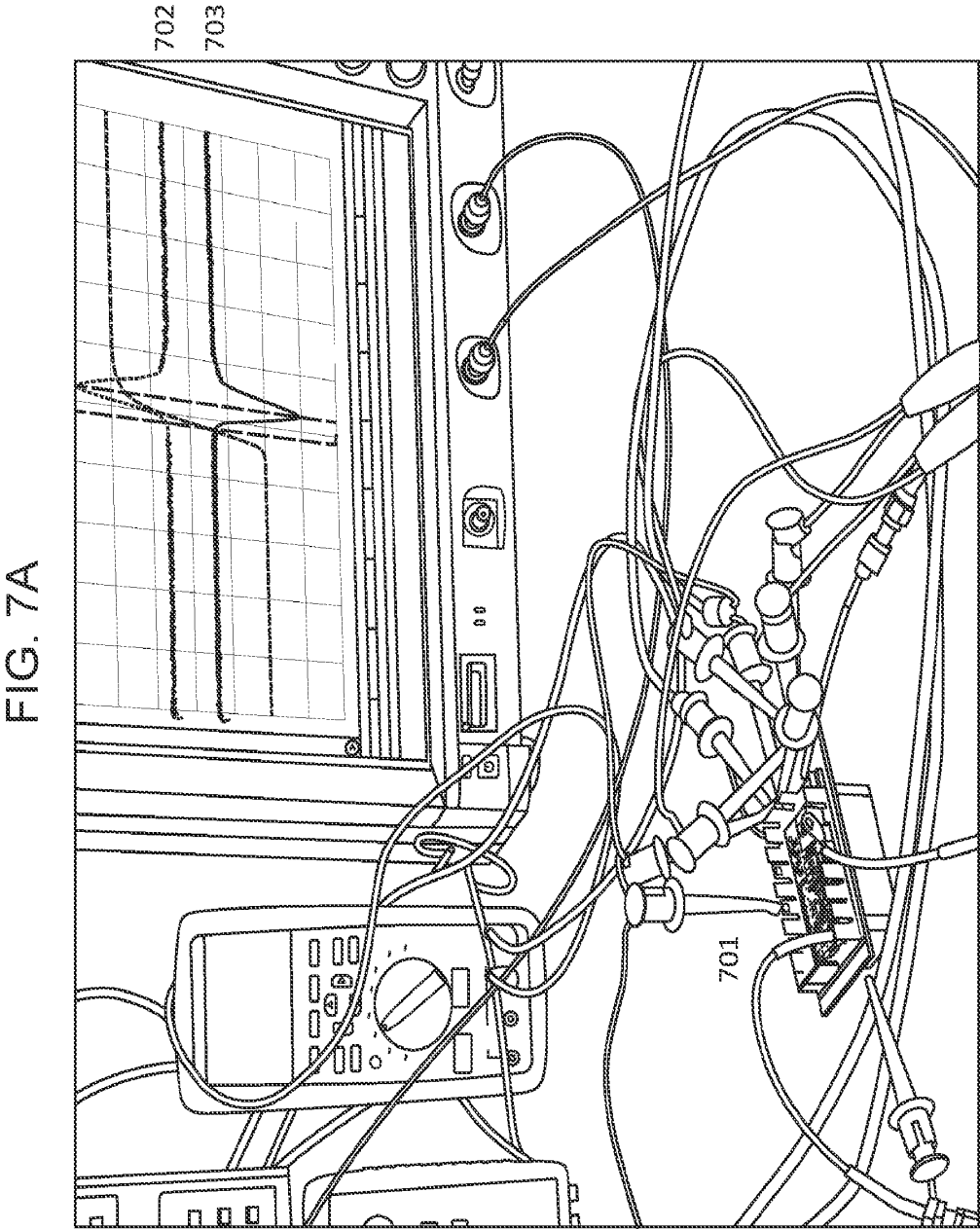
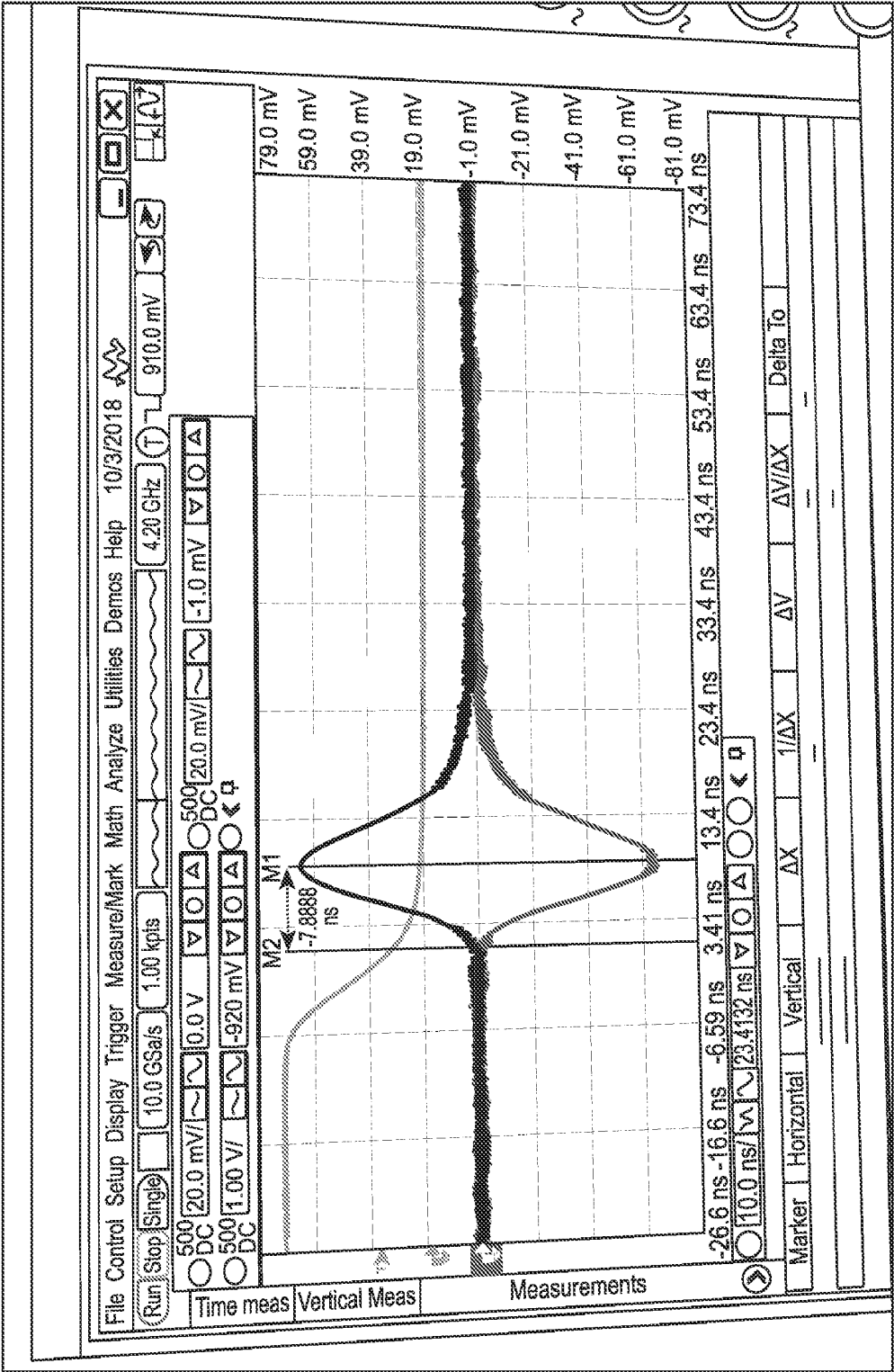
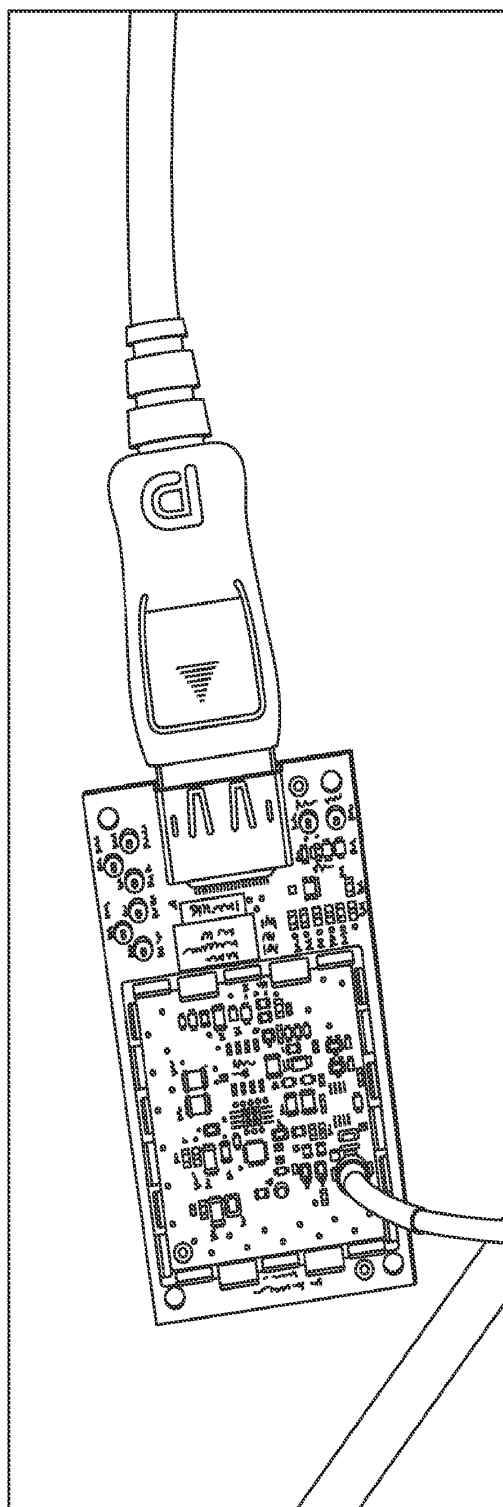


FIG. 7B



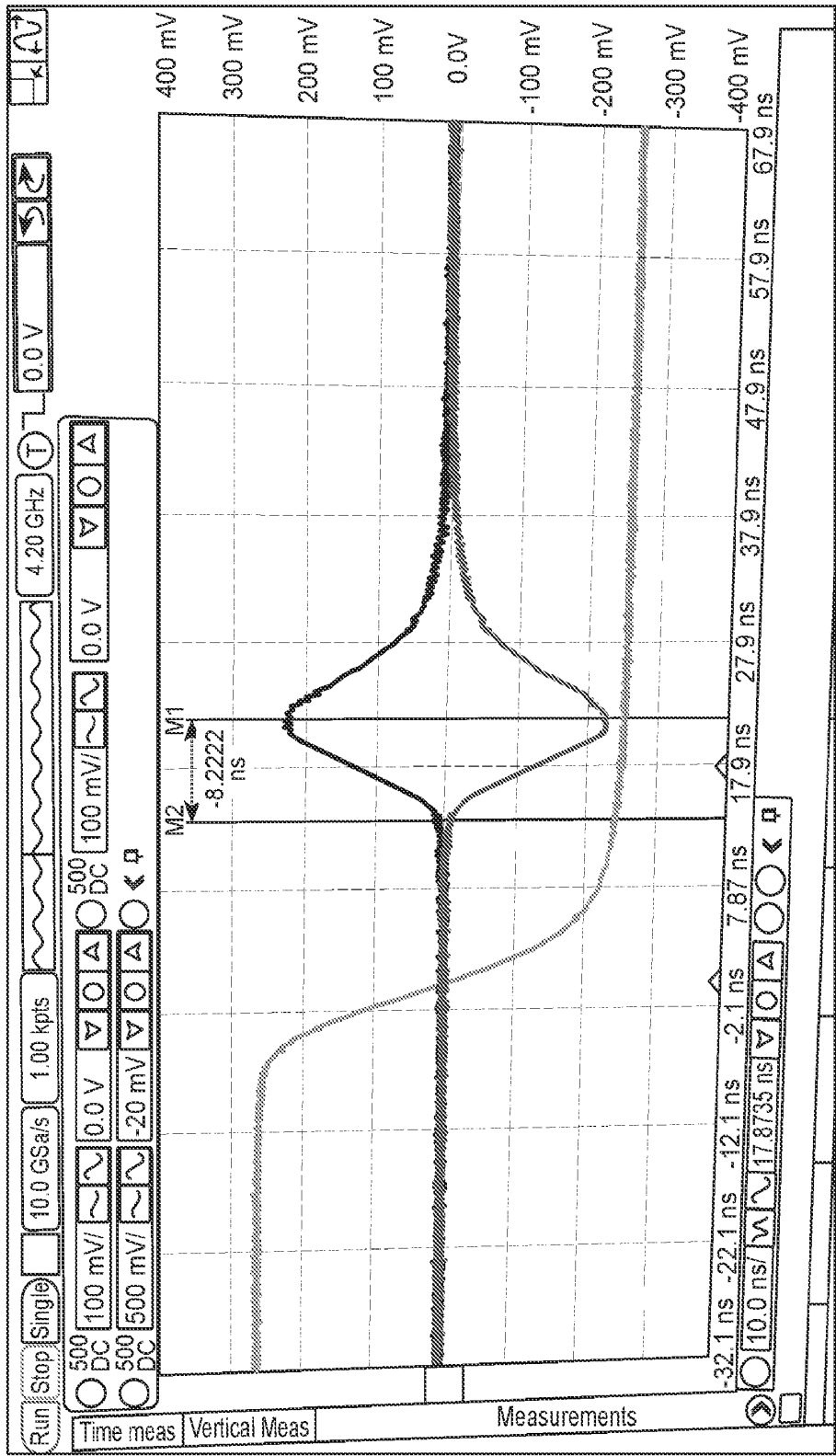
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FIG. 8A



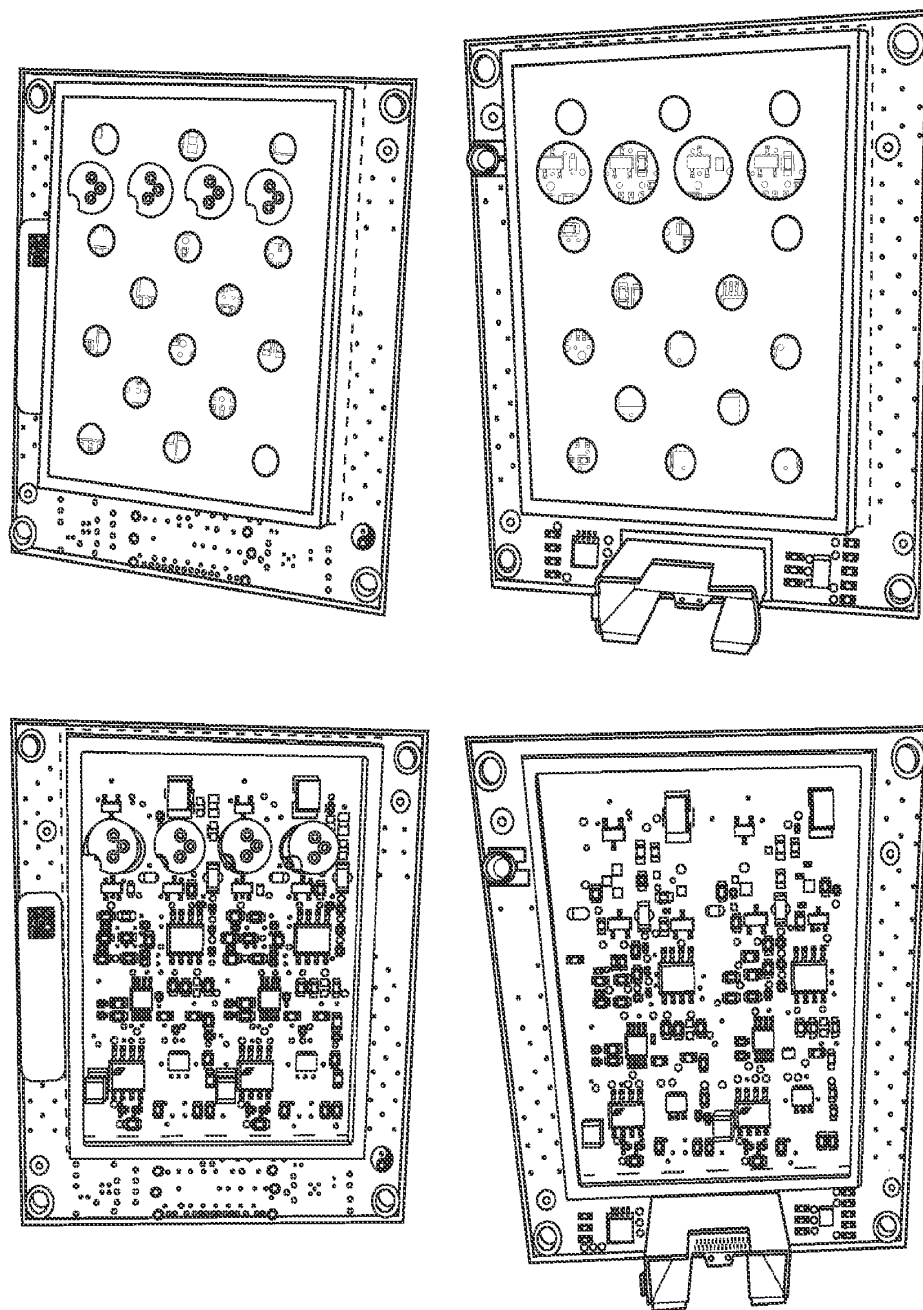
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FIG. 8B



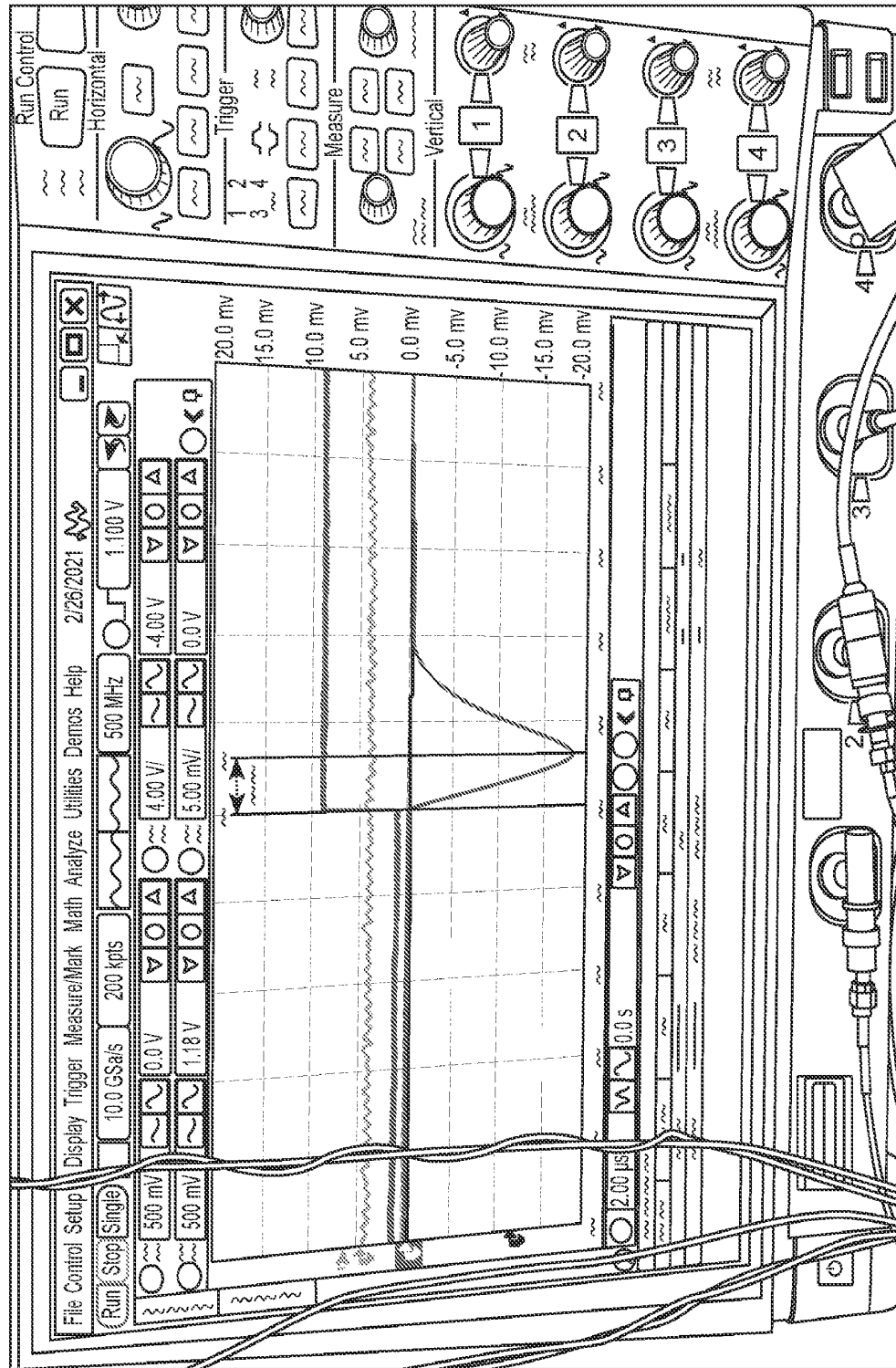
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FIG. 9A



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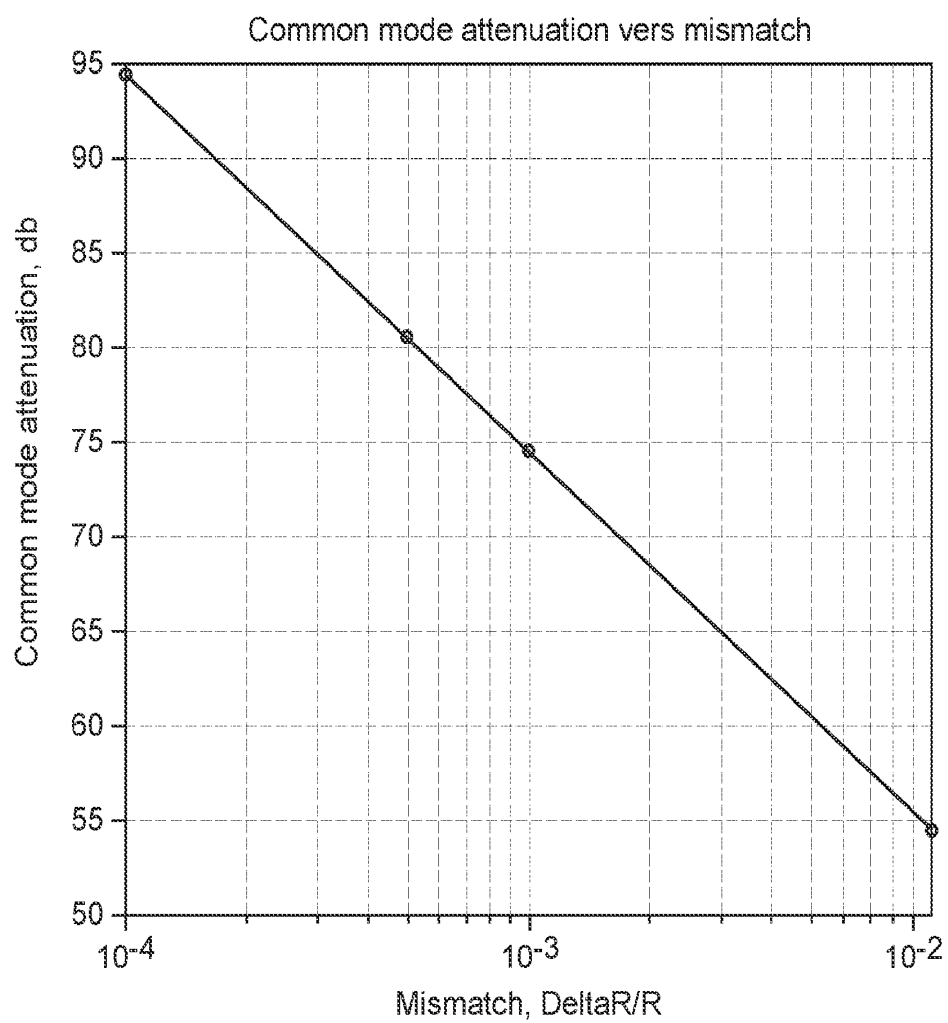
FIG. 9B



SUBSTITUTE SHEET (RULE 26)

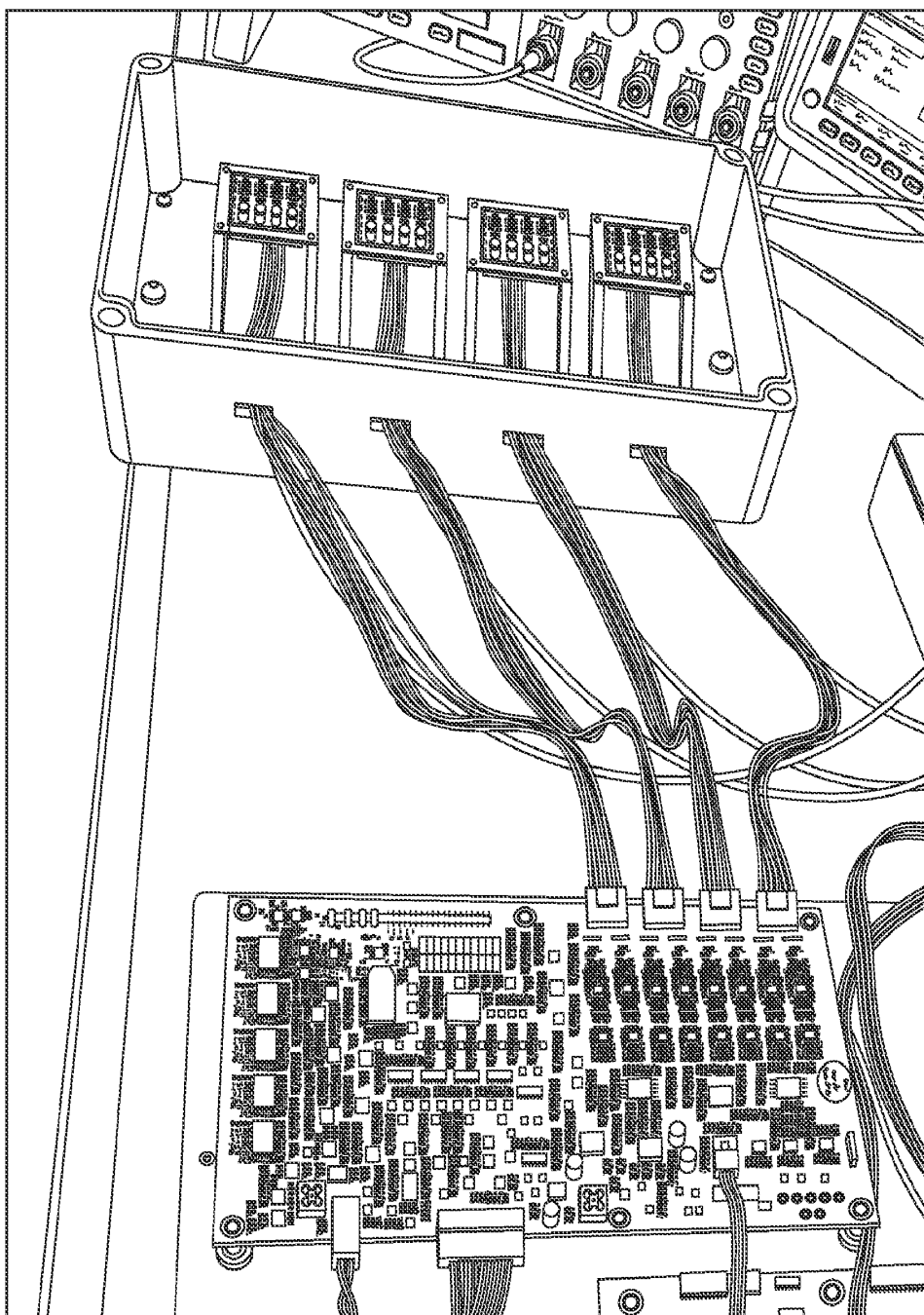
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FIG. 10



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FIG. 11



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FIG. 12

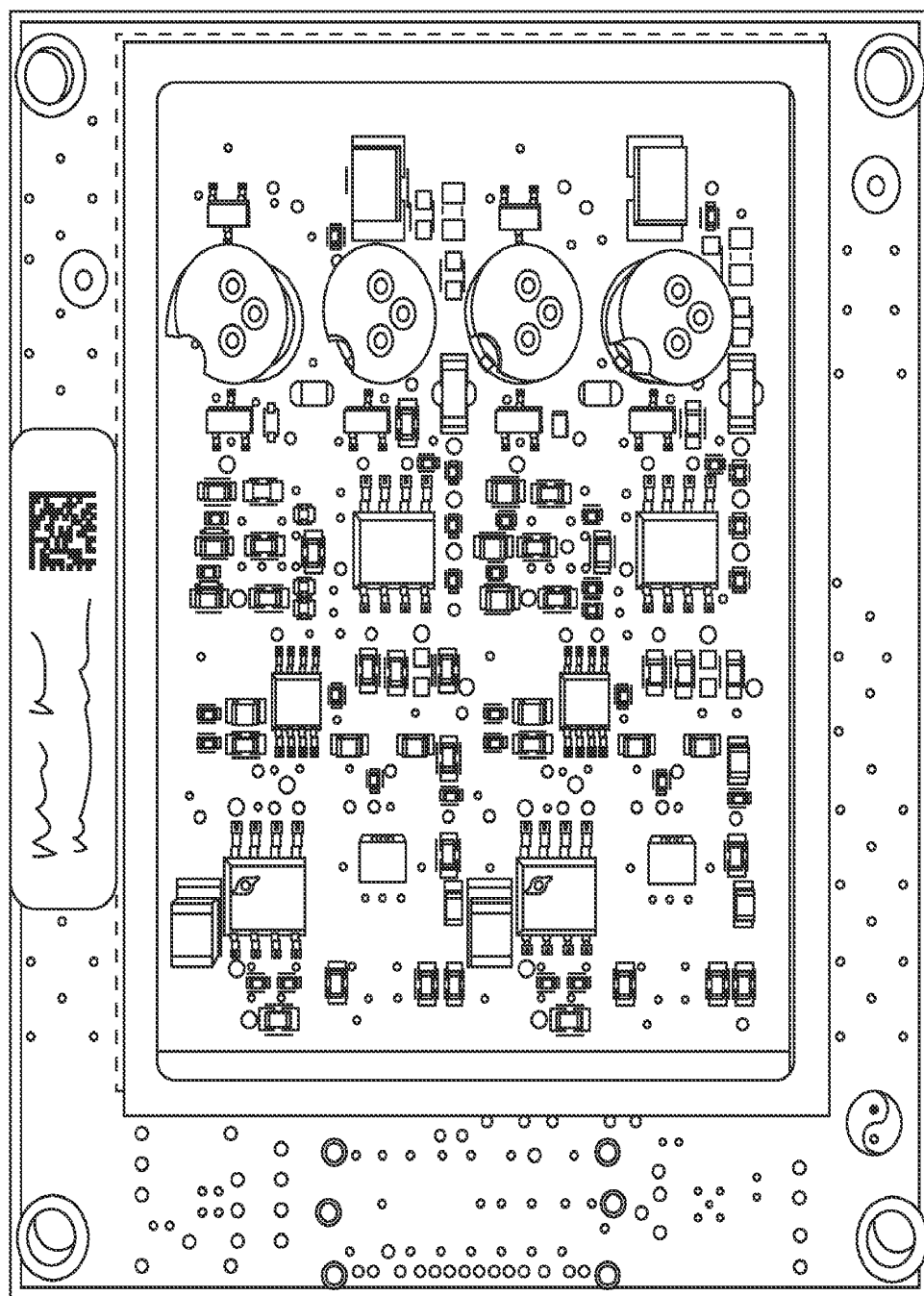
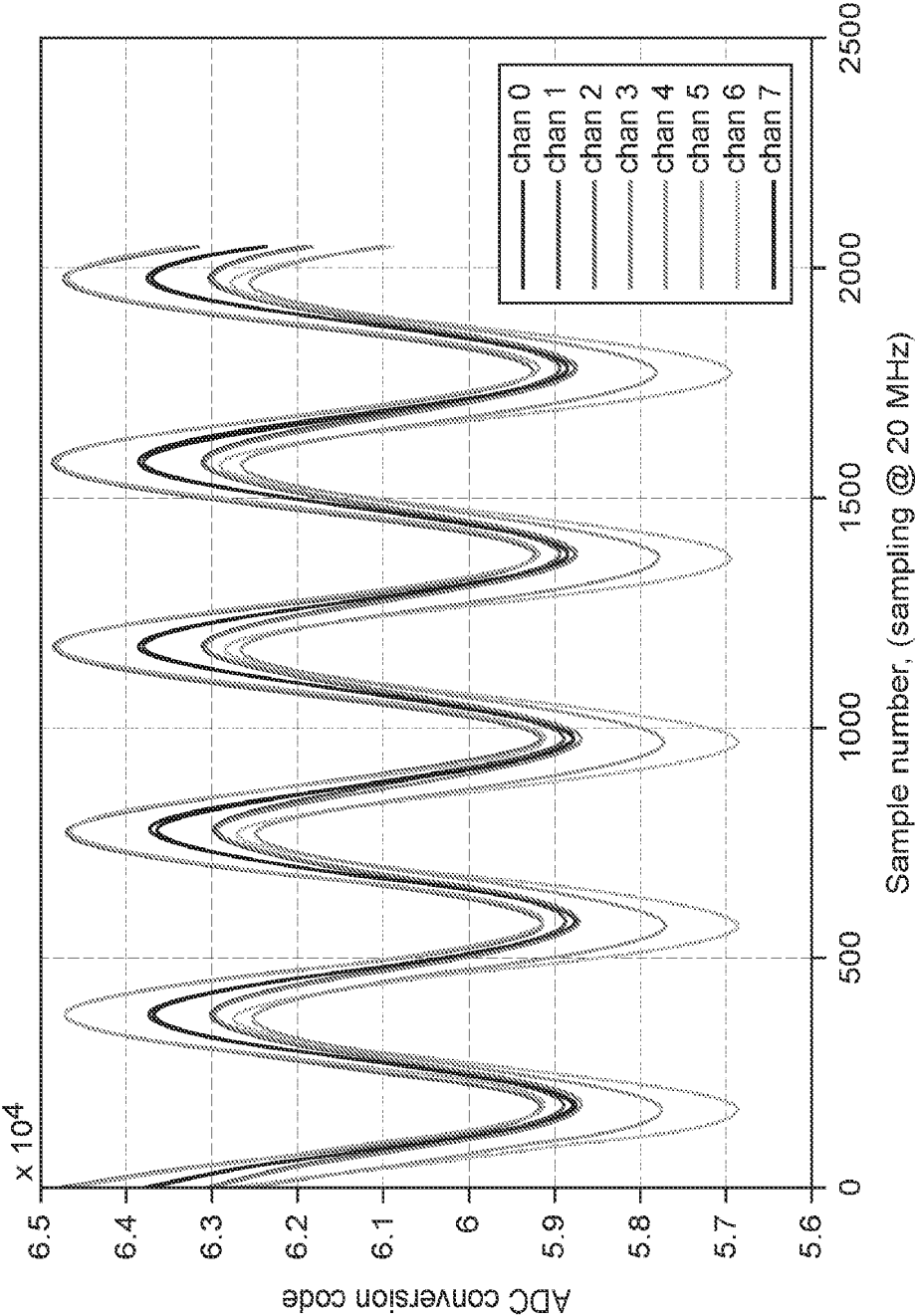


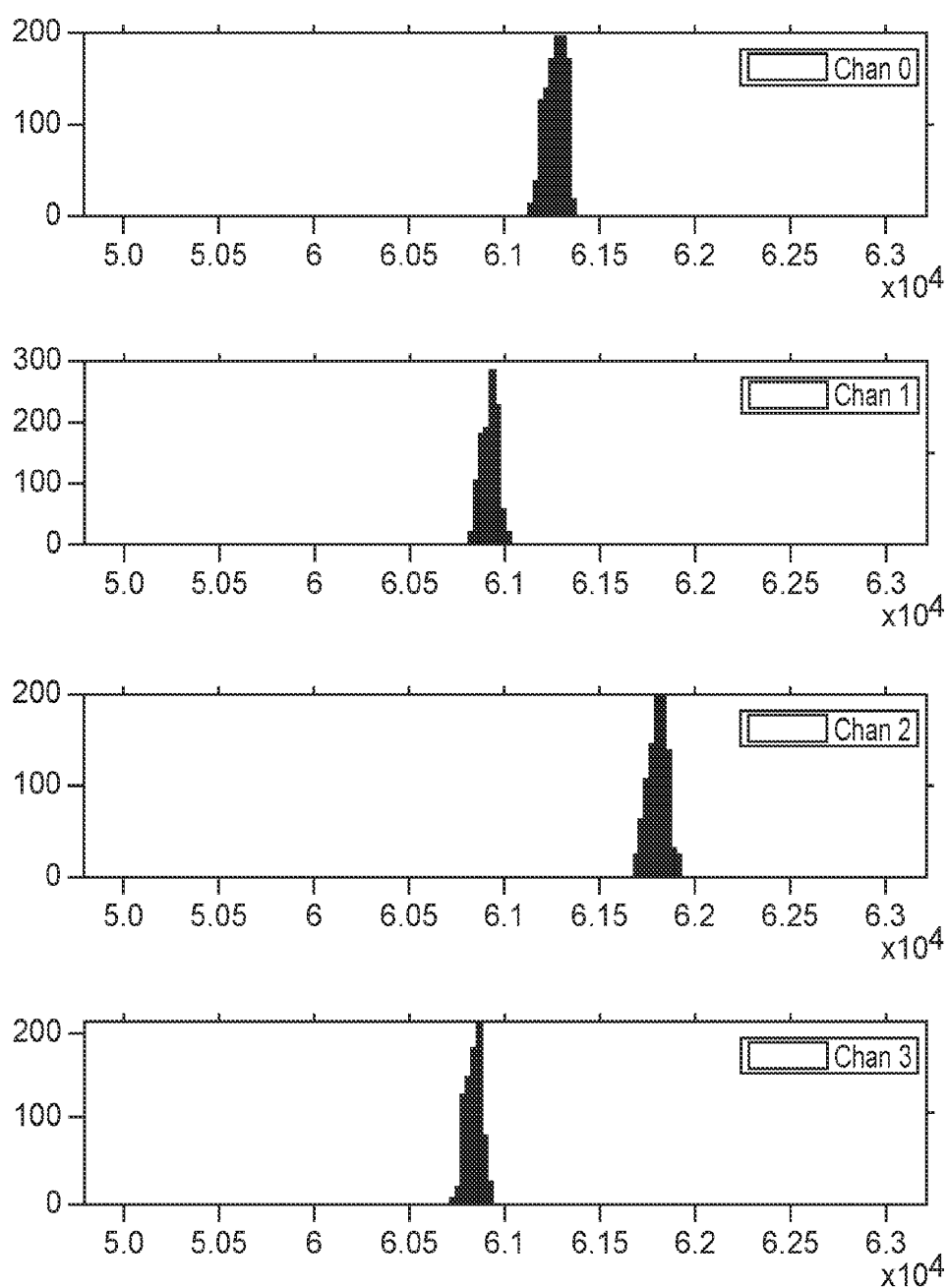
FIG. 13



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FIG. 14

Histograms of the pedestals, with the baseline offset



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FIG. 14 (Cont.)

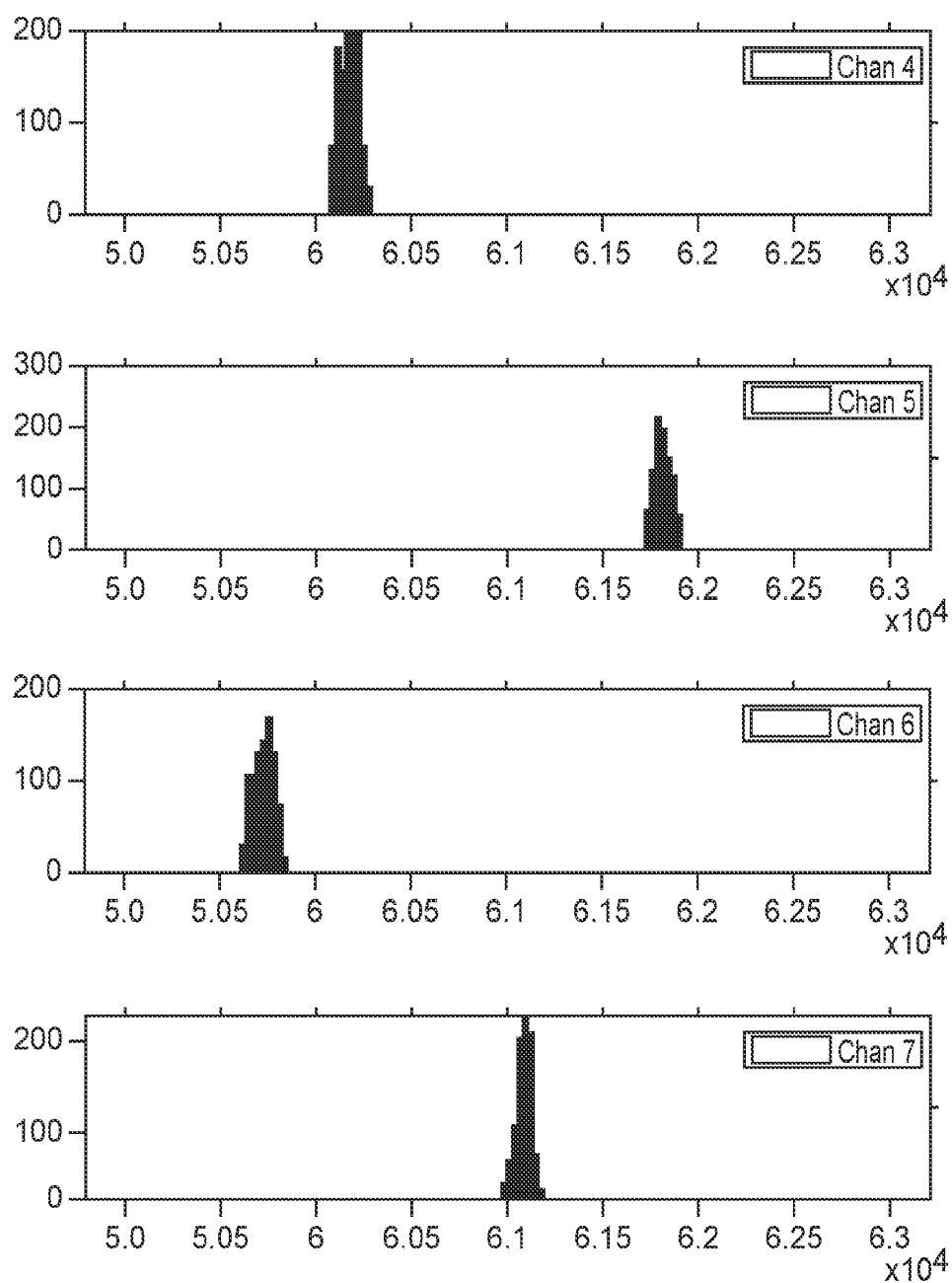


FIG. 15

Channel number, one ADC	Equivalent noise with HV OV pA RMS
1	18.9582
2	17.7941
3	18.0435
4	18.3762
5	18.4593
6	14.3850
7	17.6278
8	15.2996

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FIG. 16

