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ABSTRACT OF THE DISCLOSURE (FIG. 1)

"METHOD AND APPARATUS FOR
WRITE CONTROL IN A DISK DRIVE"

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There is disclosed a disk drive having a write controller which effectively inhibits a PTP phenomenon at a write operation time. The write controller is a CPU which changes write conditions for alleviating the PTP phenomenon on production of a write error at the write operation time. The CPU changes a write current value set to a register of a head amplifier circuit to a low current value, and executes a write operation. The CPU changes the write current value in stages until the write error is inhibited.

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