CLAIMS

1. A method, comprising: receiving first signaling from a host at a memory device that indicates through a configuration register of the memory device that the host will keep power active at the memory device to support a present operating condition of the memory device; identifying, based at least in part on second signaling from the host, an indication that power to the memory device is being turned off; determining an amount of time for which the memory device can continue to operate in the present operating condition, wherein the amount of time is an unlimited amount of time, a first amount of time that is less than the unlimited amount of time, a second amount of time that is less than the first amount of time, or a third amount of time that is less than the second amount of time; and operating the memory device in the present operating condition for the amount of time.

2. The method of claim 1, further comprising: transmitting, from a controller of the memory device, third signaling that is based at least in part on the indication; and operating the memory device in a different operating condition after the amount of time.

3. The method of claim 2, wherein operating the memory device in the different operating condition comprises aborting operation of the memory device based at least in part on the indication that power to the memory device is being turned off.

4. The method of claim 2, wherein operating the memory device in the different operating condition comprises freezing operation of the memory device based at least in part on the indication that power to the memory device is being turned off.

5. The method of claim 1, wherein the power to the memory device depends on a distance between the host and the memory device.

6. An apparatus, comprising: a memory; a configuration register configured to receive first signaling and indicate that a host will keep power active at the apparatus to support a present operating condition of the apparatus; and a controller configured to: identify, based at least in part on second signaling from the host, an indication that power to the apparatus is being turned off; determine an amount of time for which the apparatus can continue to operate in the present operating condition, wherein the amount of time is an unlimited amount of time, a first amount of time that is less than the unlimited amount of time, a second amount of time that is less than the first amount of time, or a third amount of time that is less than the second amount of time; and operate the apparatus in the present operating condition for the amount of time.

7. The apparatus of claim 6, wherein the controller is further configured to operate the apparatus in a different operating condition after operating the apparatus in the present

operating condition for the amount of time.

8. The apparatus of claim 6, wherein: the apparatus includes an interface configured to receive the first signaling from the host; and the controller is configured to receive the first signaling from the host through the interface.

9. The apparatus of claim 6, wherein the apparatus is configured to reduce its current consumption to zero after the amount of time.

10. The apparatus of claim 6, wherein the apparatus is a memory device.

11. A method, comprising: transmitting first signaling to a memory device through a configuration register of the memory device that a host will keep power active at the memory device to support a present operating condition of the memory device; determining an amount of time for which the memory device can continue to operate in a present operating condition, wherein the amount of time is an unlimited amount of time, a first amount of time that is less than the unlimited amount of time, a second amount of time that is less than the first amount of time, or a third amount of time that is less than the second amount of time; transmitting second signaling from the host to the memory device to provide an indication of the amount of time for which the memory device can continue to operate in the present operating condition; and providing power to the memory device based at least in part on the indication of the amount of time for which the memory device can continue to operate in the present operating condition.

12. The method of claim 11, wherein a current consumption of the memory device at a time when the amount of time is determined is at a higher level than a maximum current level that can be provided to the memory device.

13. The method of claim 12, wherein the second signaling includes signaling to increase the maximum current level that can be provided to the memory device.

14. The method of claim 11, further comprising moving the memory device closer to the host based at least in part on the indication of the amount of time for which the memory device can continue to operate in the present operating condition.

15. The method of claim 11, wherein the method includes decreasing the power provided to the memory device to zero after the amount of time.

16. An apparatus, comprising: a processor configured to determine an amount of time for which a memory device can continue to operate in a present operating condition, wherein the amount of time is an unlimited amount of time, a first amount of time that is less than the

unlimited amount of time, a second amount of time that is less than the first amount of time, or a third amount of time that is less than the second amount of time; and an interface configured to: transmit first signaling to a memory device through a configuration register of the memory device that the apparatus will keep power active at the memory device to support a present operating condition of the memory device; transmit second signaling from the apparatus to the memory device to provide an indication of the amount of time for which the memory device can continue to operate in the present operating condition; and provide power to the memory device based at least in part on the indication of the amount of time for which the memory device can continue to operate in the present operating condition.

17. The apparatus of claim 16, wherein the power provided to the memory device depends on a distance between the memory device and the apparatus.

18. The apparatus of claim 16, wherein a voltage supply being provided to the memory device reaches a threshold level at a time when the indication is determined.

19. The apparatus of claim 16, wherein the interface is further configured to transmit third signaling from the apparatus to the controller for the memory device to continue to operate in the present operating condition after the amount of time.

20. The apparatus of claim 16, wherein the apparatus is a host.