

MULTI-LEVEL COMPARATOR FOR DEMODULATOR

ABSTRACT OF THE DISCLOSURE

In a demodulator circuit including a multi-level comparator, the demodulator circuit obtaining output data through comparison, made in a four-level comparator and a NRZ comparator, of a signal obtained by subjecting a received signal to a process of detection and predetermined threshold levels, comprises a level detector circuit for detecting the level of the received signal transmitted through the process of detection and a control circuit for controlling relative magnitudes of the level detected in the level detector circuit of the signal transmitted through the process of detection and the predetermined threshold levels for the four-level comparator and the NRZ comparator, whereby stabilized output data conforming to changes in the level of the detected output are made obtainable.

Figure 3