

POWER CONVERTER CONTROL DEVICE

ABSTRACT

5 A direct power conversion device is controlled by a microcomputer without
using a logic IC. Interrupt handling by an interrupt command (R1[k]) starts, and an
interrupt command (R2) occurring concurrently with the interrupt command (R1[k]) is
released. Thereafter, second data (Data2[k]) is written into a buffer register (BuffReg).
This is performed after transfer of first data (Data1[k]) from the buffer register (BuffReg)
10 to a compare register (CompReg), the transfer being performed at the same timing as
occurrence of the interrupt command (R1[k]). Pieces of timing, at which the first data
(Data1[k+1]) and the second data (Data2[k+1]) are written into the buffer register
(BuffReg), are shifted from each other, whereby pieces of timing, at which these are
transferred from the buffer register (BuffReg) to the compare register (CompReg), can be
15 shifted from each other.

[FIG. 7]