INTERCONNECTED PROCESSING NODES CONFIGURABLE AS AT LEAST ONE NON-UNIFORM MEMORY ACCESD (NUMA) DATA PROCESSING SYSYEM

ABSTRACT

A data processing system (6) is provided which includes a plurality of processing nodes (8a....d) that each contain at least one processor (10a....m) and data storage (18). The plurality of processing nodes (8a....d) are coupled together by a system interconnect (22). The data processing system (6) further includes a configuration utility residing in data storage (18) within at least one of the plurality of processing nodes (8a....d). The configuration utility selectively configures the plurality of processing nodes (8a....d) into either a single non-uniform memory access (NUMA) system or into multiple independent data processing systems through communication via the system interconnect (22).

Fig 1