

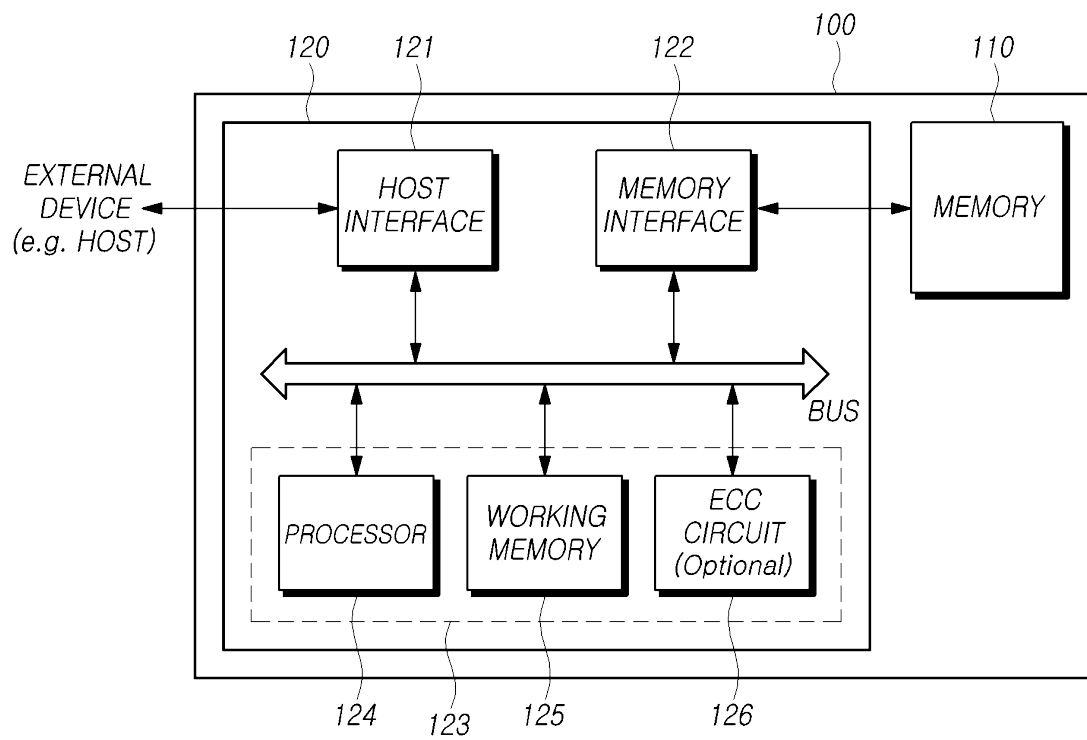
FIG. 1

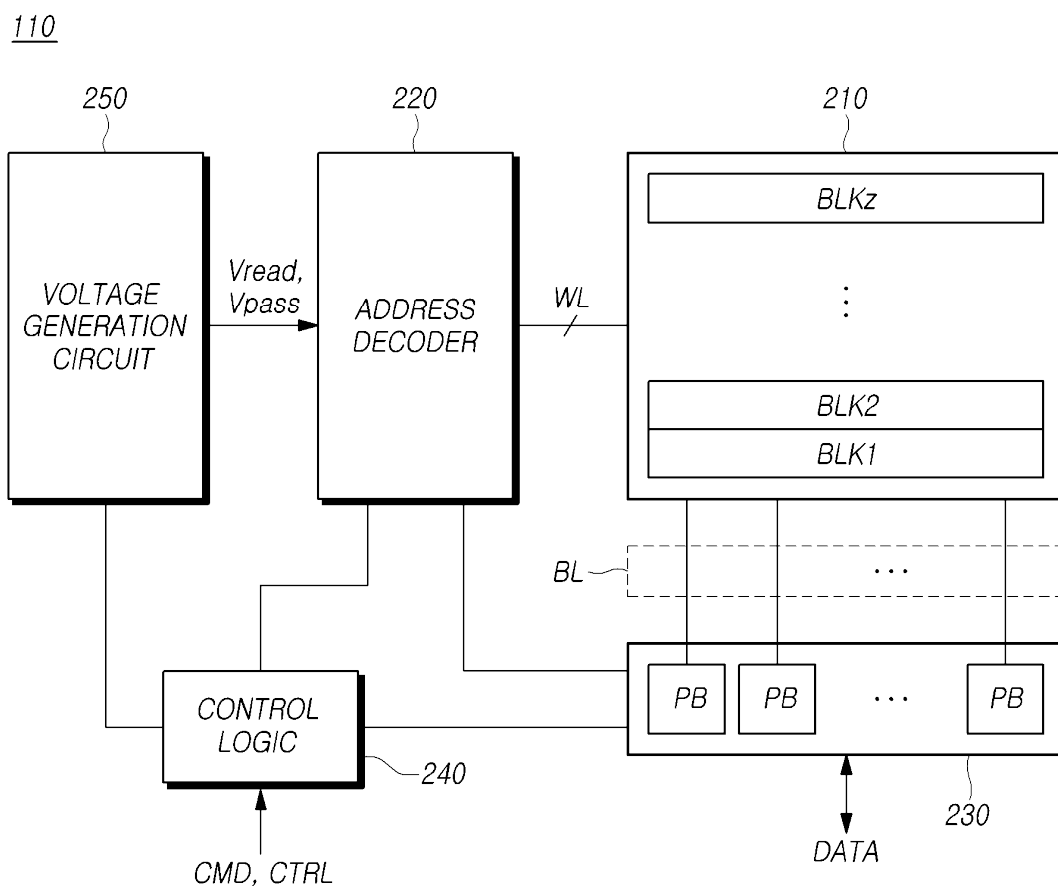
FIG. 2

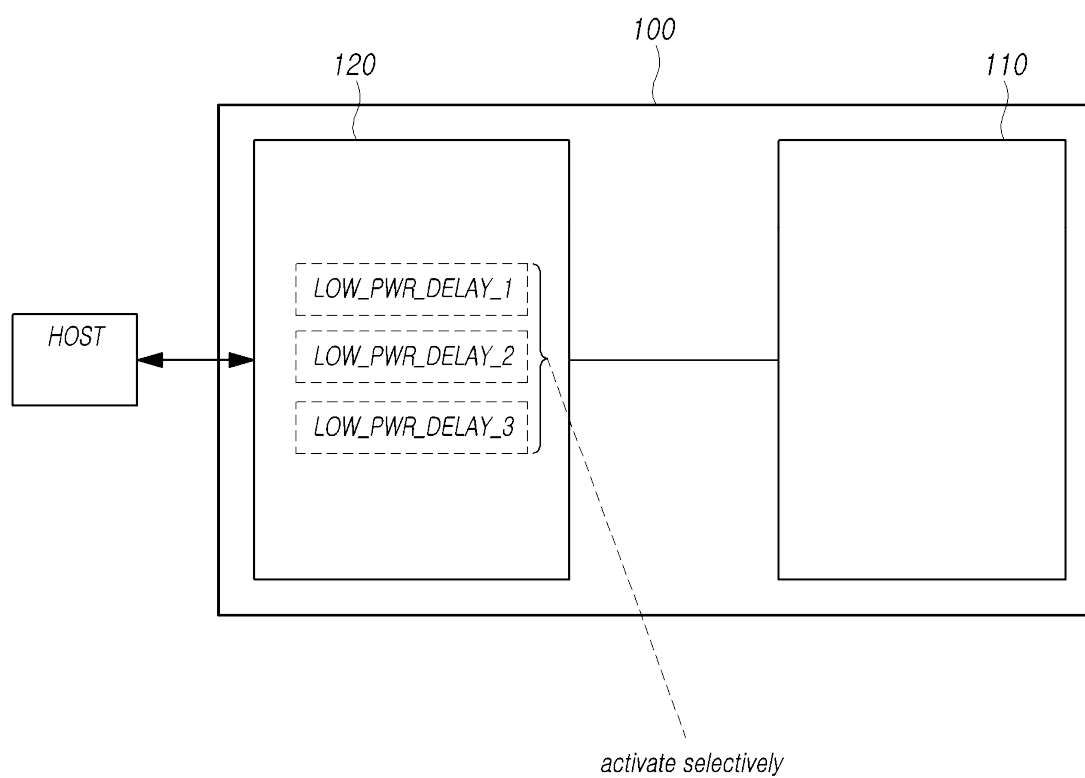
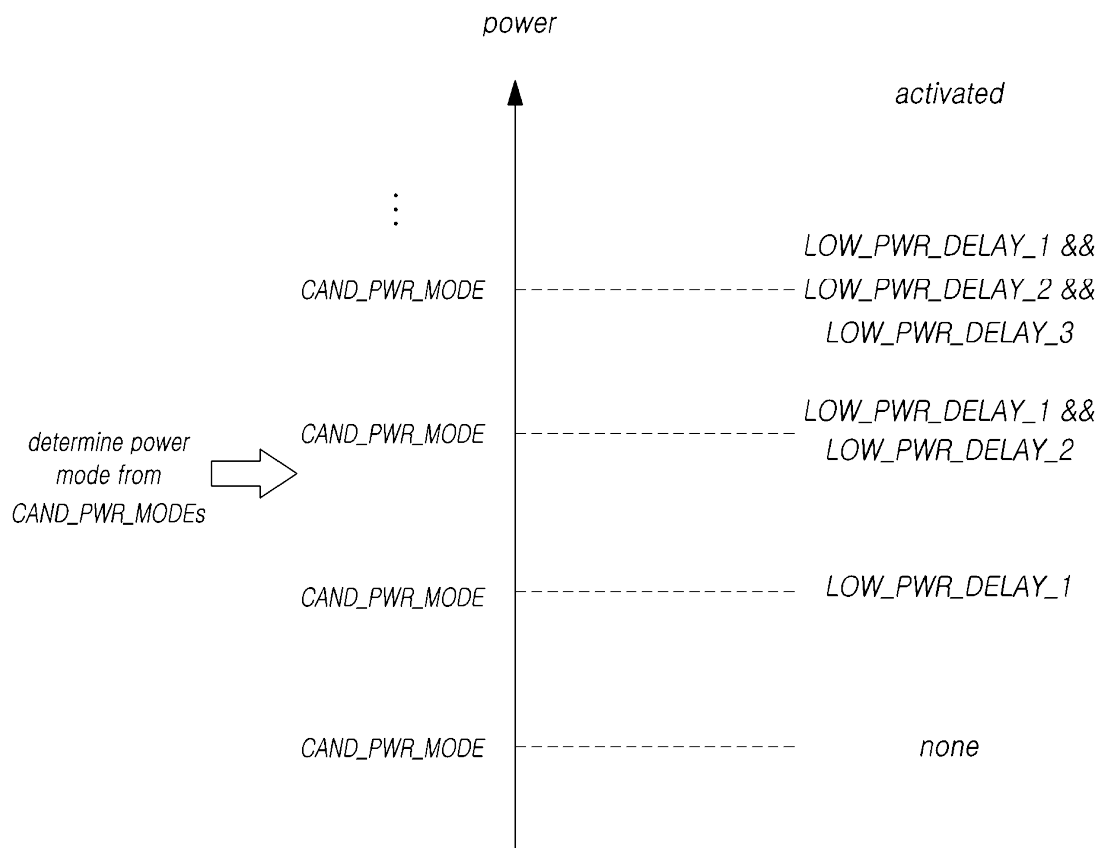
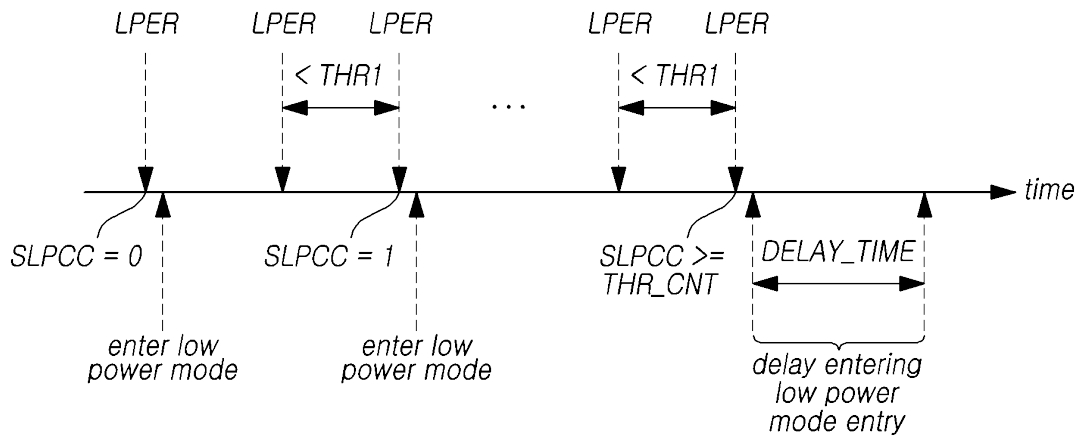
FIG. 3

FIG.4

*FIG.5*LOW PWR DELAY1

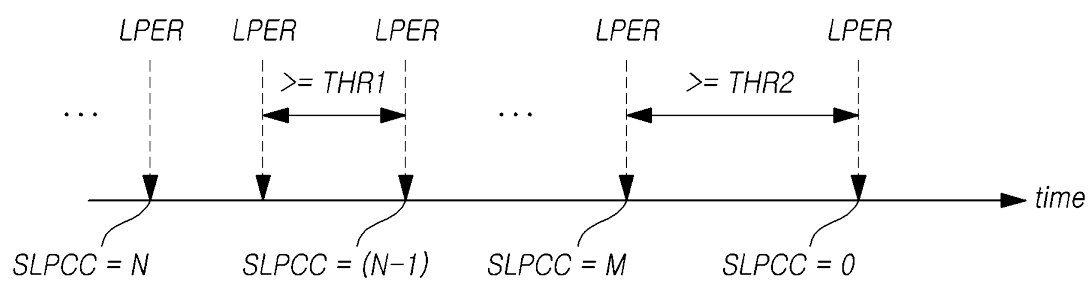
*FIG. 6*LOW PWR DELAY1

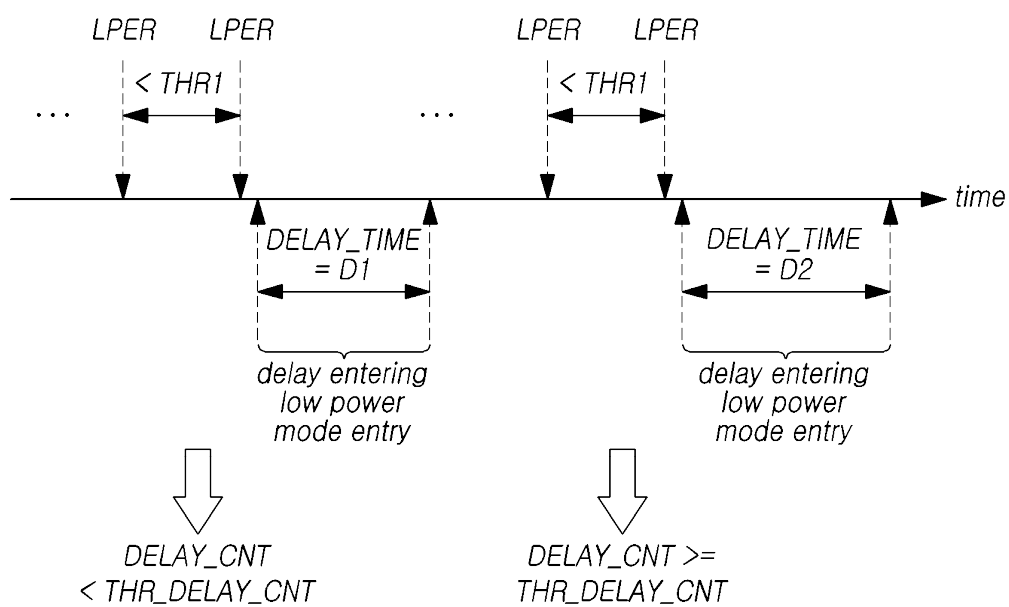
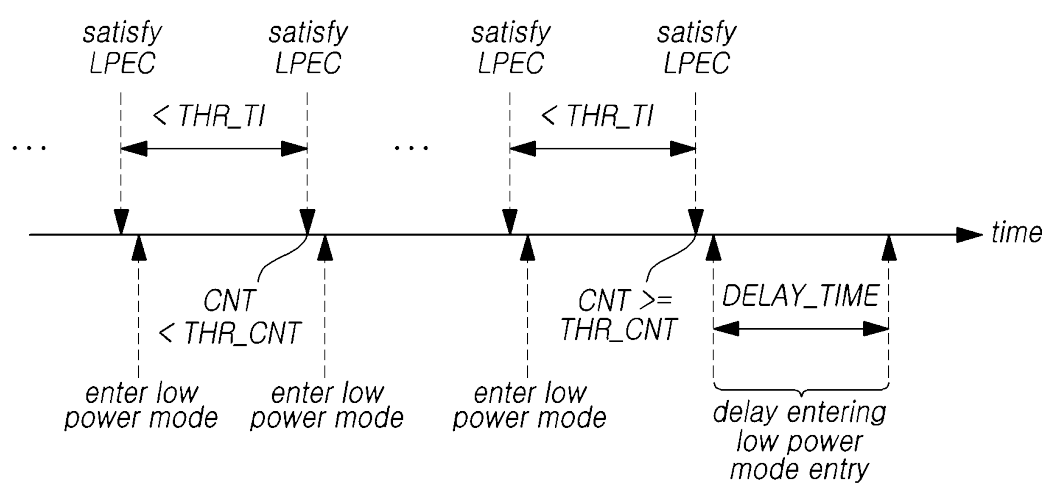
FIG. 7LOW PWR DELAY1

FIG. 8LOW PWR DELAY2

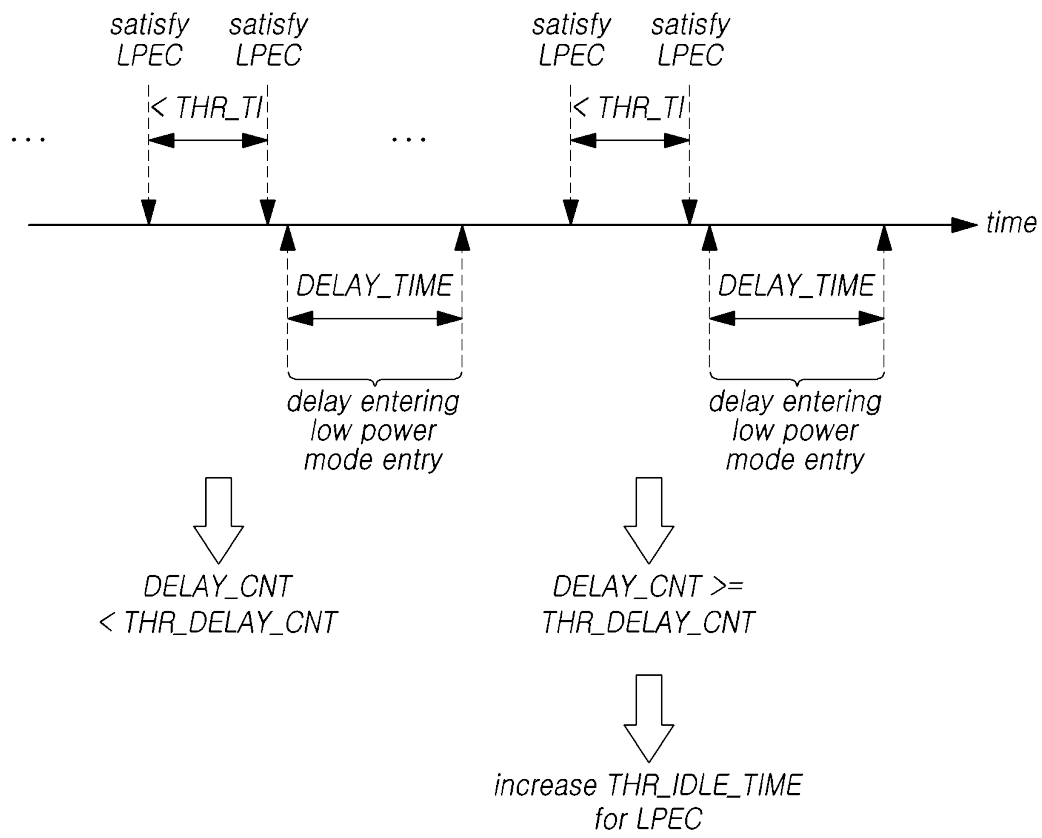
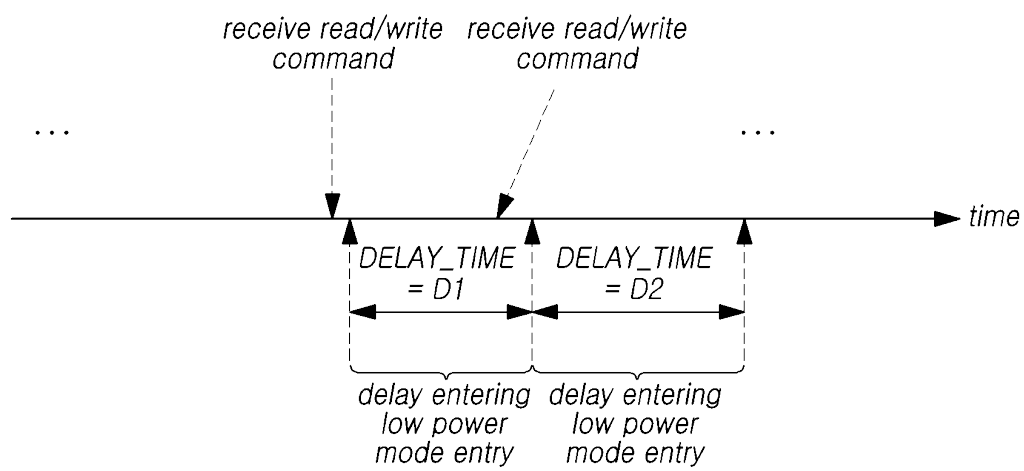
*FIG.9*LOW PWR DELAY2

FIG. 10

LOW PWR DELAY3



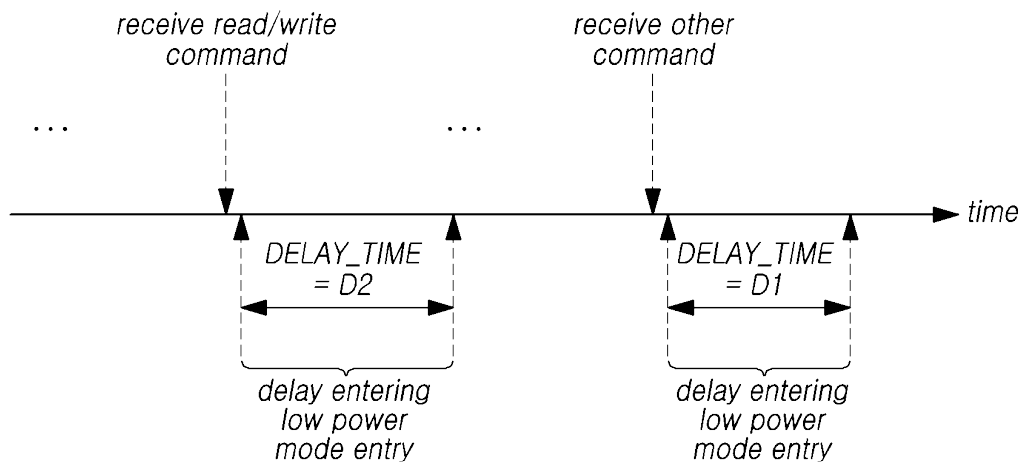
*FIG. 11*LOW PWR DELAY3

FIG. 12