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THOMAS & BETTS LEGAL DEPT.

No. 9241 P. 11/12

THIS SHEET HAS BEEN
REPLACED**IMPROVEMENTS IN OR RELATING TO
ELECTRICAL CONNECTION APPARATUS**

This Sheet Has Been Replaced

**ABSTRACT OF THE DISCLOSURE**

5 A pin grid array socket connector for connection to a
plurality of pins in a CPU chip has a plurality of
socket contacts, each socket contact having a pair of
contact arms. Each arm has a contact point adjacent a
free end thereof, the contact points of each contact
being staggered such that, in use, a particular pin of
10 the CPU chip does not make initial contact with both the
contact points of the associated contact simultaneously.
Adjacent contacts are positionally staggered with
respect to each other such that, in use, the associated
pair of CPU chip pins does not make initial contact
15 simultaneously with more than one contact point.