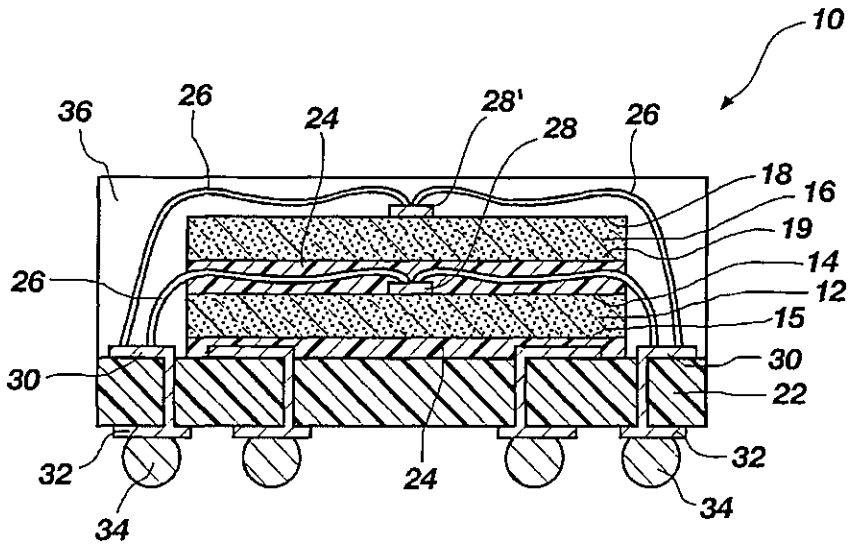


FIG. 1A
(PRIOR ART)

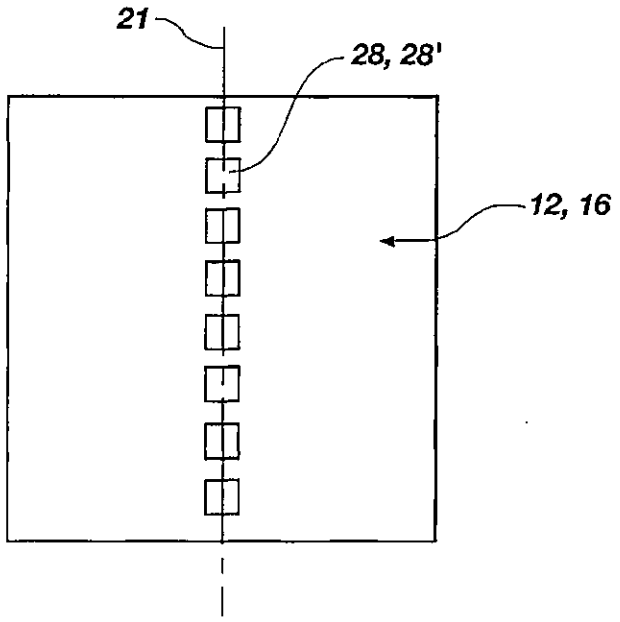


FIG. 1B
(PRIOR ART)

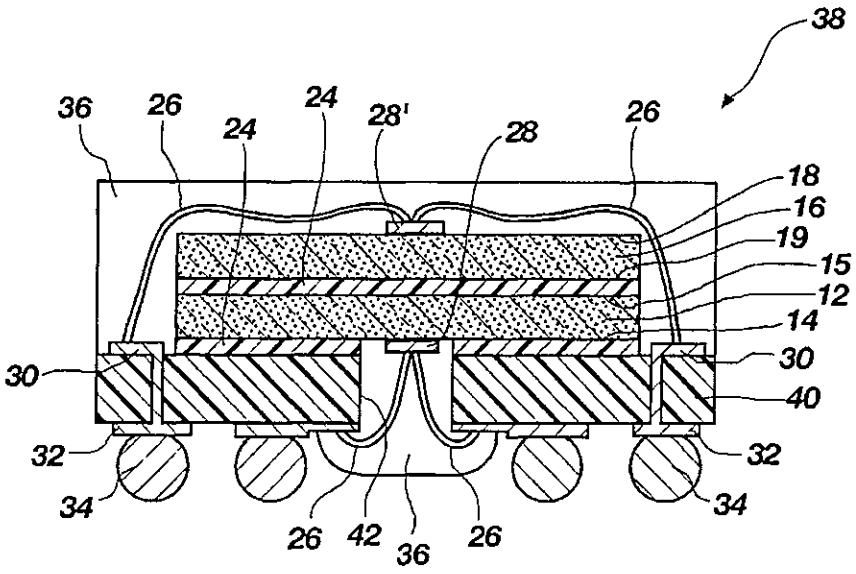


FIG. 2
(PRIOR ART)

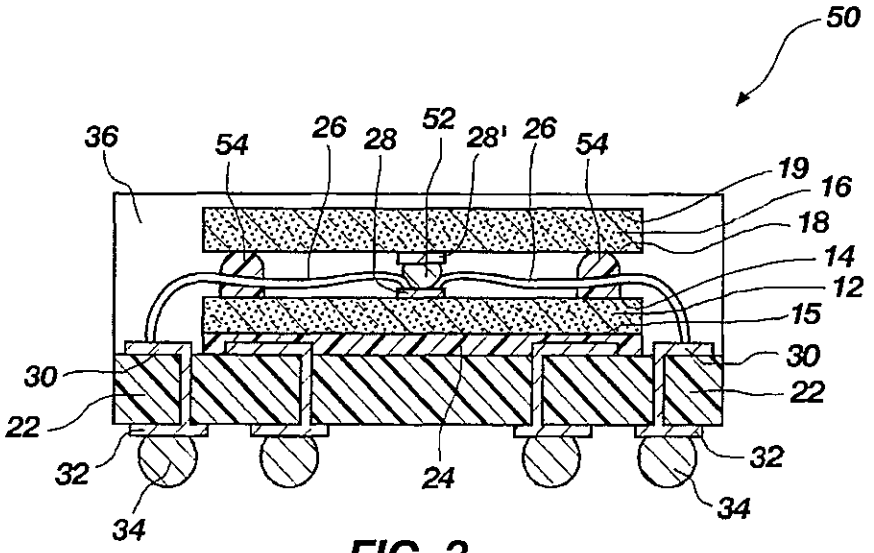


FIG. 3

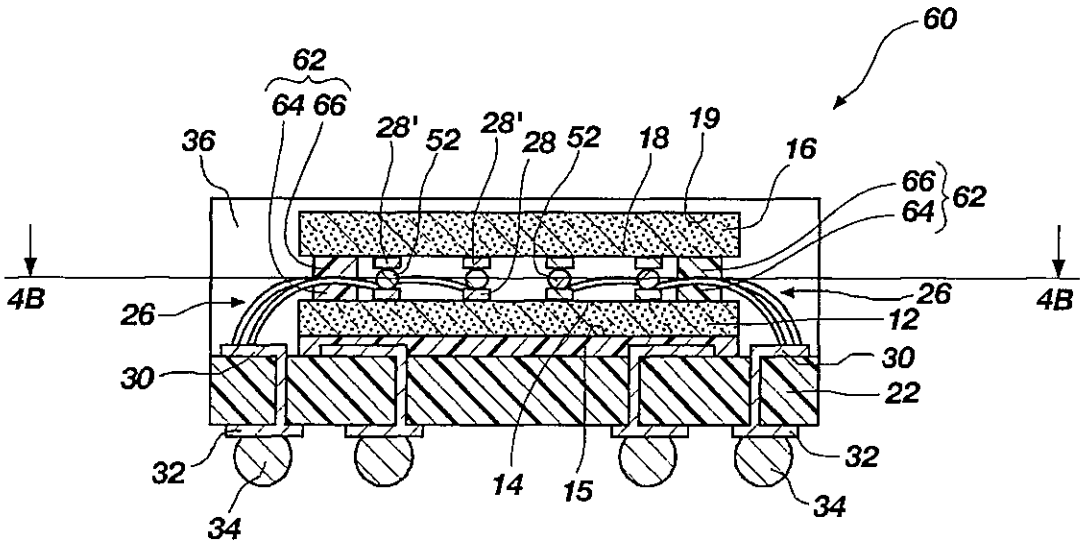

60
62
64 66
36
28'
52
28'
28
52
18
19
16
66
64
62
4B
4B
26
26
12
30
30
22
32
32
14
15
34
34

FIG. 4A

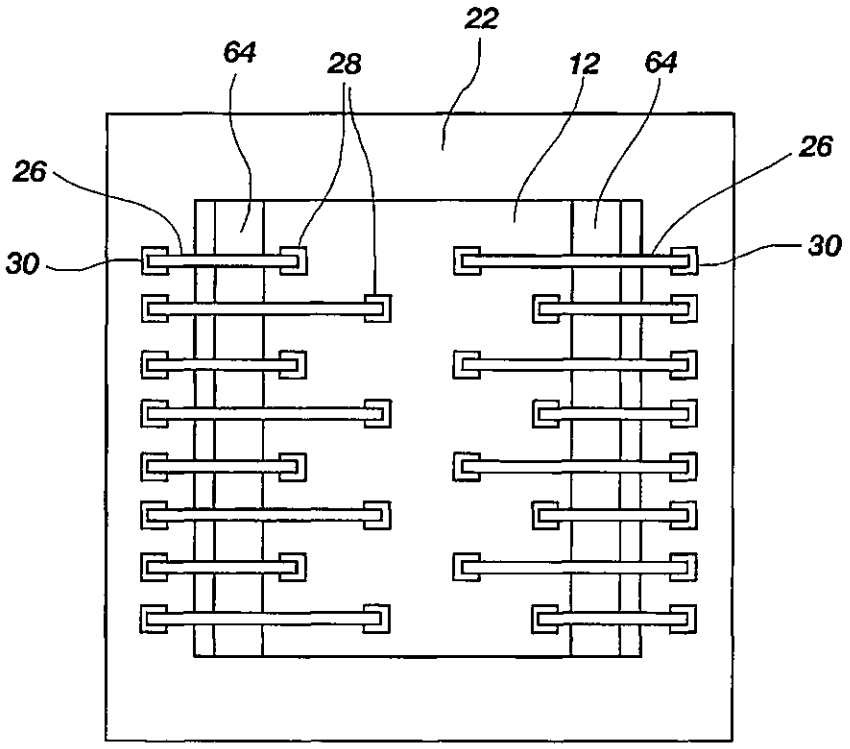

22
64
28
12
64
26
26
30
30

FIG. 4B

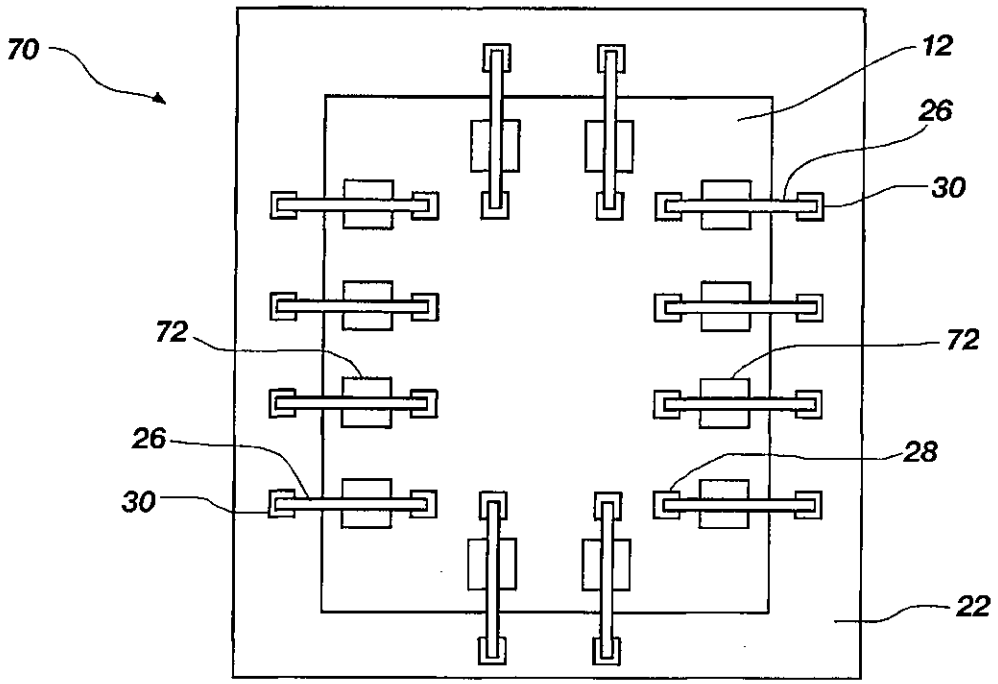

70
12
26
30
72
72
26
28
30
22

FIG. 5

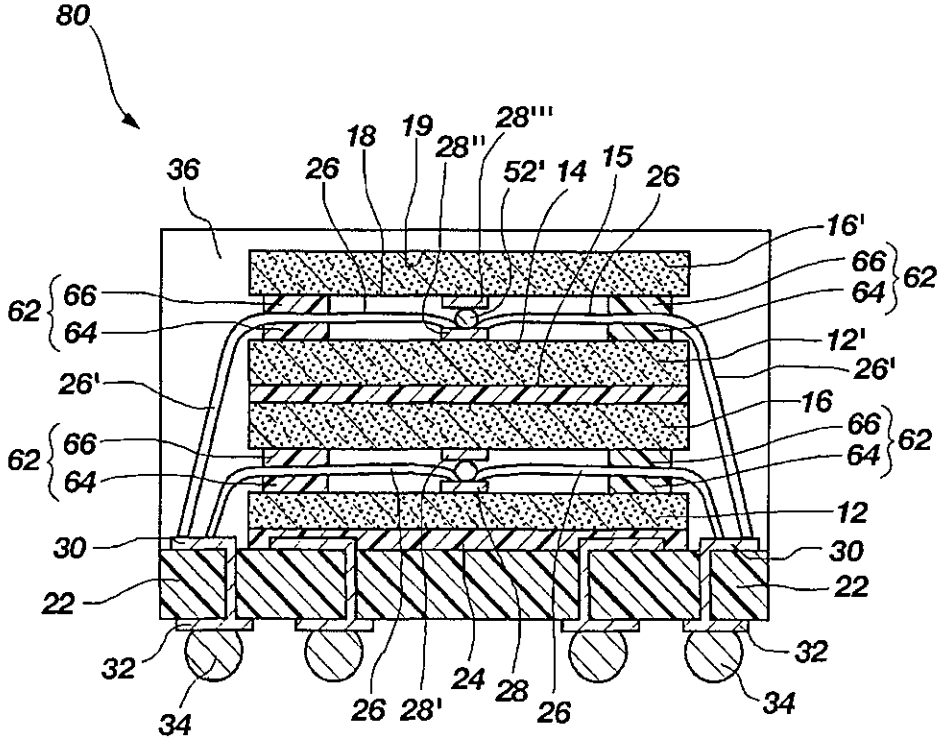

80
36
18
19
28'''
28''
26
52'
14
15
26
16'
66
64
62
62
66
64
12'
26'
26'
16
66
64
62
62
66
64
12
30
30
22
22
32
32
34
34
26
28'
24
28
26

FIG. 6

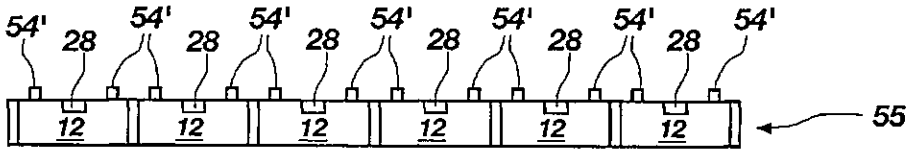


FIG. 7A

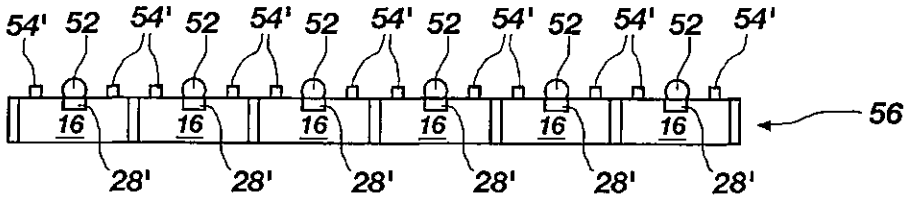


FIG. 7B

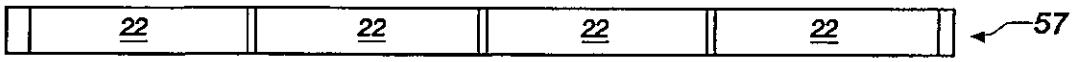


FIG. 7C

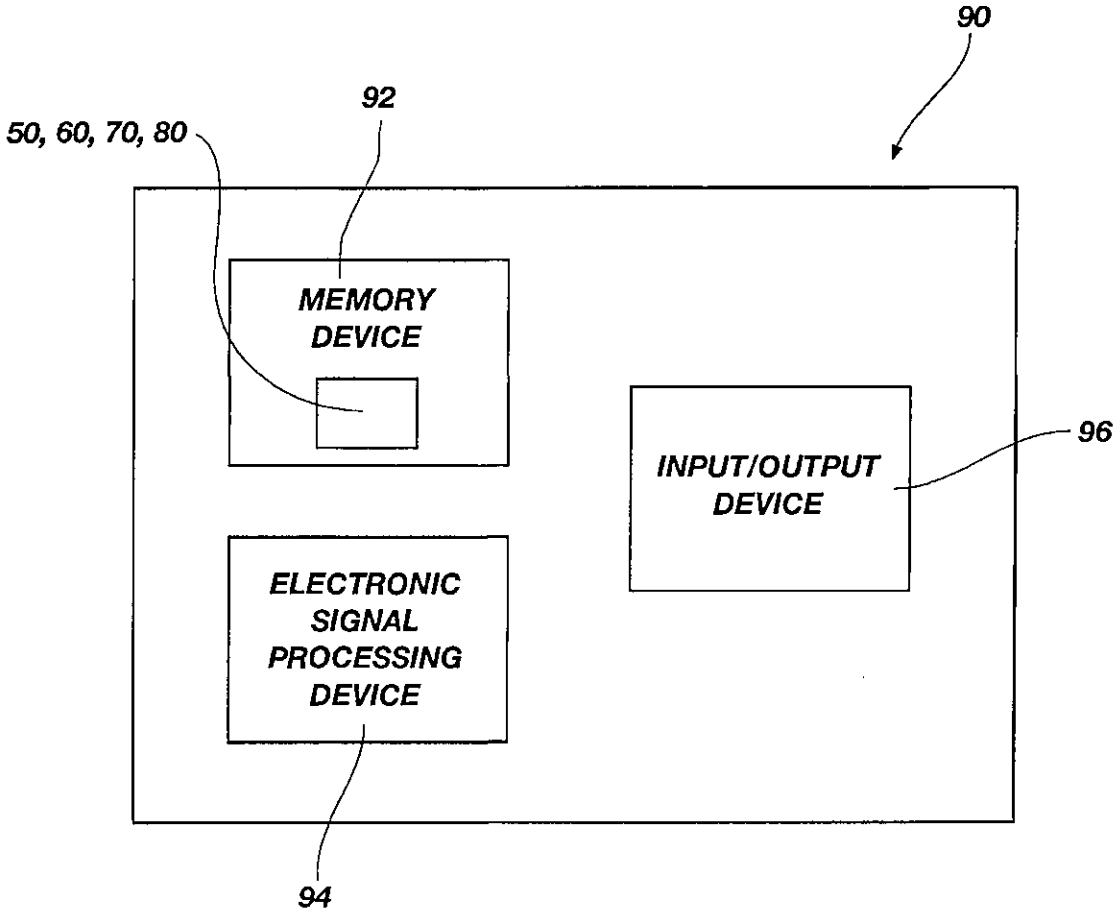

90
92
50, 60, 70, 80
MEMORY DEVICE
INPUT/OUTPUT DEVICE
96
ELECTRONIC SIGNAL PROCESSING DEVICE
94

FIG. 8