

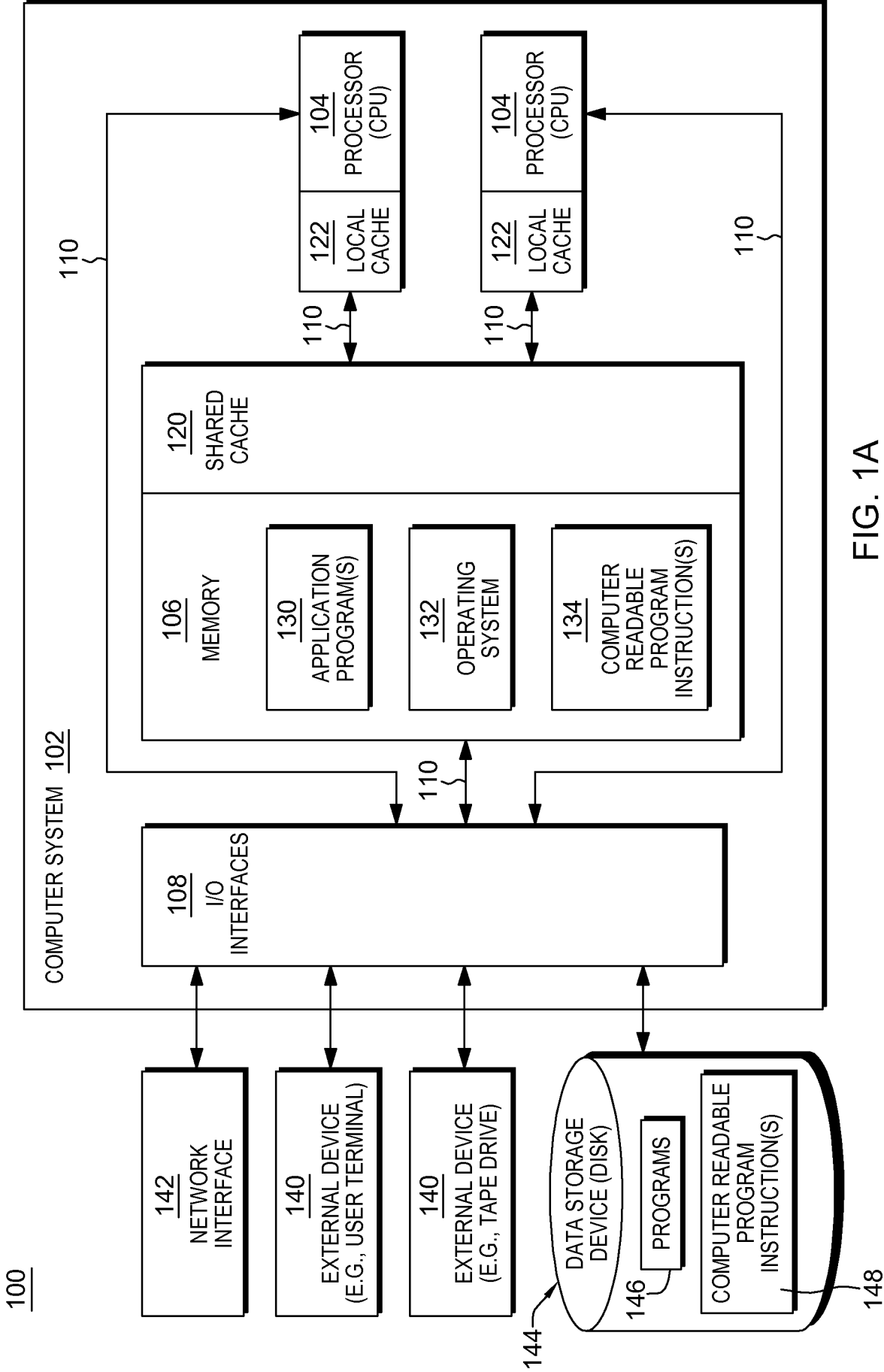


FIG. 1A

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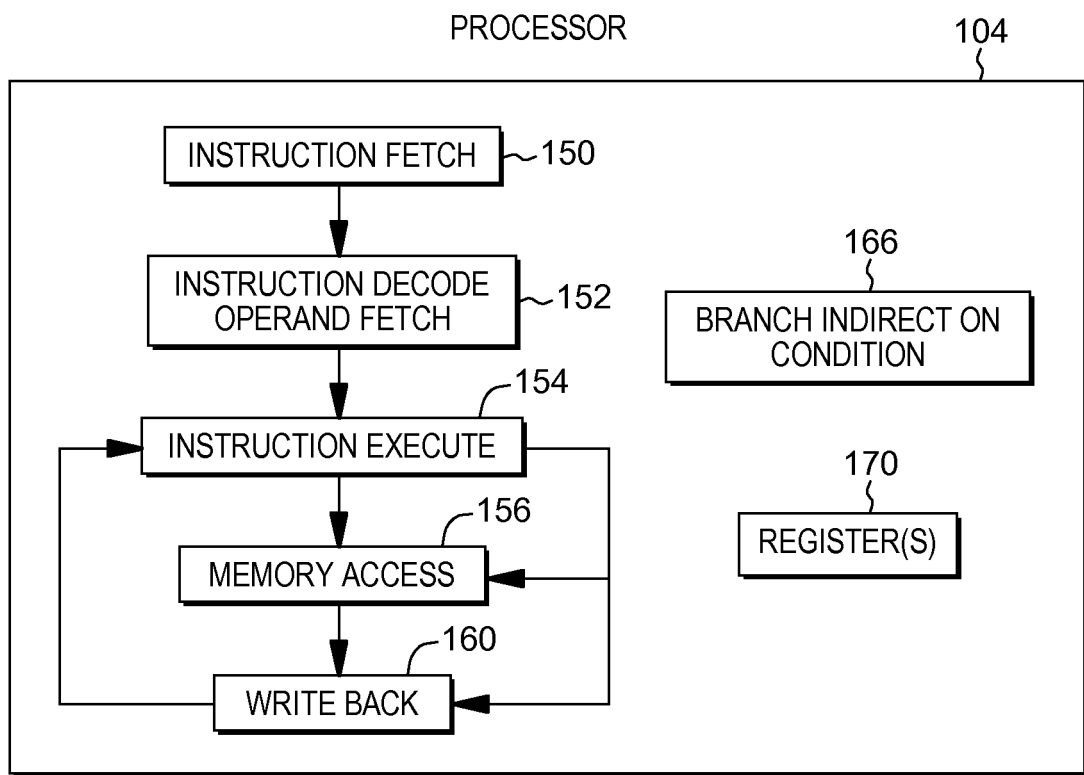


FIG. 1B

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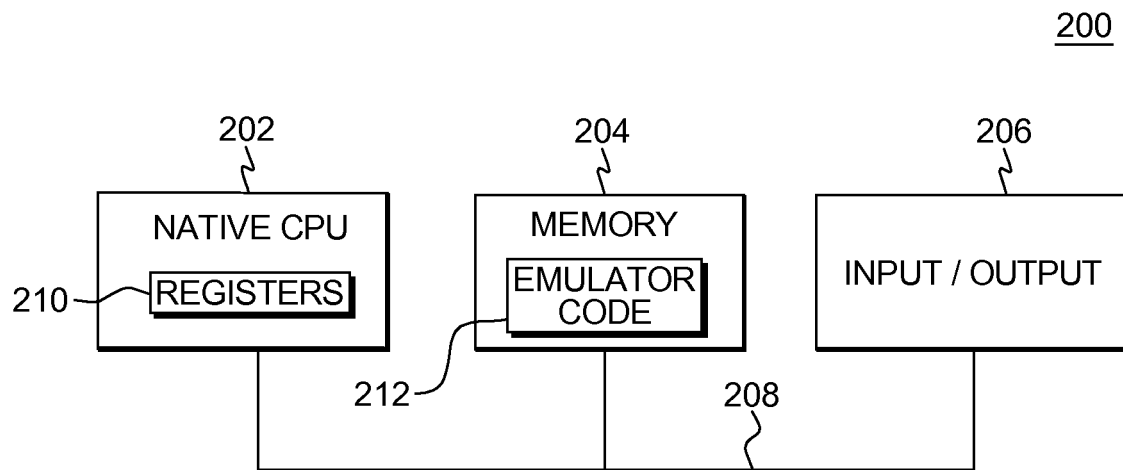


FIG. 2A

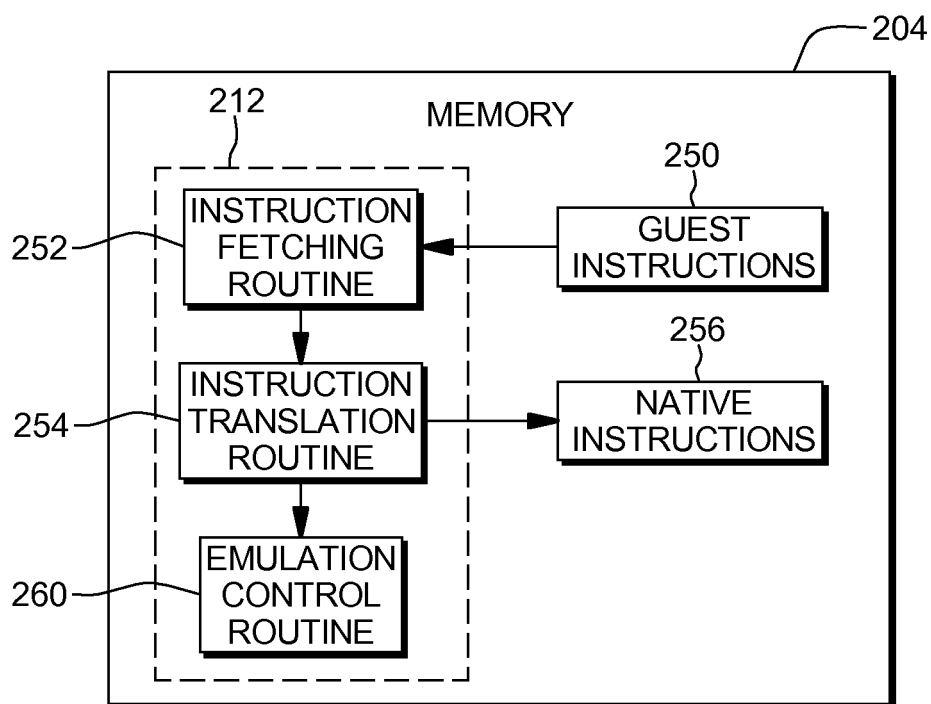


FIG. 2B

300

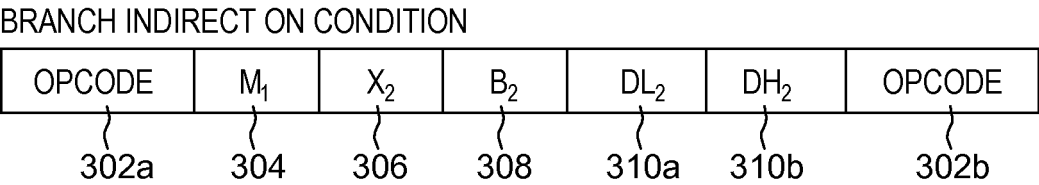


FIG. 3A

320	CONDITION CODE	INSTRUCTION BIT NUMBER OF MASK	MASK POSITION VALUE
	0	8	8
	1	9	4
	2	10	2
	3	11	1

FIG. 3B

350

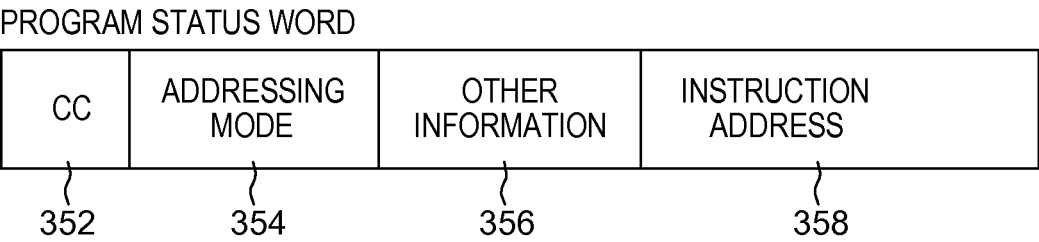


FIG. 3C

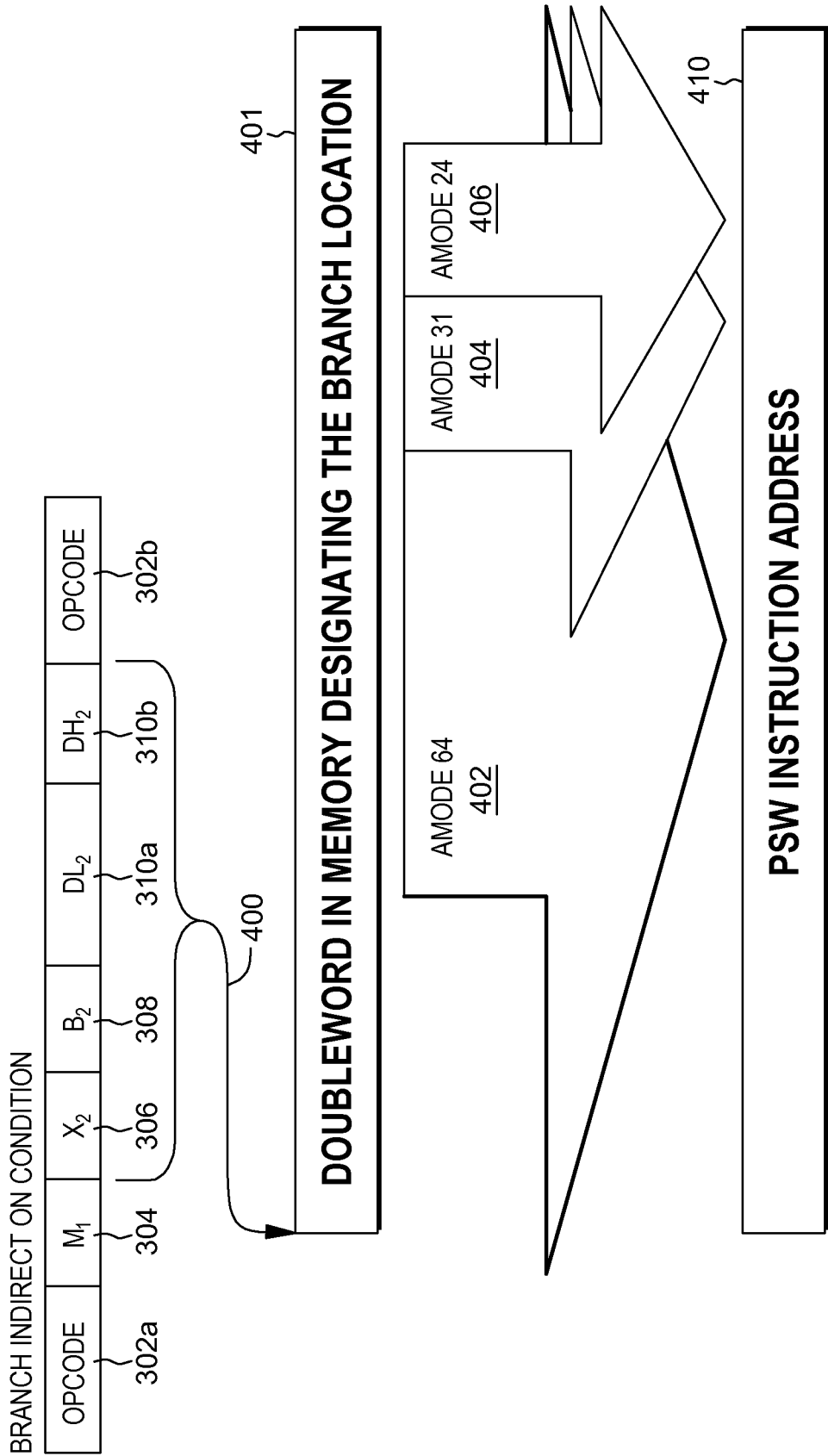


FIG. 4

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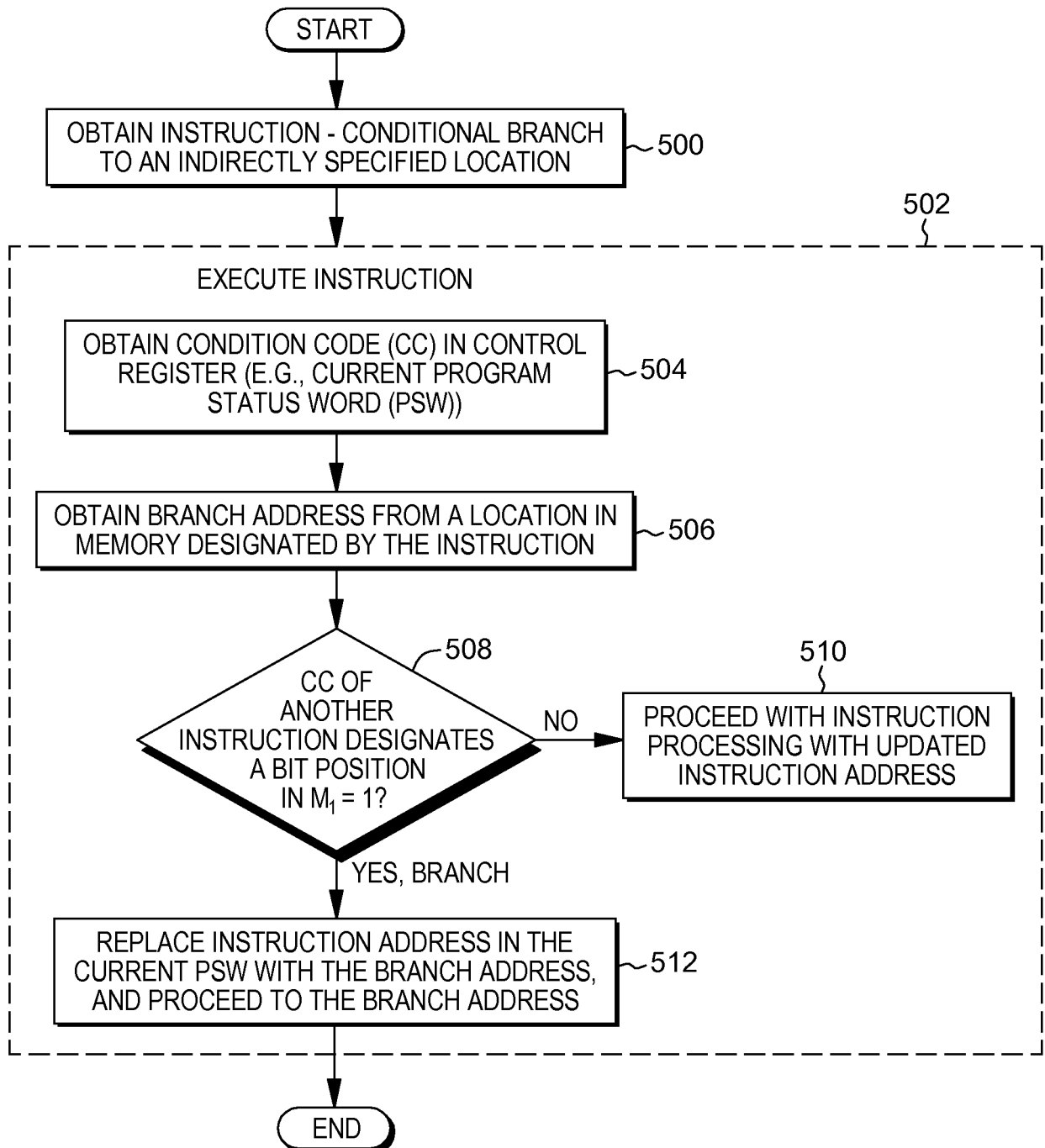


FIG. 5

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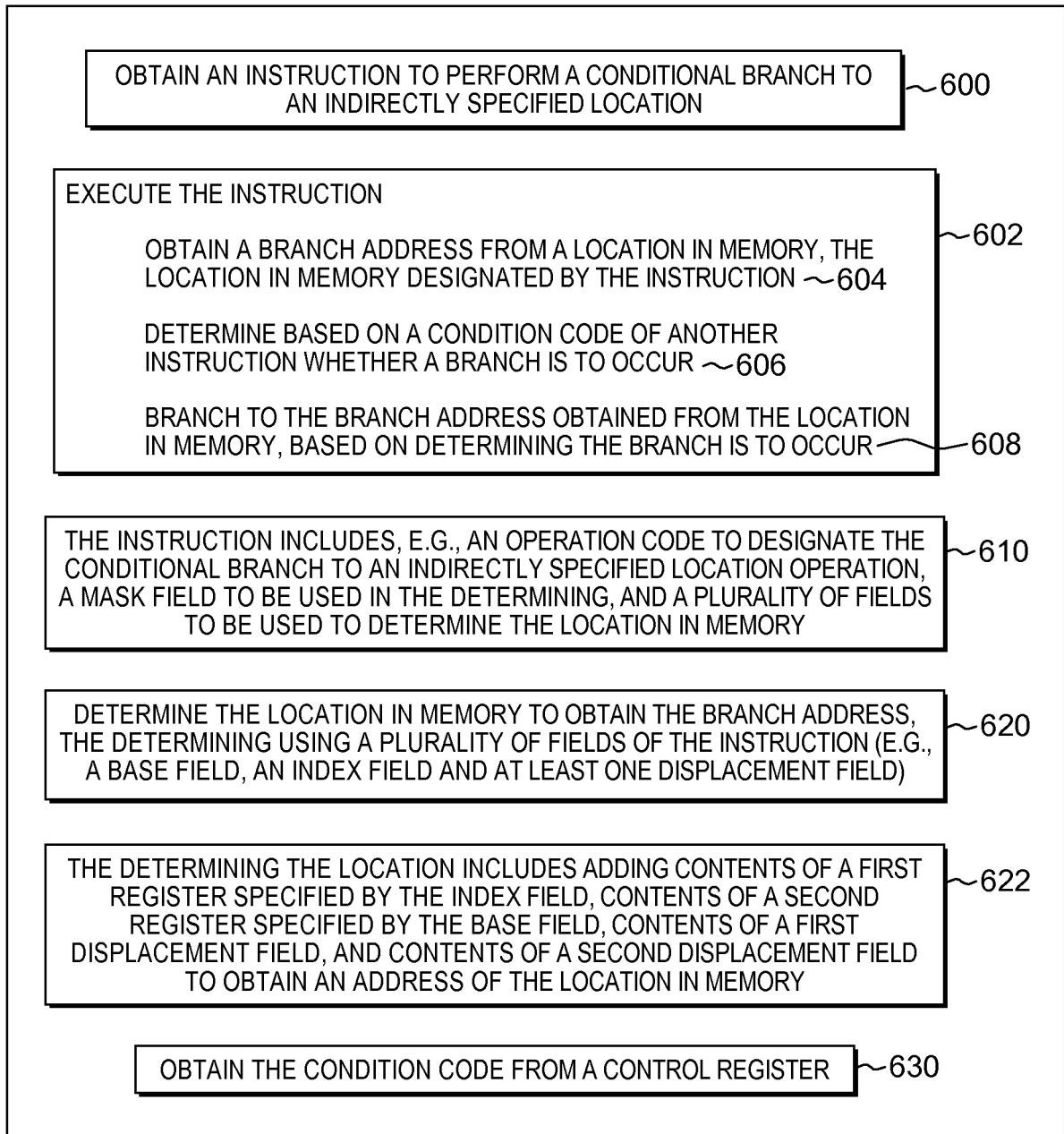


FIG. 6A

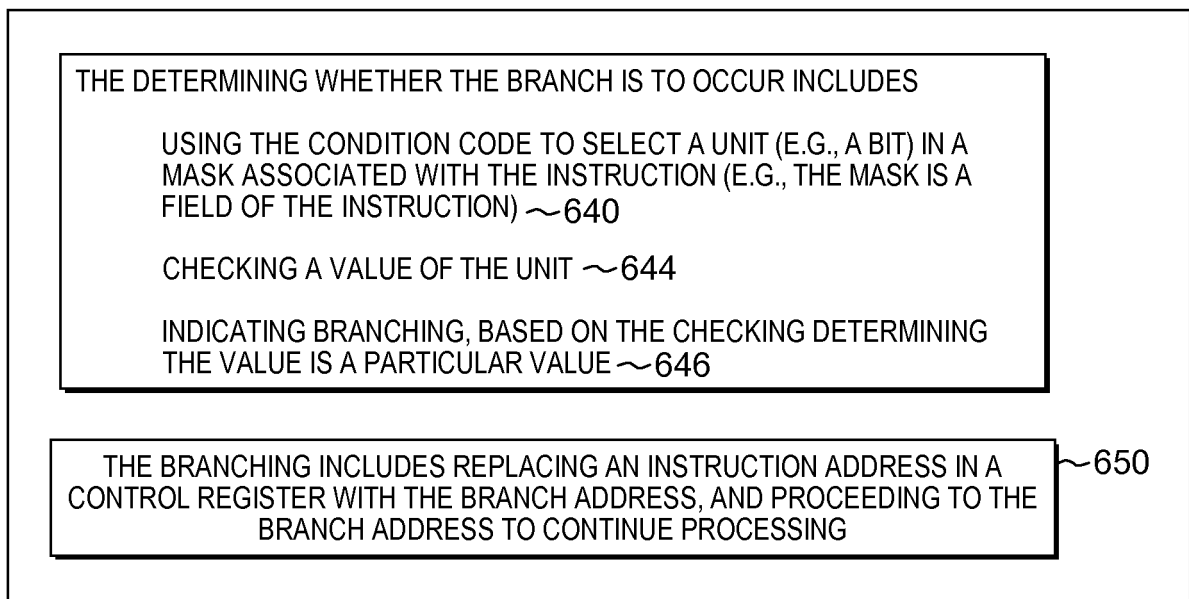


FIG. 6B