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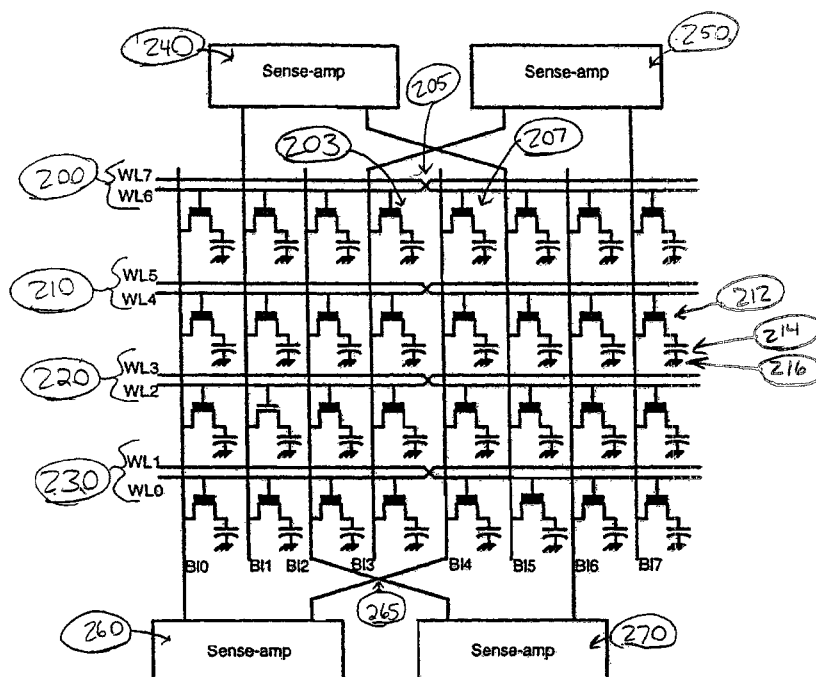
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(54) Title: MULTIPLE WORD-LINE ACCESSING AND ACCESSOR



(57) Abstract: The present invention is in the field of memory device architecture. More particularly, the present invention provides a method, apparatus, system, and machine-readable medium to use a set of word-lines to access a row of memory cells.