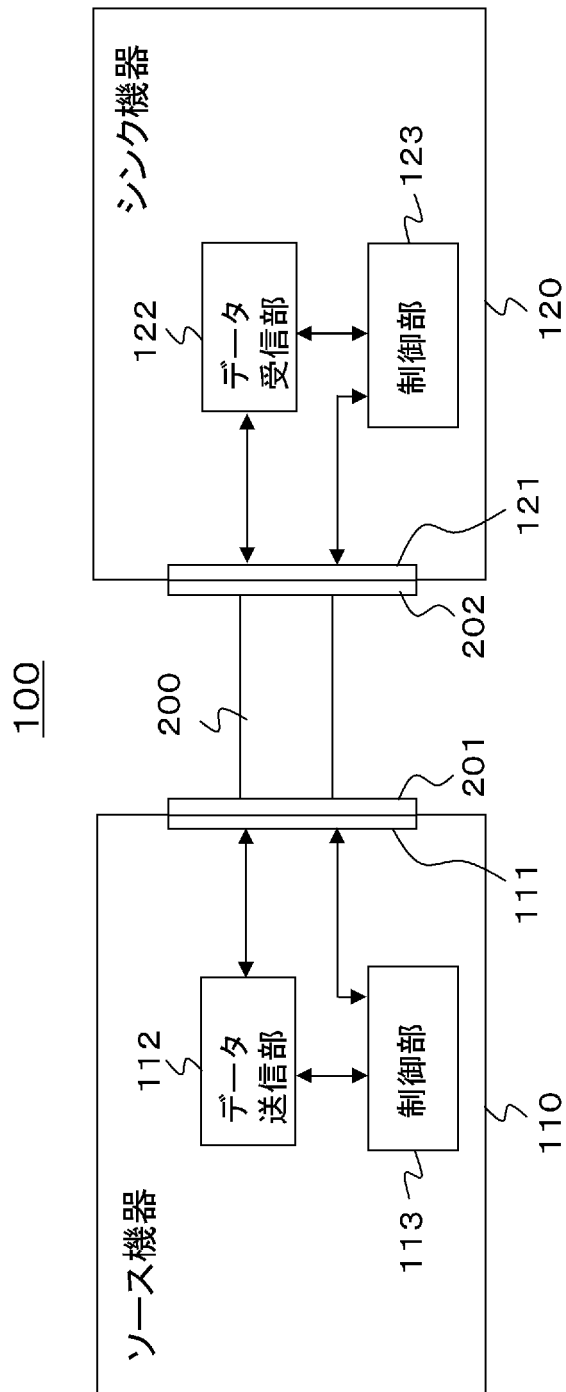
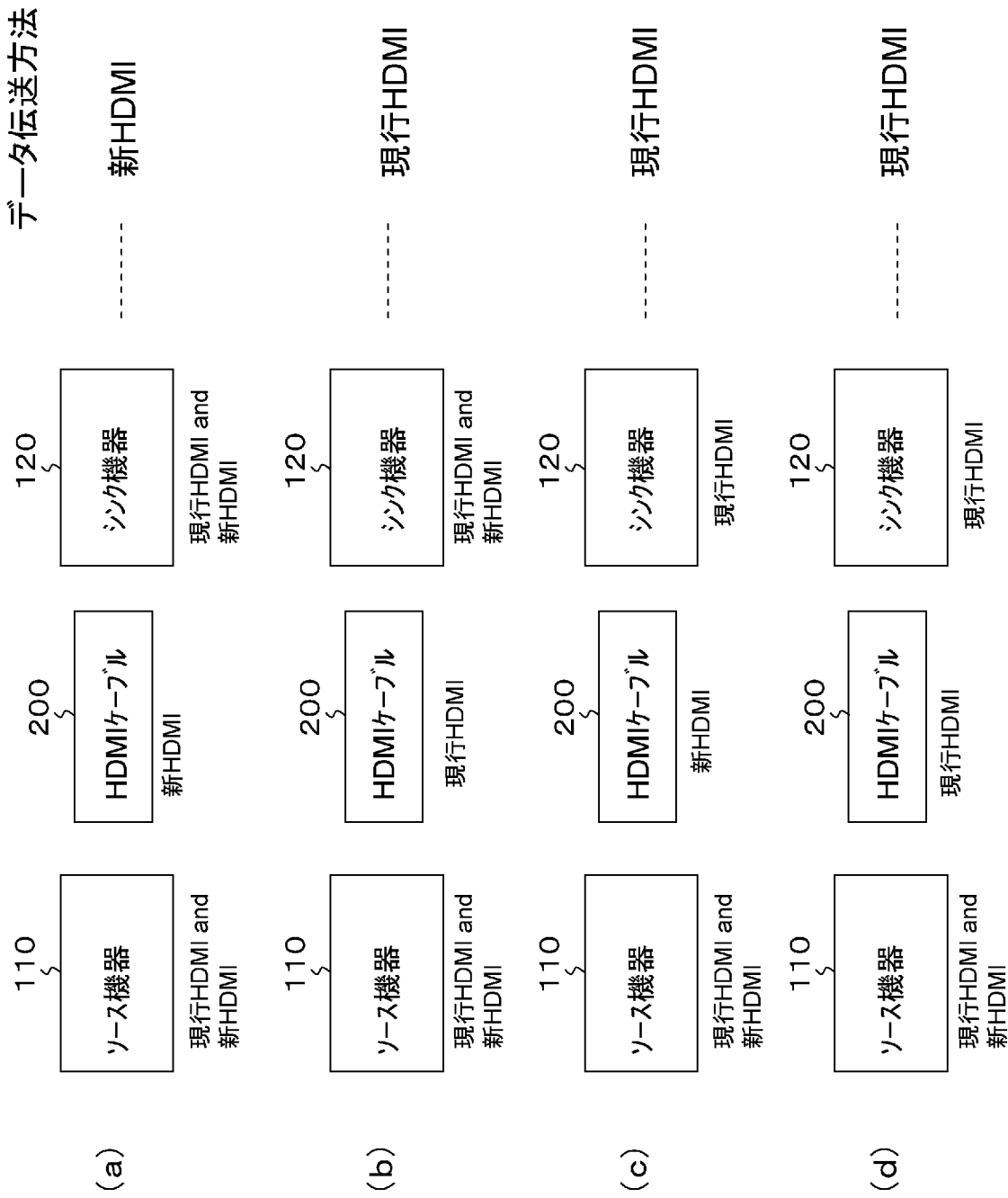


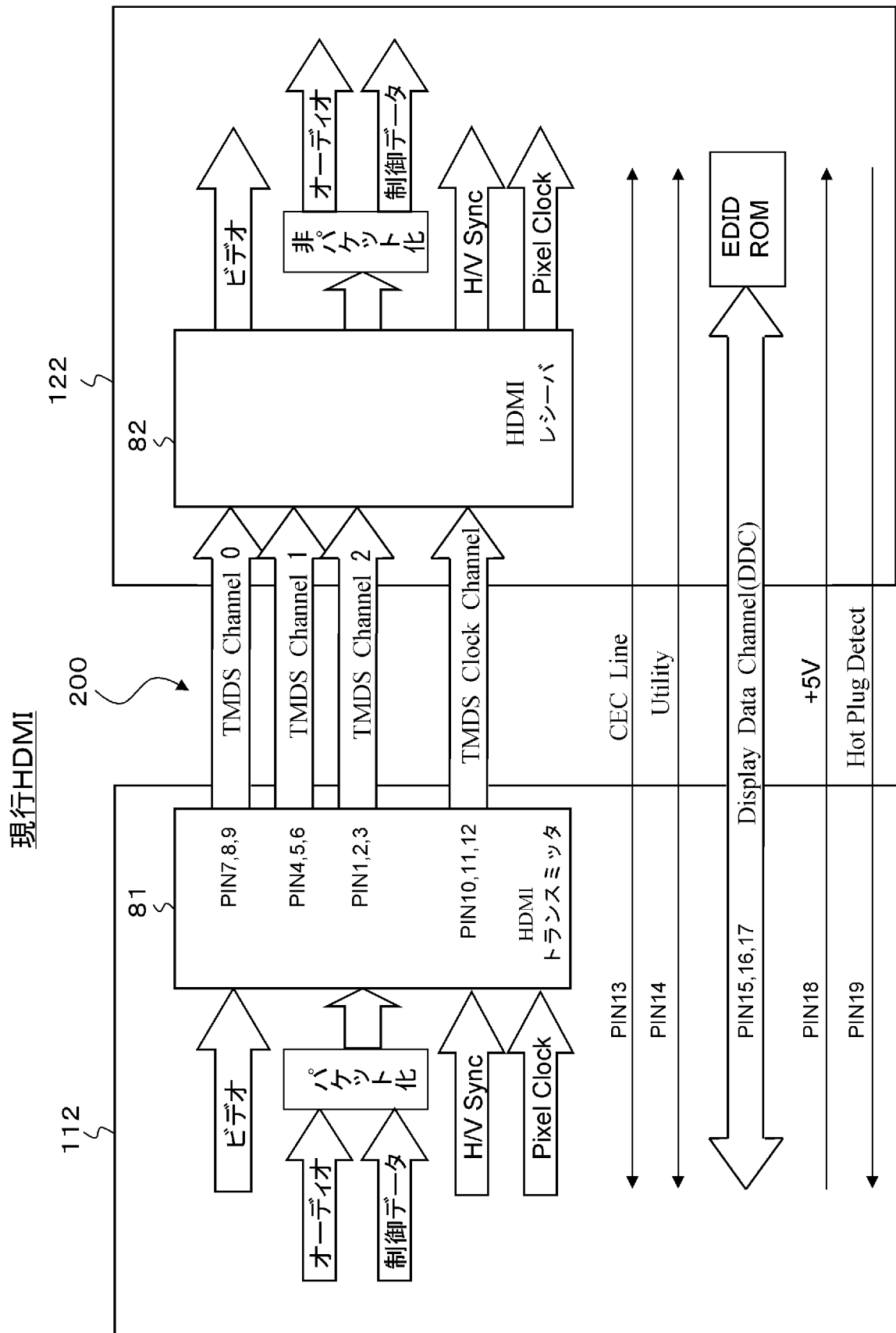
[図1]



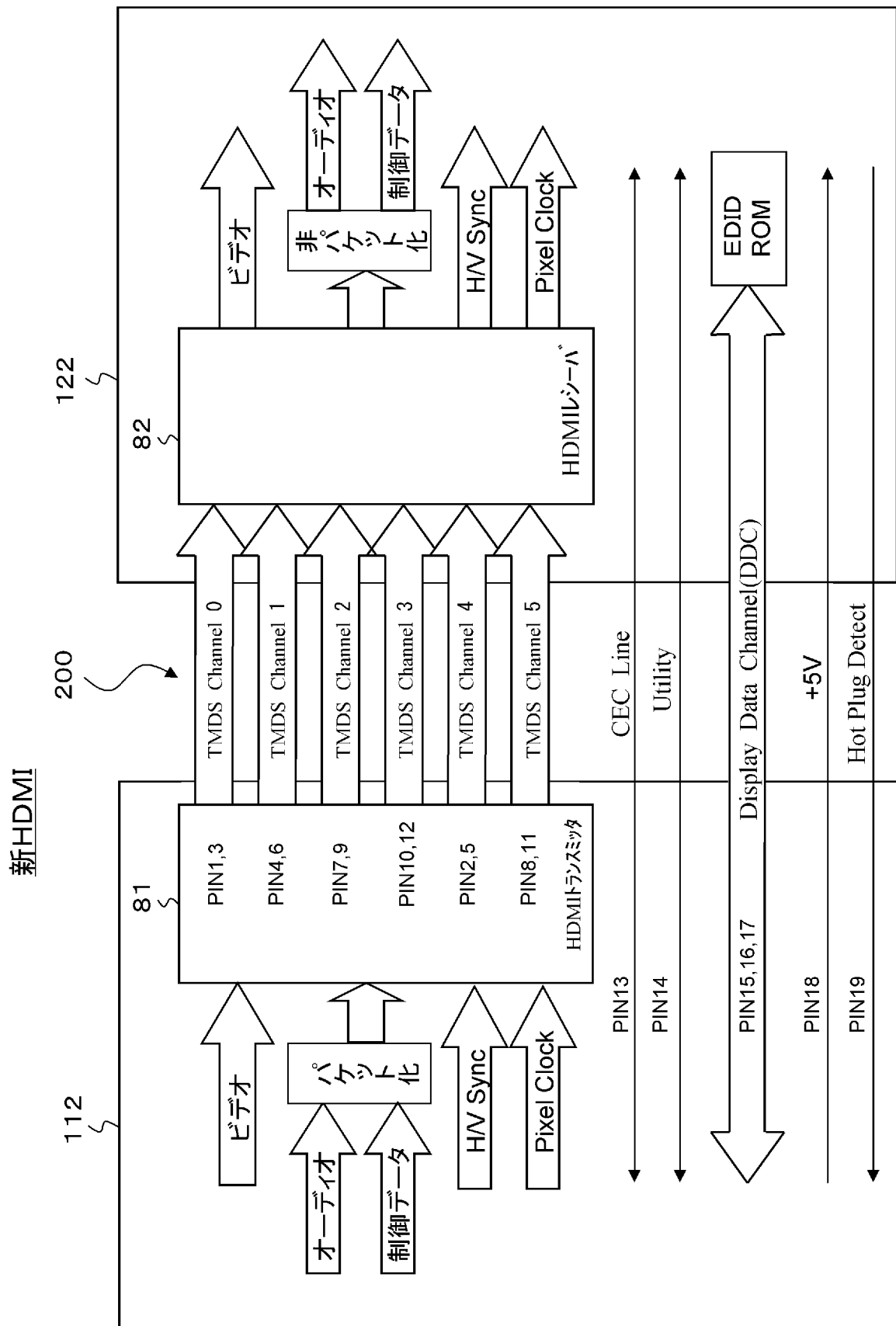
[図2]



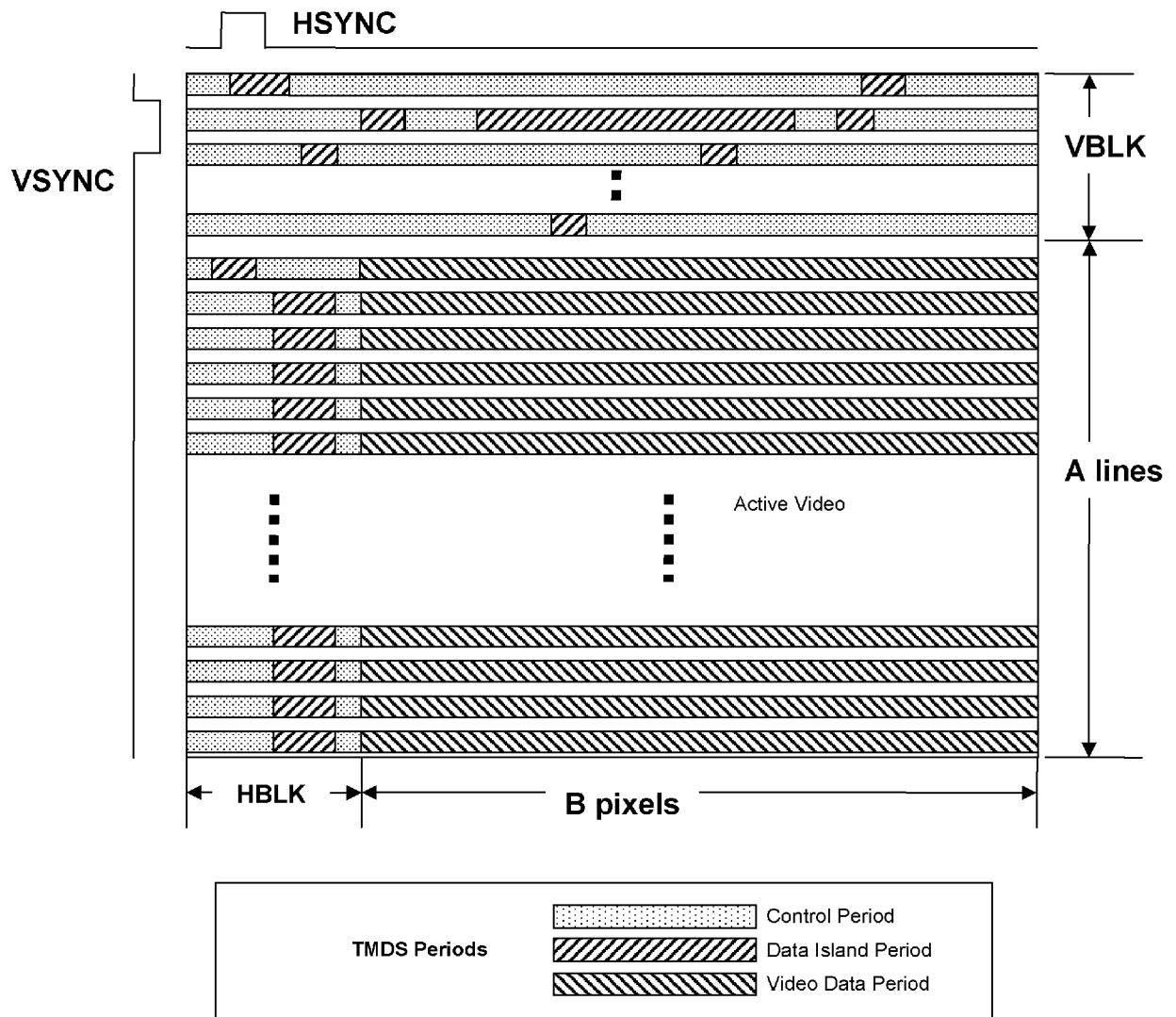
[図3]



[図4]



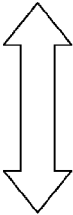
[図5]



[図6]

(a) 現行HDMIピンアサインメント
(Type-A)

PIN	Signal Assignment
1	TMDS Data#2+
2	TMDS Data#2 Shield
3	TMDS Data#2-
4	TMDS Data#1+
5	TMDS Data#1 Shield
6	TMDS Data#1-
7	TMDS Data#0+
8	TMDS Data#0 Shield
9	TMDS Data#0-
10	TMDS Clock+
11	TMDS Clock Shield
12	TMDS Clock-
13	CEC
14	Utility/HEAC+
15	SCL
16	SDA
17	DDC/CEC Ground /HEAC Shield
18	+5V Power
19	Hot Plug Detect/HEAC-

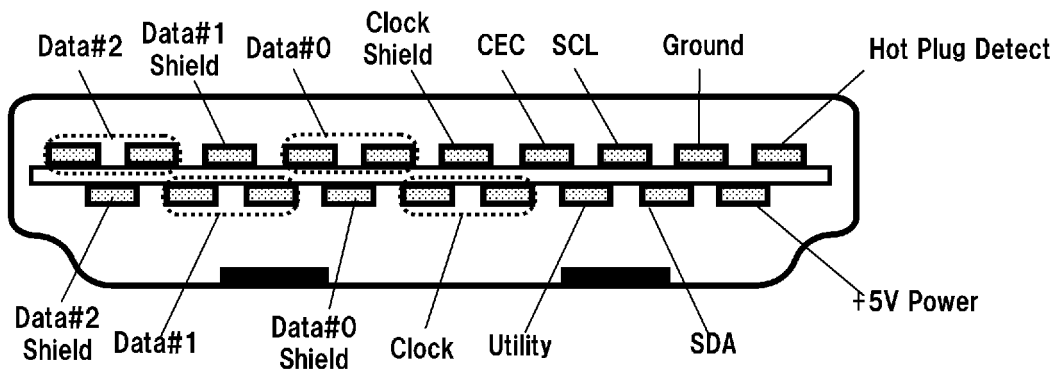


(b) 新HDMIピンアサインメント

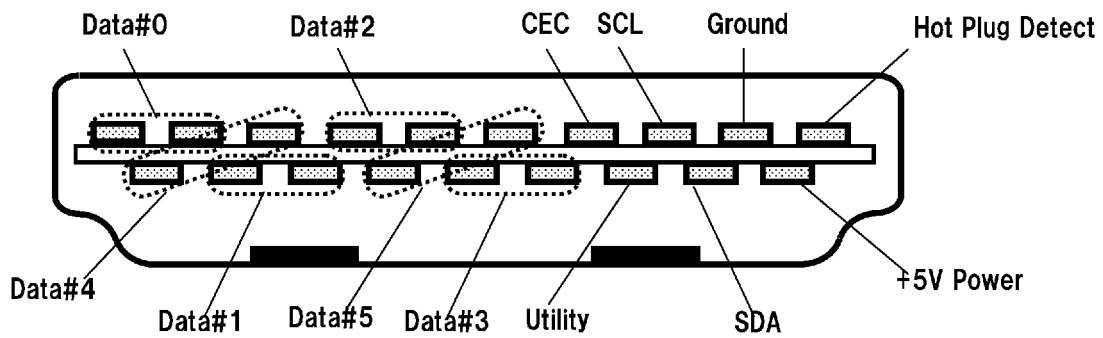
PIN	Signal Assignment
1	TMDS Data#0 +
2	TMDS Data#4 +
3	TMDS Data#0 -
4	TMDS Data#1 +
5	TMDS Data#4 -
6	TMDS Data#1 -
7	TMDS Data#2 +
8	TMDS Data#5 +
9	TMDS Data#2 -
10	TMDS Data#3 +
11	TMDS Data#5 -
12	TMDS Data#3 -
13	CEC
14	Utility/HEAC+
15	SCL
16	SDA
17	DDC/CEC Ground /HEAC Shield
18	+5V Power
19	Hot Plug Detect/HEAC-

[図7]

(a) 現行HDMIピン配置

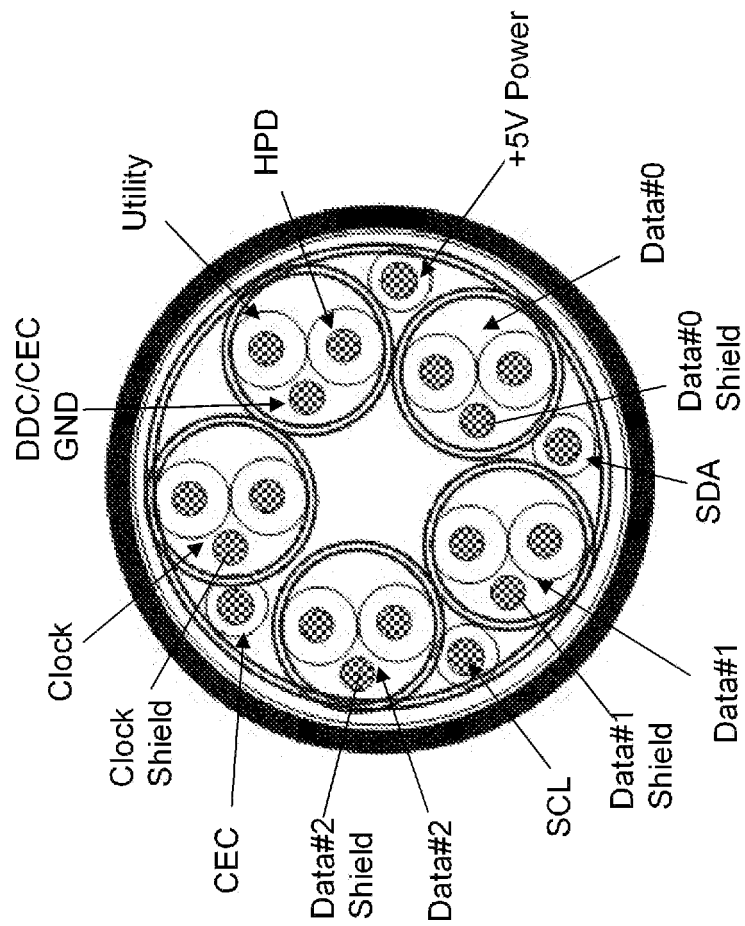


(b) 新HDMIピン配置

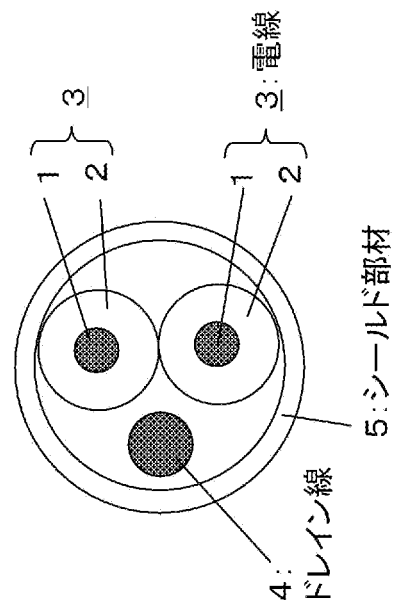


[図8]

(a) 現行HDMIケーブル構造例

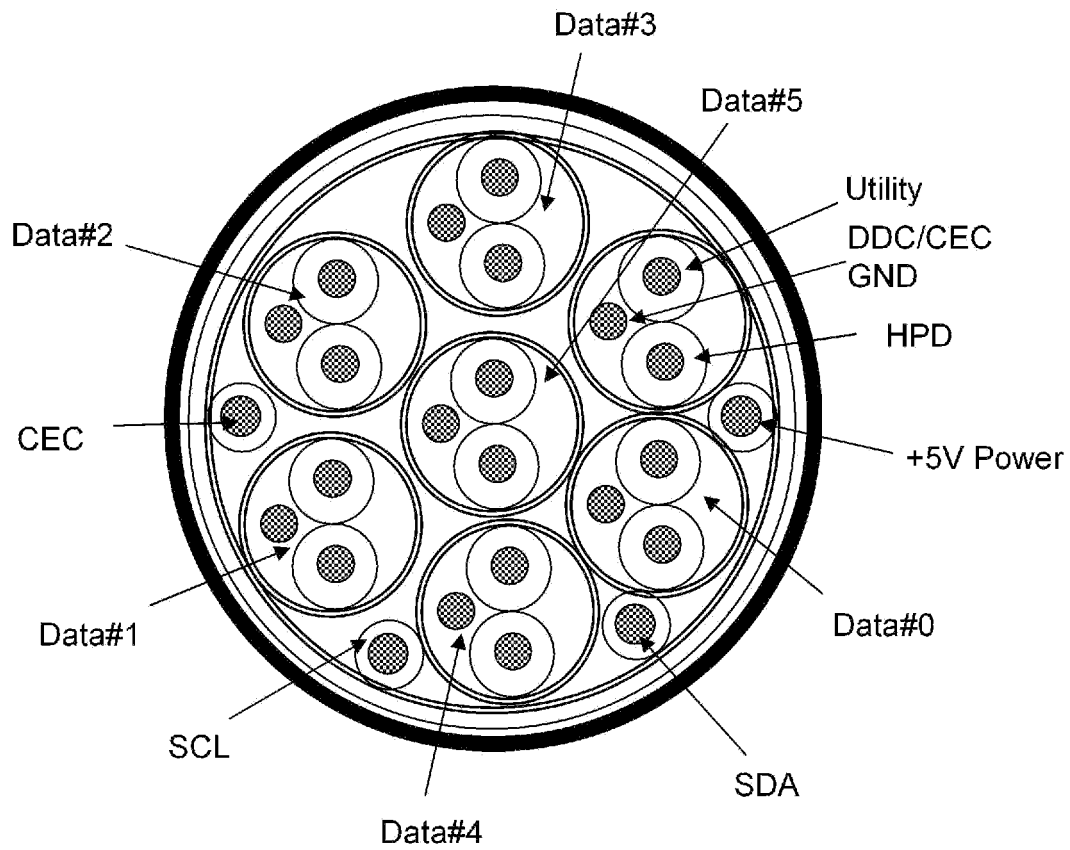


(b) シールドツイストペア部



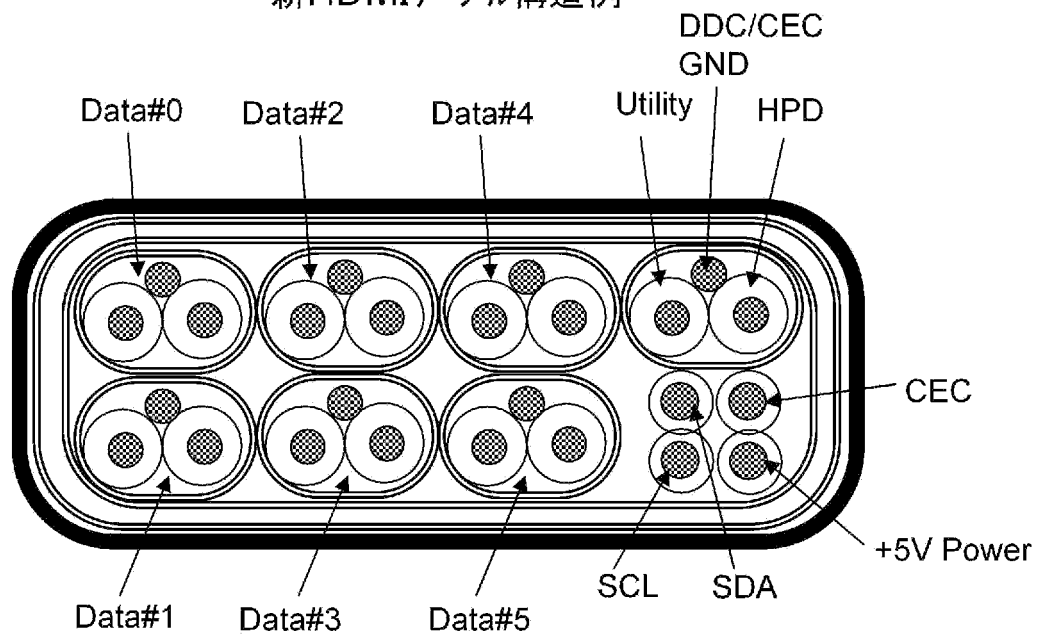
[図9]

新HDMIケーブル構造例

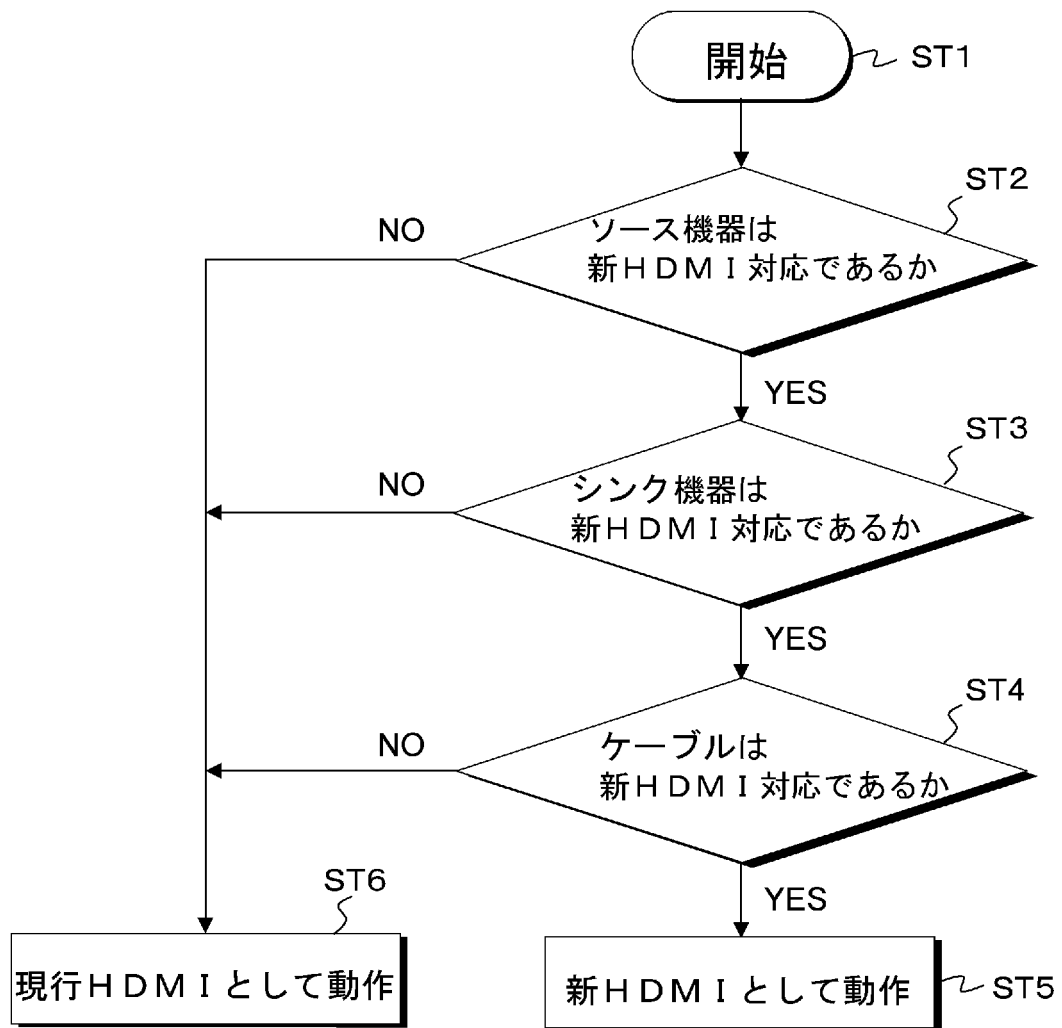


[図10]

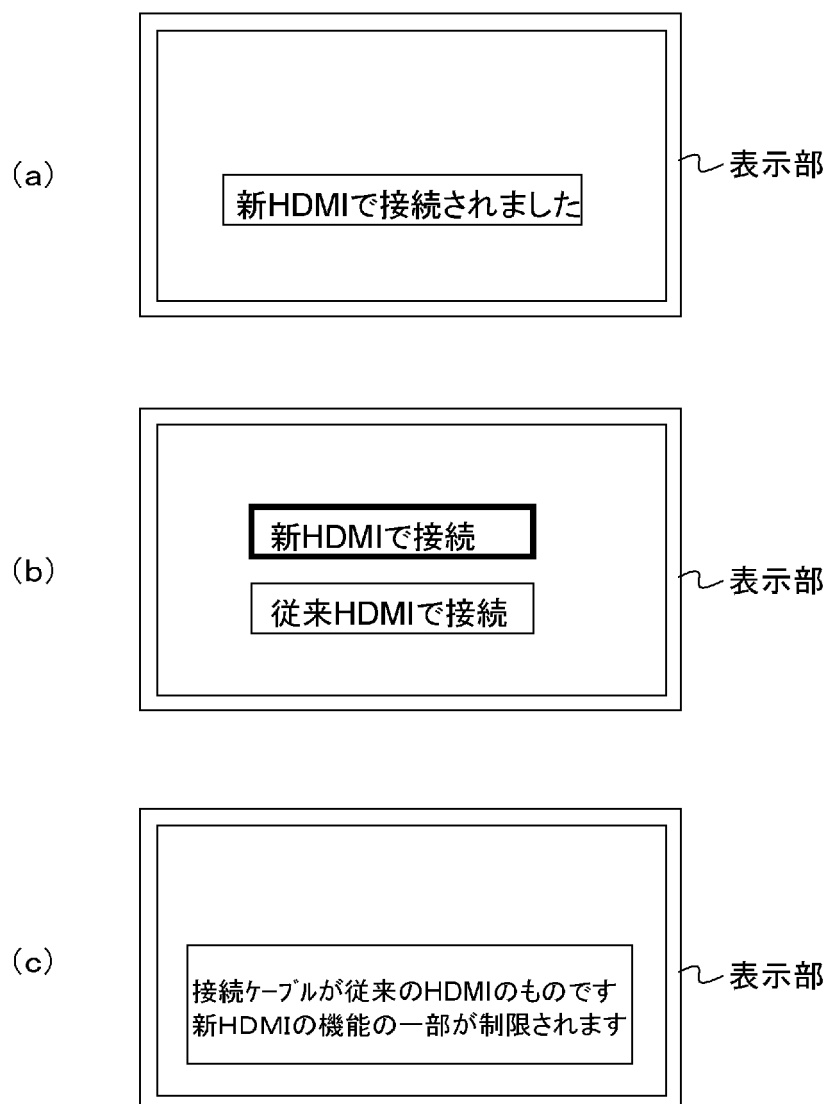
新HDMIケーブル構造例



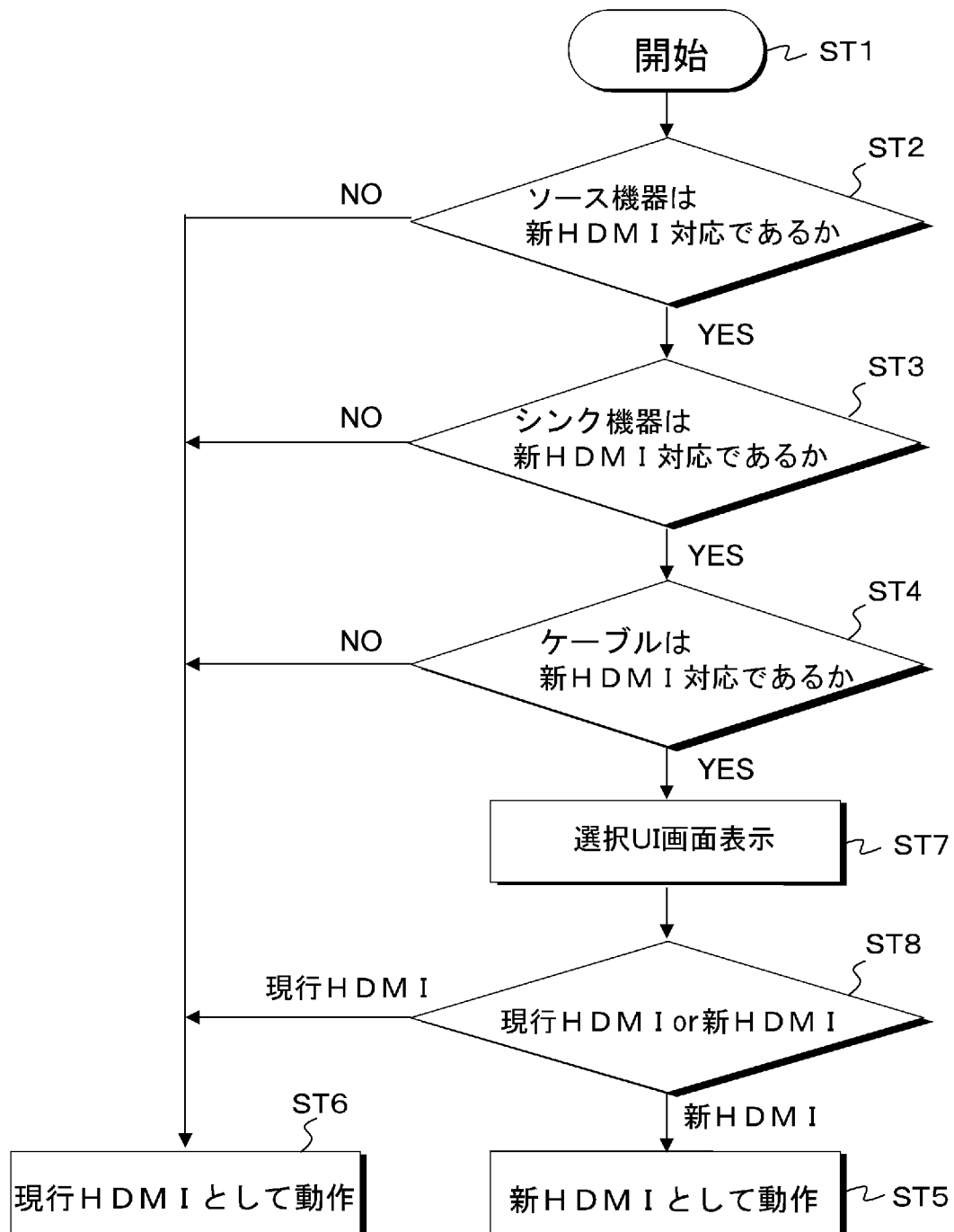
[図11]



[図12]



[図13]

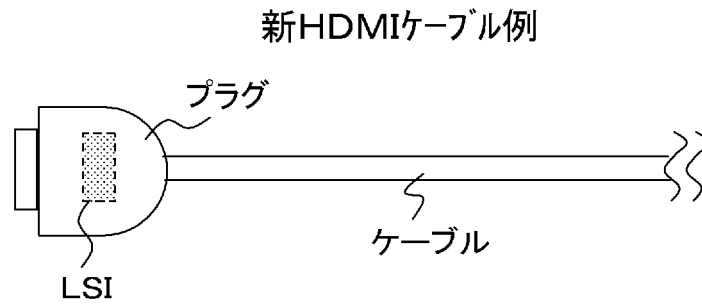


[図14]

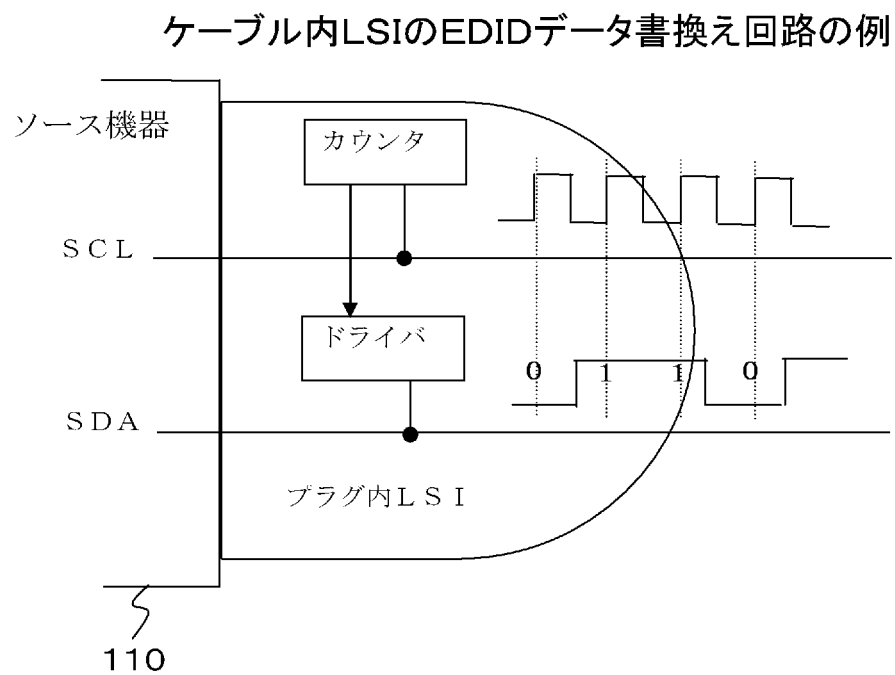
EDID例

Byte #	7	6	5	4	3	2	1	0
	Rsvd (0)	Rsvd (0)	Rsvd (0)	Rsvd (0)	Rsvd (0)	New Rx Sink	New Cable	Rsvd (0)

[図15]

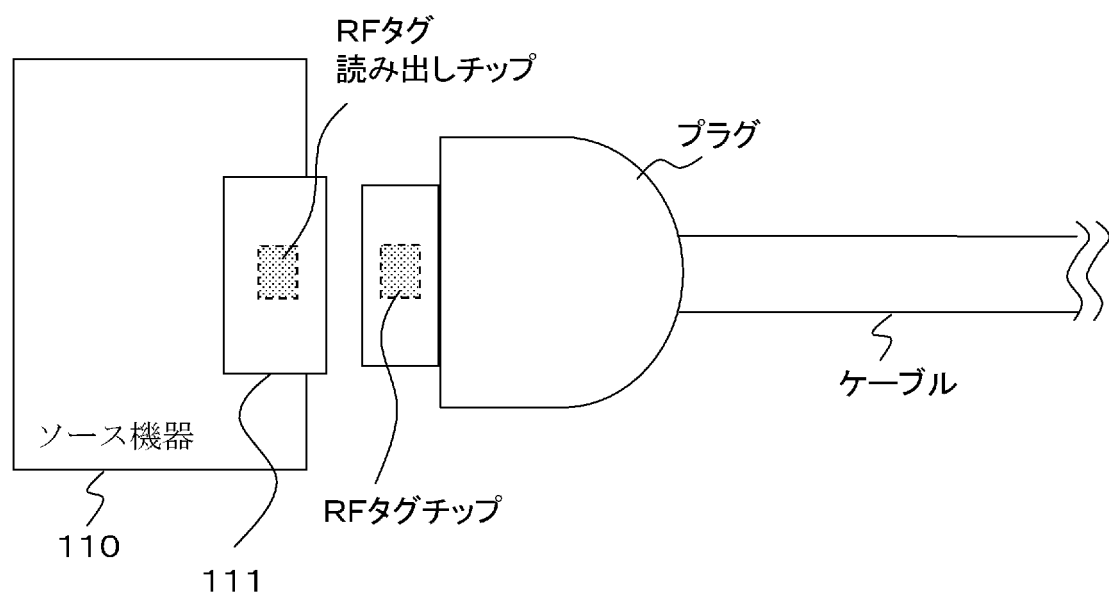


[図16]

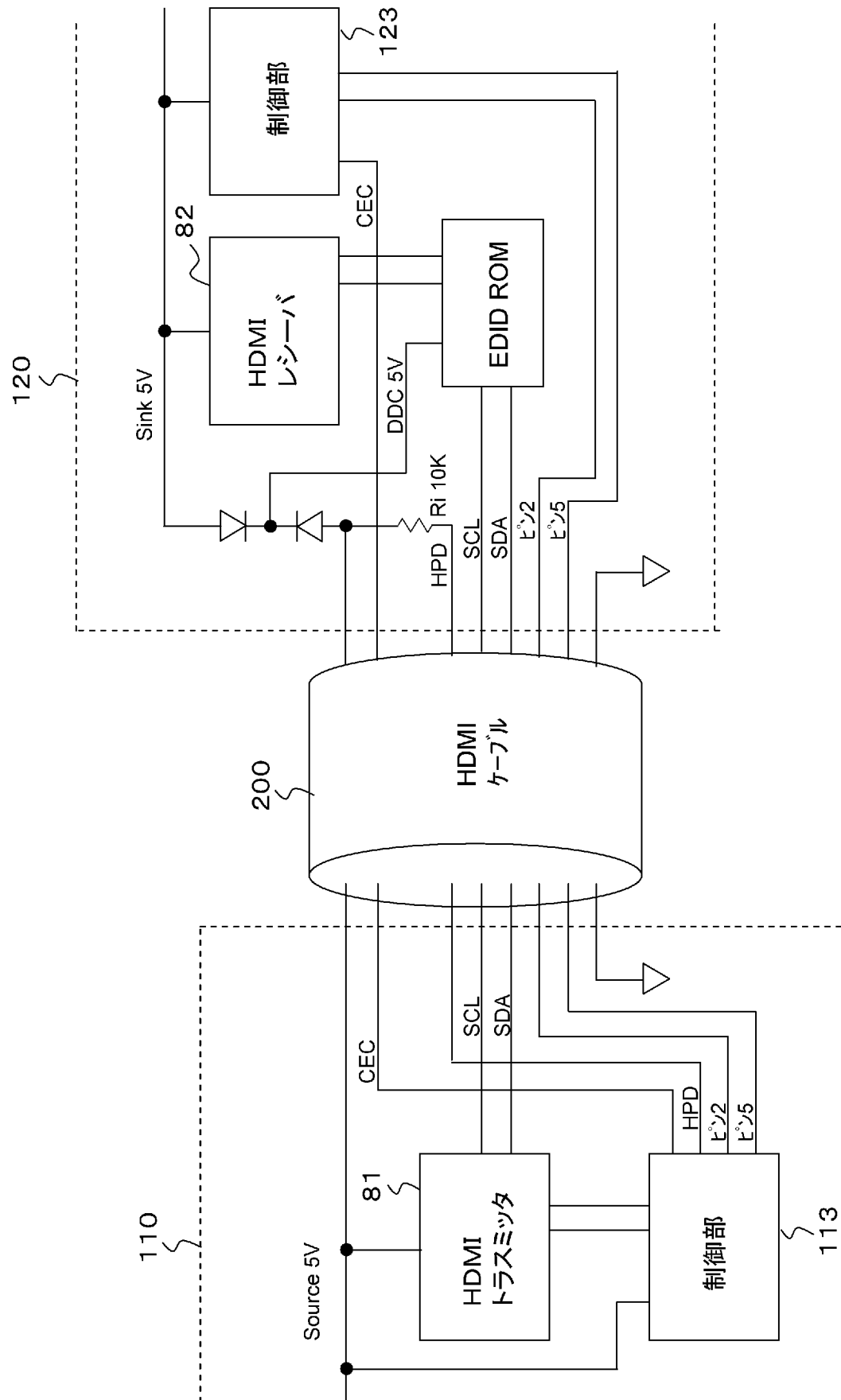


[図17]

RFタグチップを用いたケーブル情報通知回路例

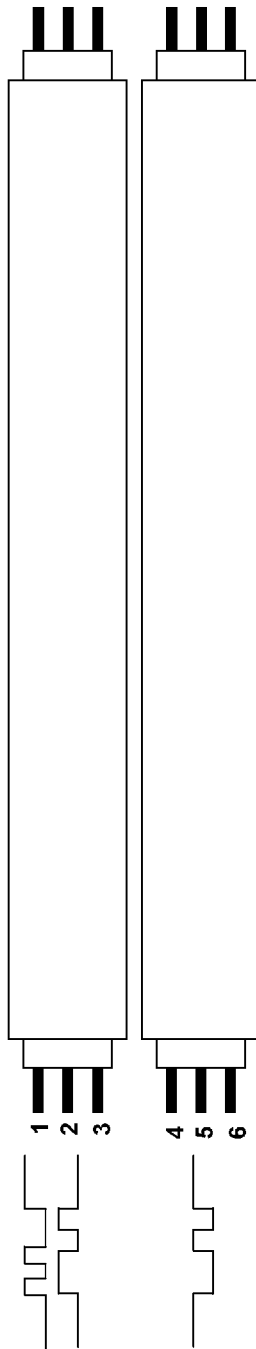


[図18]

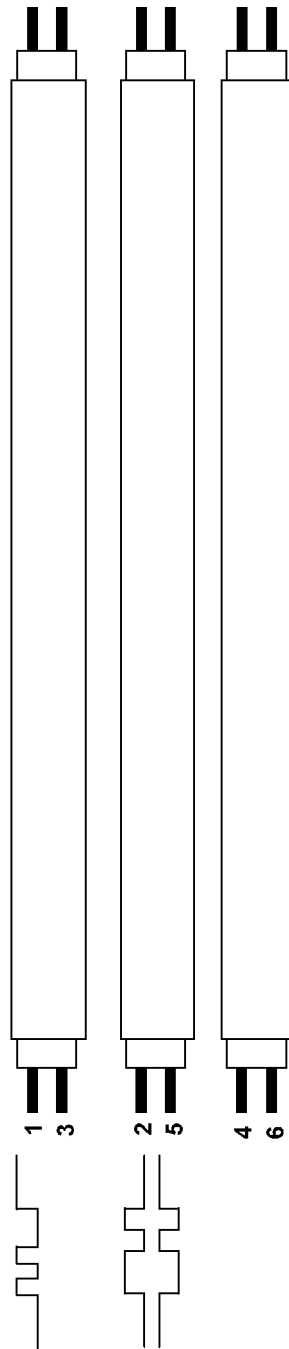


[図19]

(a) 現行HDMIケーブル

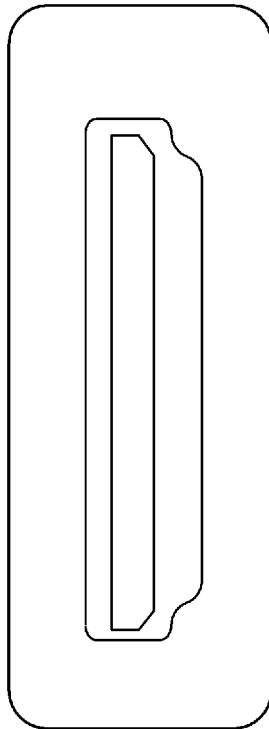


(b) 新HDMIケーブル

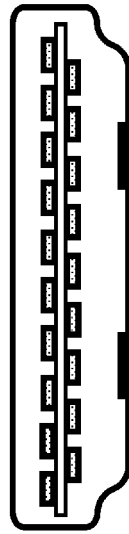


[図20]

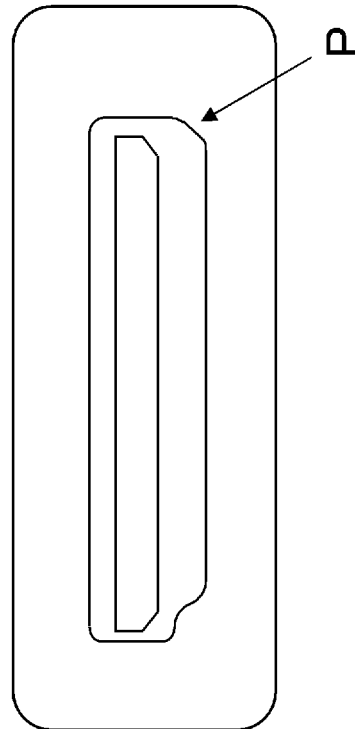
(a) 現行HDMIプラグ



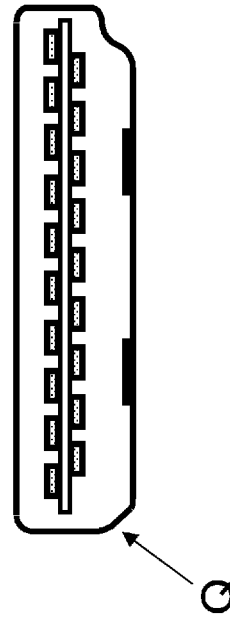
(b) 現行HDMIレセプタクル



(c) 新HDMIプラグ

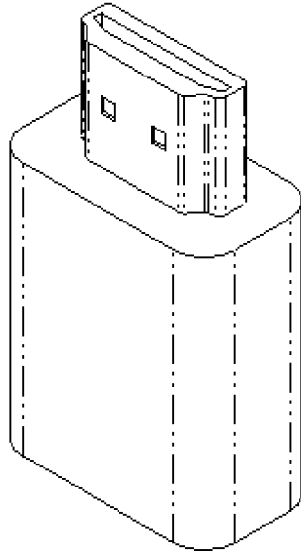


(d) 新HDMIレセプタクル



[図21]

(a) 従来HDMIプラグ



(b) 新HDMIプラグ

