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TITLE OF THE INVENTION

A Video Camera Having an Adaptive Automatic Iris
Control Circuit

BACKGROUND OF THE INVENTION

5 Field of the Invention

The present invention relates to video cameras, and more particularly, to an automatic iris control circuit for automatically adjusting the aperture size to regulate the amount of light incident into the optical system of a video camera according to the incident amount of light.

10 Description of the Background Art

Referring to Fig. 1, a conventional video camera includes a lens 102 for bringing together incident light from an object to form an image on a predetermined image formation plane, an image sensing device 104 having an image sensing plane arranged on the image sensing plane for converting the optical image on the image sensing plane into a video signal which is an electrical signal by photoelectric conversion, an iris plate 103 for regulating the amount of light incident upon image sensing device 104, an iris motor 113 for driving iris plate 103, a preamplifier 116 for amplifying the video signal provided from image sensing device 104 and providing the same into a signal processing circuit (not shown) for converting into a television method, and an automatic iris control

circuit 217 responsive to the video signal provided from preamplifier 116 for operating iris motor 113 so that the level of the video signal attains a predetermined level.

Automatic iris control circuit 217 includes a gain control amplifier (GCA) 206 for amplifying the video signal provided from preamplifier 116 corresponding to the central portion of the screen by a gain that is greater than that of the remaining portion of the screen, a voltage adder 207 for adding to the amplified video signal provided from GCA 206 sawtooth voltage that will raise the voltage in the lower portion of the screen, a detection circuit 208 for averaging the outputs of voltage adder 207, and a comparator 209 having a negative input terminal connected to the output of detection circuit 208 and a positive input terminal connected to a variable reference voltage (V_{IR}) 210 for comparing the signal provided from detection circuit 208 and reference voltage 210 to drive iris motor 113 according to the comparison output.

Referring to Fig. 2, the amplification carried out in GCA 206 is for the purpose of giving a particularly great weight to the central portion 252 of a screen 251. When object 254 is located at the central portion 252, as shown in Fig. 3A, iris plate 103 is driven to obtain an appropriate image sensing state of object 254 owing to the



amplification by GCA 206. Even if there is ground 255 in the background scenery with rear light, photometry is carried out giving weight to object 254 rather than to ground 255 or the background scenery.

5 Referring to Fig. 2 again, voltage adder 207 adds offset voltage to the lower region 253 of screen 251. In the case where sawtooth voltage such as that shown in Fig. 1 is added, the weight for control of iris plate 103 becomes greater as a function of distance towards the
10 bottom of screen 251. Therefore, when object 256 is located with the sky as the background in screen 251 as shown in Fig. 3B, the diaphragm is adjusted to a value that can shoot object 256 in an optimum state without being affected by the luminance of the sky.

15 Referring to Fig. 1, a conventional video camera operates as follows. The incident amount of light from object 101 forms an image on the image sensing plane of image sensing device 104 by lens 102. Image sensing device 104 converts the optical image into a video signal
20 by photoelectric conversion. The video signal is provided to amplifier 116. Preamplifier 116 amplifies output of image sensing device 104 to provide the same to a signal processing circuit and the input of GCA 206.

As already described with reference to Figs. 2 and
25 3A, GCA 206 amplifies the converted video signal



corresponding to the central region of the screen by a gain that is greater than that of the surrounding region. The amplified signal is provided to voltage adder 207. As described with reference to Figs. 2 and 3B, voltage adder 5 207 adds sawtooth voltage to the input signal so that the video signal representing the lower region of the screen becomes a greater value. This signal is provided to detection circuit 208, where the video signals from voltage adder 207 are averaged and provided to the 10 negative input terminal of comparator 209. Comparator 209 compares the output of detection circuit 208 and reference voltage 210 to drive iris motor 113 according to the comparison output. Iris motor 113 drives iris plate 103 according to the drive voltage. The amount of light of 15 the object image upon image sensing device 104 is adjusted by the open/close of iris plate 103.

The detail operation of automatic iris control circuit 217 is as follows.

If the luminance of the object image is too bright, 20 the amplitude of the image signal provided from preamplifier 116 becomes great. This increases the average voltage of the video signal provided from detection circuit 208. If the output voltage of detection circuit 208 becomes greater than reference voltage 210, 25 the output of comparator 209 shifts to a low potential.



Iris motor 113 responds to the output of comparator 209 to be operative to close iris plate 103. This reduces the incident of light from the object to image sensing device 104.

5 If the incident amount of light from the object to image sensing device 204 is low, an operation opposite to that described above is carried out. That is to say, the output voltage of detection circuit 208 decreases to become lower than reference voltage 210. The output of
10 comparator 209 shifts to a high potential so that iris motor 113 operates to open iris plate 103. Thus, the incident amount of light to image sensing device 104 increases.

By the above-described operation of the automatic
15 iris control circuit, the aperture size of iris plate 103 is adjusted to obtain a maximum luminance of an object located in the central and lower region of the screen. The operator of the video camera does not have to manually adjust the aperture size to obtain a desired shooting
20 state.

As described above, a video camera having a conventional automatic iris control circuit employs center-weighted metering and foot-weighted metering. This is based on the typical shooting condition where an object
25 is usually located at the central region of a screen, and



that the sky is located above as the background with the object in the lower region. However, there are some cases where the center-weighted metering and the foot-weighted metering may not result in an optimum diaphragm.

5 Consider the case where the entire background of object 254 is of high luminance in screen 251, as shown in Fig. 3C. In this case, the luminance of the lower portion of screen 251 becomes high. If foot-weighted metering is employed, the diaphragm will be operated towards the
10 closing direction. This means that the sensed image of object 254 becomes dark.

 Consider the case where object 254 is not located in the center portion 252, as shown in Fig. 3C. In this case, most of central portion 252 becomes a high luminance
15 portion. The diaphragm will be operated towards the closing direction. This will also result in a very dark sensed image of object 254.

 The above-described object state is not so rare. This is often encountered when shooting at a ski resort,
20 for example. In this case, the background scenery is snow, which has a very high luminance. The state such as shown in Fig. 3C may often be seen in ski resorts.

 For users that often shoot at a ski gelände, optimum aperture value could not be obtained by automatic iris
25 control with the conventional center-weighted metering or



foot-weighted metering, resulting in an unsatisfactory picture. There are some video cameras that can adjust the diaphragm manually. However, it is very difficult to control the iris while shooting. There was a problem that control of the iris could not be carried out easily with the above-described conventional video camera.

A technique, not directed to a video camera, but to a still camera, is disclosed in Japanese Patent Laying-Open No. 2-96724 for controlling the camera according to the condition of the object.

Referring to Fig. 4, the still camera disclosed in Japanese Patent Laying-Open No. 2-96724 includes a lens 320, a diaphragm 319 provided in front of lens 320, and an in-focus mechanism 335 for focusing the optical image of an object at a predetermined image formation plane by moving lens 320 along the optical axis. The optical image of the object is provided to an amplifier 322 as data representing the luminance of the object for each photoelectric converted device by an image sensing device 321 formed of photoelectric conversion devices allocated in a matrix manner. The luminance information of the object amplified by amplifier 322 is A/D converted by A/D converter 323 to be provided to an operation circuit 324 as a stopped down luminance BV' . Operation circuit 324 is previously input with an aperture value AV_0 representing



the open aperture of diaphragm 319. Operation circuit 324 calculates and provides the actual luminance $BV (= BV' - AV_0)$ of the object from the two values of BV' and AV_0 . The output object luminance BV is provided to a multiplexer 328 and a frame memory 334. The operation of multiplexer 328 will be described afterwards.

Frame memory 334 stores object luminance BV according to the output of each photoelectric conversion device of image sensing device 321 for each photoelectric conversion device. Frame memory 334 is connected to a neuro-computer 325, to which the luminance information stored in frame memory 334 is supplied, and from which signal P_{xy} representing the position of the main object of the video stored in frame memory 334 is output. Neuro-computer 325 is connected to a coefficient memory 326 for storing coefficient W_{ji} to determine the operational process carried out by neuro-computer 325. Coefficient W_{ji} is rewritten to obtain an appropriate output corresponding to the input in the learning process of neuro-computer 325.

The output of neuro-computer 325 is connected to one input of a selector 336. The output of selector 336 is connected to multiplexer 328. The output of an operation panel 327 is connected to the other input terminal of selector 336. The output of operation panel 327 is also connected to neuro-computer 325. Operation panel 327 is



for the purpose of providing to neuro-computer 325 a
signal tp_i representing the location of the main object
from the picture stored in the frame memory 334 at the
time of the learning mode. Operation panel 327 includes a
5 touch panel switch (not shown), for example, having a one-
to-one correspondence to the position of the screen.

The user inputs the position of the main object on
the screen while looking through the finder, whereby
signal tp_i representing the position of the main object is
10 provided from operation panel 327 to neuro-computer 325.
Neuro-computer 325 carries out operation according to the
coefficient stored in coefficient memory 326 by the input
provided from frame memory 324 to temporarily determine an
output. Neuro-computer 325 also compares signal tp_i
15 provided from operation panel 327 with the its own output
to rewrite coefficient W_{ji} in coefficient memory 326 so
that the offset is minimized. By repeating such learning
several times, an artificial neural network implemented
with neuro-computer 325 and coefficient memory 326 is
20 self-organized to provide an appropriate signal P_{xy}
according to the input from frame memory 334.

The output of operation panel 327 is also provided to
selector 336. At the time of learning mode, selector 336
provides the output of operation panel 327 to multiplexer
25 328. At the time of automatic mode, selector 326 provides



the output of neuro-computer 325 to multiplexer 328.

Multiplexer 328 passes only output BV of the photoelectric conversion device corresponding to the main object of the screen designated by control signal P_x provided from

5 neuro-computer 325 or operation panel 327. The passed output BV is provided to operation circuits 329 and 331.

Operation circuit 329 carries out operation for focus-detection according to the so-called hill-climbing method based on luminance output BV of the photoelectric
10 conversion device corresponding to the main object. The output of operation circuit 329 is provided to a driver 330. Driver 330 moves in-focus mechanism 335 according to the supplied operation result to move lens 320 in its optical axis direction. By operation circuit 329, lens
15 320 stops at a location so that an image is formed on the light receiving plane of image sensing device 321.

Operation circuit 331 determines the shutter speed or the signal value according to luminance BV of the main portion of the object provided from multiplexer 328, film
20 sensitivity SV, aperture value AV of diaphragm 319, and the set shutter speed TV. The determined shutter speed and aperture value are provided to a shutter control device 332 and an iris control device 333, respectively.

Thus, the still camera disclosed in Japanese Patent
25 Laying-Open No. 2-96724 has the aperture value, the



shutter speed, the in-focus position determined according to the luminance information of not the entire object, but only the main portion of the object. The position of the main object is detected by neuro-computer 325. This is
5 carried out according to the learning process specified by the user through operation panel 327. Therefore, the detection of the position of the main portion of the object can be carried out similarly as to the liking of the user. It is described in the aforementioned Japanese
10 Patent Application that the main object can be shot under an appropriate shooting state by controlling the camera according to the luminance of the main object.

However, the technique of Japanese Patent Laying-Open No. 2-96724 is directed to a still camera. There is no
15 disclosure as to how this technique is applied to a video camera. Although it is suggested in Japanese Patent Laying-Open No. 2-96724 that it is possible to provide as a teacher signal an exposure correction signal according to the brightness or the luminance pattern of the object
20 to such a neuron-computer for carrying out learning such as retrogressive correction, no specific structure is taught.

The still camera described in the embodiment of the Japanese Patent Application which is controlled according
25 to the luminance information of only the main object has



the following problems which will be described hereinafter. The relation between the luminance information of the background scenery excluding the main object and the luminance signal of the main object is critical in obtaining an optimum aperture value. However, it is impossible to optimize the luminance balance between the main object and the background with the technique disclosed in Japanese Patent Laying-Open No. 2-96724. This is not a problem in practice for a silver salt camera represented by a still camera since luminance adjustment can be carried out at the time of printing. However, for a video camera, there is limitation in carrying out luminance adjustment at the time of reproduction. The technique disclosed in Japanese Patent Laying-Open No. 2-96724 cannot be applied to a video camera.

There is also another problem. It is general to use the output of the image sensing device required to obtain a video signal for the control of the diaphragm in a video camera. The number of photoelectric conversion devices that are allocated in the image sensing device is significant to comply with the high requirements of the picture quality. If the technique disclosed in Japanese Patent Laying-Open No. 2-96724 is applied to a video camera and the output of each photoelectric conversion device is stored in a frame memory to be provided to the



neuro-computer, the number of inputs for the neuro-computer will become too great and not appropriate for practical use. A great number of inputs for the neuro network will result in a problem of a longer time period
5 for the learning process of the neural network.

Furthermore, if the output of the photoelectric conversion device is directly input to the neural network, the input value to the neural network will vary greatly in response to just a slight change in position of the object,
10 resulting in an unstable operation of the neural network.

SUMMARY OF THE INVENTION

An object of the present invention is to provide a video camera that can automatically control the diaphragm to an aperture value that the user feels optimum according
15 to a specific state of the object.

Another object of the present invention is to provide an automatic camera that can control the diaphragm automatically and stably to an aperture value that the user feels optimum according to a specific state of the
20 object.

A further object of the present invention is to provide a video camera that can automatically control the diaphragm to an aperture value that the user feels optimum, and that can learn to realize automatic diaphragm
25 that the user feels optimum for various states.



Still another object of the present invention is to provide a video camera that can control automatically the diaphragm to an aperture value that the user feels optimum according to a specific state of the object, and that can
5 carry out stably and speedily learning for realizing automatic diaphragm that the user feels optimum for various conditions.

The video camera according to the present invention includes an optical system for gathering incident light
10 from an object for forming an image on a predetermined image formation plane, an iris and an iris driving circuit for regulating the incident amount of light in response to a supplied incident light amount regulating signal, an image sensor for image sensing an optical image of the
15 object formed in a predetermine frame for providing a luminance signal, and a luminance distribution characteristics extraction circuit connected to the image sensor to divide the frame in which the optical image is formed into a plurality of sub-areas for extracting the
20 luminance distribution characteristics of the optical image in the frame as the luminance for each sub-area according to the luminance signal and providing the same as a plurality of luminance distribution signals. The video camera includes a target value signal generating
25 circuit for generating a target value signal to determine



a target value of the incident light amount regulating
signal that can be arbitrary set by the user according to
the luminance distribution of the optical image, an
adaptive circuit using an artificial neural network to
5 which a plurality of luminance distribution signals are
input and from which an incident light amount regulating
signal obtained by a predetermined conversion is provided,
for adapting conversion so that the offset of the output
with respect to a provided teacher signal is minimized,
10 and a selecting circuit responsive to the operation of the
user for selecting one of the output of the target value
signal and the adaptive circuit for providing the same as
a teacher signal to the adaptive circuit.

The image formed by the optical system in the video
15 camera is converted into a luminance signal by the image
sensor. The frame of a picture is divided into plurality
of sub-areas, whereby the luminance for each sub-area is
extracted from the luminance signal as the characteristics
of the luminance distribution of the picture. The
20 extracted characteristics of the luminance distribution of
the picture is provided to the artificial neural network
of the adaptive circuit as a plurality of luminance
distribution signals. The artificial neural network
applies a predetermined conversion to the plurality of
25 luminance distribution signals to provide the same as an



incident light amount regulating signal. At the time of learning of the artificial neural network, the selecting circuit provides the target value signal set by the user as the teacher signal. The adaptive circuit has the
5 conversion carried out internally adapted so that the offset between the teacher signal and its own output is minimized. At the time of normal operation, the adaptive circuit processes the luminance distribution signals according to the conversion adapted during the learning
10 process to provide the same to the iris driving circuit. Therefore, the value of the incident light amount regulating signal at the time of normal operation takes a value according to the users preference which is learned based on various states. Optimum aperture can be obtained
15 automatically thereafter just by a slight manual operation even in special states which could not be obtained in the conventional automatic iris control. An optimum aperture value taking into consideration the luminance balance between the main object and the background can be obtained
20 in the artificial neural network, resulting in a picture having satisfactory balance in luminance all over the entire screen.

The luminance distribution characteristics extraction circuit in the video camera of a preferred embodiment of
25 the present invention includes an A/D converting circuit



for A/D (analog-digital) converting the video signal provided from the image sensor for providing the same as a luminance data indicating the luminance of the optical image, and an average luminance calculating circuit to
5 integrate luminance data for each sub-area by a predetermined time period for calculating the average luminance for each sub-area to provide the same as a luminance distribution signal.

In the video camera, the luminance data for each sub-
10 area of the screen is extracted, whereby the average luminance thereof is calculated for each sub-area to be provided to the adaptive circuit as the luminance distribution signal. Because the luminance is averaged for each sub-area, there is no great change in the
15 luminance distribution signal when there is a slight movement of the object. A stable automatic iris control can be realized. Furthermore, the number of inputs to the artificial neural network will not become too great.

The artificial neural network of the adaptive circuit
20 in the video camera according to another embodiment of the present invention includes: an intermediate layer having a plurality of neurons, each carrying out a predetermined conversion for a set of the luminance distribution signals for obtaining one output; an output layer having a
25 plurality of neurons, each carrying out a predetermined



conversion to the signal provided from the intermediate layer for obtaining one output; an output converting circuit for carrying out a predetermined conversion to the set of signals provided from the output layer for
5 obtaining a single incident light amount regulating signal; and a conversion updating circuit for adaptively updating the conversion carried out in the intermediate layer and the output layer according to the difference between the output of the output layer and the teacher
10 signal so that the offset thereof is minimized.

In this video camera, each neuron in the intermediate layer of the artificial neuron network carries out a predetermined conversion for the set of input luminance distribution signals for obtaining one output, which is
15 provided to each neuron of the output layer. Each neuron in the output layer carries out a predetermined conversion to the signal provided from the intermediate layer to obtain one output, which is provided to the output converting circuit. The output converting circuit carries
20 out a predetermined conversion to the set of output of the output layer to provide an incident light amount regulating signal. The conversion carried out in each converting circuit of the intermediate layer and the output layer is adaptively updated so that the difference
25 between the output of the output converting circuit and



the provided teacher signal is minimized. As a result,
the conversion carried out in each converting circuit is
updated according to each repetition of the learning
process of the artificial neural network, whereby learning
5 is carried out that realizes automatic diaphragm such that
the user feels optimum in various states.

Each neuron in the intermediate layer and the output
layer of the artificial neural network of the video camera
according to a further preferred embodiment of the present
10 invention, includes a weighting circuit for giving a
predetermined weight to each input signal, an adding
circuit for adding all the weighted signals, and a
function converting circuit for converting and providing
the output of the adding circuit according to a
15 predetermined monotone increasing function. The
conversion updating circuit includes a weight updating
circuit for recalculating and updating each weight so that
the root mean square of the offset is minimized according
to the offset between the output of the output converting
20 circuit and a teacher signal.

Each converting circuit of this video camera applies
a predetermined weight to the input thereof, followed by
addition and a predetermined function conversion to obtain
one output. This weighting value is sequentially
25 recalculated and updated by the weight updating circuit so

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that the root mean square of the offset between the teacher signal and the output of the neuron network is minimized. Thus, the so-called error backpropagation rule is realized, whereby a stable and rapid learning is carried out.

The foregoing and other objects, features, aspects and advantages of the present invention will become more apparent from the following detailed description of the present invention when taken in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram of a video camera having a conventional automatic iris control circuit.

Fig. 2 is a schematic diagram of a picture for describing center-weighted metering and foot-weighted metering.

Figs. 3A-3C are schematic diagrams showing conventional photometry methods.

Fig. 4 is a block diagram of a conventional still camera using neural network.

Fig. 5 is a block diagram showing an example of a video camera according to the present invention.

Fig. 6 is a schematic diagram showing an example of a divided screen.

Fig. 7 is a block diagram of an area dividing and



average producing circuit.

Fig. 8 illustrates waveform charts of horizontal area pulses for dividing the screen.

Fig. 9 illustrates waveform charts of vertical area
5 pulses for dividing the screen.

Fig. 10 is a block diagram of an integrating circuit.

Fig. 11 is a block diagram of a neural network.

Fig. 12 is a block diagram of a neuron of an output layer and a weight updating circuit.

10 Fig. 13 is a block diagram of a neuron of the output layer.

Fig. 14 is a graph showing the characteristics of a sigmoid function of conversion carried out in a function generator.

15 Fig. 15 is a block diagram of a weight calculating circuit for calculating the weight coefficient of the neuron of the output layer.

Fig. 16 is a block diagram of a neuron of the input layer and the weight updating circuit.

20 Fig. 17 is a block diagram of a neuron of the input layer.

Fig. 18 is a block diagram of a weight calculating circuit for calculating the weight coefficient of the neuron of the input layer.

25 DESCRIPTION OF THE PREFERRED EMBODIMENTS



Fig. 5 is a block diagram of a video camera according to an embodiment of the present invention. Referring to Fig. 5, the video camera includes lens 102 as the optical system for gathering the incident light from object 1 and forming an image on a predetermined image formation plane, a CCD image sensor 104 for image sensing the optical image of object 101 formed on a predetermined frame by lens 102 for converting the same into a video signal which is an electrical signal, an iris plate 103 for regulating the incident amount of light to image sensor 104, an iris motor 113 for driving iris plate 103 according to the provided incident light amount regulating signal, i.e. iris control voltage, a preamplifier 116 for amplifying the video signal provided from image sensor 104 to provide the same to a signal processing circuit, an automatic iris control circuit 117 responsive to the level of the video signal provided from preamplifier 116 for providing an iris control voltage to drive iris motor 113 according to an iris control pattern learned by the iris control process set manually by a user, and a manually operable iris control signal generating circuit 118 connected to automatic iris control circuit 117 for providing a teacher signal thereto in the learning of the automatic iris control pattern of automatic iris control circuit 117.

Automatic iris control circuit 117 includes an A/D



converter 105 for A/D (analog-digital) converting the
luminance component of the video signal amplified by
preamplifier 116, an area dividing and average producing
circuit 106 for dividing the screen into a plurality of
5 sub-areas and integrating the luminance data provided from
A/D converter 105 for a predetermined time period for each
sub-area for calculating the average luminance to provide
an average luminance signal for each sub-area, an
artificial neural network (referred to as ANN hereinafter)
10 107 responsive to the average luminance signal provided
from area dividing and average producing circuit 106 for
recognizing the pattern of the luminance distribution
pattern in the screen to carry out conversion conforming
to the iris control by the user obtained by learning for
15 providing in a digital value the optimum aperture size, a
D/A converter 108 connected to the output of ANN 107 for
D/A (digital-analog) converting the digital signal
representing an optimum aperture value provided from ANN
107, an adder 114 for adding and providing the offset
20 voltage representing the offset between the aperture value
which is the target value set by the user and the output
of ANN 107 provided from manual iris control signal
generating circuit 118, a comparator 112 for comparing the
output voltage of adder 114 and reference voltage 111 for
25 driving iris motor 113 according to the comparison output,

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and an A/D converter 115 for A/D converting the output of adder 114 for providing to ANN 107 a teacher signal for learning.

Manual iris control signal generating circuit 118 includes a variable resistor 110 connected between a predetermined power supply potential and ground potential, and a switch 109 having one input terminal 109a connected to variable resistor 110, the other input terminal 109b connected to ground potential, and the output terminal connected to one input of adder 114.

Referring to Fig. 6, area dividing and average producing circuit 106 divides screen 251 into 4×4 sub-areas of a_{00} - a_{33} , where each area is equal in size. Area dividing and average producing circuit 106 obtains the average luminance for each sub-region for a predetermined time period to provide the same as the luminance data of each sub-region.

Referring to Fig. 7, area dividing and average producing circuit 106 includes a plurality of integrating circuits 121 provided corresponding to the number of sub-areas for integrating luminance signal from one of the sub-areas of a_{00} - a_{33} (refer to Fig. 2) for a predetermined time period to provide to ANN 107 an integrated value, and an area dividing timing signal generator 122 for generating and providing to each integrating circuit 121



area diving timing signals v0-v3 and h0-h3 for allowing
integration of the luminance data from only the
corresponding area and a clear signal CL for clearing the
integration for each screen to provide the same to each
5 integrating circuit 121.

Regarding the area dividing timing signals, signals
h0-h3 are area pulses for indicating the time of dividing
screen 251 (Fig. 6) along the line in the vertical
direction. It is assumed that a video signal in the
10 waveform shown in Fig. 8(A) is provided to A/D converter
105 (Fig. 5). Referring to Fig. 8 (B), signal h0 attains
a high potential from the beginning of the video period of
each horizontal scanning period. This high potential of
signal h0 is maintained for 1/4 of the video period.
15 Signal h0 has a low potential in the remaining period.

Referring to Fig. 8 (C), signal h1 attains a high
potential in response to the fall of signal h0. Signal h1
maintains the high potential state for 1/4 of the video
period and attains a low potential at the remaining
20 period. Referring to Figs. 8 (D) and (E), signals h2 and
h3 attain a high potential in response to the fall of
signals h1 and h2, respectively, and maintain the high
potential state for 1/4 of the video period. Signals h2
and h3 are both held at a low potential in the remaining
25 period.



Signals v0-v3 are signals indicating the timing for dividing screen 251 along the line in the horizontal direction. It is assumed that the video signal has a waveform shown in Fig. 9 (A). Referring to Figs. 9 (B)-
5 (E), the vertical scanning period having the region including the effective video signal is divided into 4. Signals v0-v3 attain a high potential in the first, the second, the third and the fourth period, respectively, of the period divided into 4. Signals v0-v3 attain a low
10 potential in their respective remaining periods.

By combining one of signals h0-h3 and one of signals v0-v3, and integrating the luminance of the video signal when both signals attain a high potential, the luminance data can be obtained for one of the sub-areas a_{00} - a_{33} shown
15 in Fig. 6.

Integrating circuit (i, j) 121 for integrating luminance data of sub-area a_{ij} includes an adder 124 having one input connected to A/D converter 105, a register 123 for storing added data provided from adder 124, and a gate
20 circuit 125 for ANDing clock signal CLK provided from area dividing timing signal generator 122 (refer to Fig. 7) in synchronization with the input luminance data and area pulses v_i and h_j provided from area dividing timing signal generator 122 to control the storing timing of register
25 123. The output of register 123 is connected to one input



of adder 124. The added value of the stored contents of register 123 and the luminance data provided from A/D converter 105 is provided again to register 123 to be added. The clear terminal of register 123 is supplied
5 with clear pulse CL provided for each vertical period by area dividing timing signal generator 122. By clear pulse CL, the contents of register 123 is cleared for each screen.

In the present embodiment, screen 251 is divided into
10 equal sub-areas of a_{00} - a_{33} shown in Fig. 6. The number of luminance data sampled from each sub-area is identical. Therefore, the integrated value of the luminance data obtained by register 123 can be directly processed as the luminance average of the sub-area. The output 134 of
15 integrating circuit 121 is provided to ANN 107.

Fig. 11 is a block diagram schematically showing ANN 107. The operation mechanism of the neural network is described in, for example, "Using Neural Network for Pattern Recognition, Signal Processing and Knowledge
20 Process" Nikkei Electronics, August 10, 1987, No. 427 pp. 115-124), Neuro-Computer (by Toyohiko Kikuchi, published by OHM issued in 1990), and Artificial Neural Networks-Theoretical Concepts (ed. V. Vemuri, Washington D.C, IEEE Computer Press, 1988). The details of the operation
25 mechanism will not be described here.



Referring to Fig. 11, ANN 107 includes an intermediate layer 132 for applying a predetermined process to luminance average data 134 provided from area dividing and average producing circuit 106 for obtaining
5 an intermediate output, an output layer 136 to apply a predetermined conversion to the output of intermediate layer 132 for obtaining n normalized outputs, and n comparators 135 provided corresponding to each of output layer 136 for comparing the output of output layer 136
10 with 0.5 for providing an output of 1 or 0 according to the compared value. Intermediate layer 132 includes m neurons 131. Each is applied with all the luminance average data 134 branched by an input layer not shown.

Output layer 136 includes n neurons 137, each having
15 the output connected to the positive input terminal of the corresponding comparator 135 and the input connected to the output of all the neurons 131 in intermediate layer 132. In Fig. 11, neuron 137 of the output layer is designated a serial number of 0 to (n-1).

20 ANN 107 further includes an encoder 133 for converting the number into a 8-bit data allocated to the neuron 137 connected to the comparator having an output of 1 and providing the same to D/A converter 108, and a decoder 130 having the input connected to A/D converter
25 115 (refer to Fig. 5) for decoding the signal provided



from A/D converter 115 into 1 bit data of t0-t255 to provide the same as a teacher signal to output layer 136.

Each neuron 131 of intermediate layer 132 and each neuron 137 of output layer 136 serve to apply a
5 predetermined weight to each input signal and carrying out addition, followed by a predetermined function conversion to provide the same. Therefore, each of neurons 131 and 137 is provided with a circuit for carrying out weighting for each input and a circuit for sequentially updating the
10 weight according to data t0-255 provided from decoder 130. These circuits are not shown in Fig. 11 for the purpose of simplicity. These circuits will be described with reference to Figs. 12-15.

The number of neurons 137 in output layer 136 is set
15 to 2^a (a is an integer), for example 128 or 256, taking into consideration the encoding of a 8-bit data.

Each neuron 131 of intermediate layer 132 applies a predetermined weight to the input signal, followed by addition and conversion with a particular function.
20 Therefore, each neuron 131 is provided with a weighting circuit for applying weight to each input, and a circuit for updating each weight coefficient according to data for learning provided from output layer 136. These circuits are not indicated in Fig. 11 for the sake of simplicity.
25 These circuits will be described in detail with reference



to Figs. 16-18.

In the ANN shown in Fig. 11, the teacher signal provided from A/D converter 115 is decoded by decoder 130 into one bit signals of t_0 - t_{255} . The bit signal is
5 provided to output layer 136, whereby each weight coefficient is updated. Using the updated result, each weight coefficient of intermediate layer 132 is updated. In other words, the flow of data at the time of normal operation and that at the time of learning is opposite to
10 each other. Such a learning method is called error backpropagation rule.

Fig. 12 is a block diagram of neuron 137 of output layer 136 and a weight updating circuit 138 provided associated with neuron 137 for calculating and updating
15 the weight for output 139 (x_1 - x_m) from intermediate layer 132 provided to neuron 137. Weight updating circuit 138 is not shown in Fig. 11, as described before. Referring to Fig. 12, weight updating circuit 138 includes m weight calculating circuits 141 for carrying out the weight
20 calculation for each of input signals 139 (x_1 - x_m).

It is assumed that neuron 137 is allocated with number j . The output thereof is y_j . It is clear from Fig. 12 that weight calculating circuit 141 corresponding to the i -th input signal 139 (x_i) recalculates the weight
25 coefficient according to output y_j of neuron 137, 1 bit



signal t_j provided from decoder 130, input signal x_i , and the weight coefficient corresponding to input signal x_i included in neuron 137 for updating the weight coefficient in neuron 137. The output of weight calculating circuit 141 is provided to each neuron 131 of intermediate layer 132 to be used in updating the weight coefficient of the weight applying process carried out in each neuron 131.

Referring to Fig. 13, neuron 137 of output layer 136 includes m registers 143 for storing weight coefficients $w_{0j1}-w_{0jm}$ corresponding to respective input signals 139 (x_1-x_m), m multipliers 142 for multiplying respective input signals 139 (x_1-x_m) by weight coefficients $w_{0j1}-w_{0jm}$ stored in the corresponding register 143, an adder 144 for adding all the outputs of the m multipliers 142 for providing an output w_{j0} , and a function generator 145 for carrying out a predetermined conversion f to the output w_{j0} of adder 144 for providing an output 140 (y_j) as the converted result. Weight coefficient $w_{0j1}-w_{0jm}$ of each register 143 is updated by the corresponding weight calculating circuit 141. Each weight coefficient of $w_{0j1}-w_{0jm}$ is used in the process carried out in weight calculating circuit 141 for the next learning.

The characteristic of function generator 145 is represented as $y_j = f(w_{j0})$. Function f is a monotone non-decreasing function. In the present embodiment, a sigmoid



function taking the value of intervals (0, 1) is employed as function f. Sigmoid function is defined by the following equation (3). The operation of neuron 137 can be defined by the following three equations.

$$W_j = \sum_{i=1}^n W_{ji} \cdot X_i \quad \dots (1)$$

$$y_j = f(W_j) \quad \dots (2)$$

$$f(W_j) = \frac{1}{1 + \text{Exp}(W_j)} \quad \dots (3)$$

The above equation (3) representing the sigmoid function is shown in the graph of Fig. 14. It can be appreciated from the graph of Fig. 14 that 0 and 1 are approximated when w_j are $-\infty$ and $+\infty$, respectively. When $w_j = 0$, 0.5 holds. Function f is not limited to this sigmoid function, and can take any other function that is a monotone non-decreasing function.

Fig. 15 is a block diagram of weight calculating circuit 141 for updating the weight for the i-th input 139 (x_i) of neuron 137. One neuron includes n weight calculating circuits 141.

Weight coefficient w_{0ji} corresponding to the i-th input x_i carried out by weight calculating circuit 141 is calculated according to the operation indicated by the following equation with output y_j of the output layer and teacher signal t_j .



$$\Delta w_{ji} = \epsilon \cdot \delta_{0j} \cdot x_i \quad \dots (4)$$

$$\text{where } \delta_{0j} = (y_i - t_j) y_j (1 - y_j) \quad \dots (5)$$

ϵ : small positive real number

By adding Δw_{ji} to weight coefficient w_{ji} for the i -th
 5 input x_i of the j -th neuron 137 of the output layer, weight
 coefficient w_{ji} is updated. The updated weight
 coefficient w_{ji} of register 143 is multiplied by input x_i
 by multiplier 142 to be provided to adder 144 (refer to
 Fig. 13).

10 Fig. 15 is a block diagram of weight calculating
 circuit 141 for carrying out the weight calculation
 indicated by equations (4) and (5). Referring to Fig. 15,
 weight calculating circuit 141 includes an adder 146 for
 obtaining value $(y_j - t_j)$ from output y_j of neuron 137 and
 15 teacher signal t_j provided from decoder 130, an adder 147
 for generating value $(1 - y_j)$ from output y_j of neuron 137, a
 multiplier 148 for multiplying output y_j of neuron 137 by
 output $(y_j - t_j)$ of adder 136 by output $(1 - y_j)$ of adder 147
 for providing value δ_{0j} shown in equation (5), a multiplier
 20 150 for multiplying output δ_{0j} of multiplier 148 by the i -
 th input x_i by value $(-\epsilon)$ for calculating value Δw_{ji} shown
 in equation (4), and an adder 149 for obtaining the
 difference between weight coefficient w_{ji} of the i -th
 input x_i and output Δw_{ji} of multiplier 150 for storing the



same as weight coefficient w_{0j_i} in register 143.

Weight calculating circuit 141 further includes a multiplier 151 for multiplying output 80_j of multiplier 148 by weight coefficient w_{0j_i} provided from register 143 for providing the result to intermediate layer 132. The output $80_j \cdot w_{0j_i}$ of multiplier 151 is used for updating the weight coefficient in each neuron 131 of intermediate layer 132.

A small positive real number value ϵ is used in the operation carried out within weight calculating circuit 141. This value is obtained empirically so as to improve the convergence properties of each weight coefficient of the learning process.

Fig. 16 is a block diagram of weight updating circuit 129 for updating the weight coefficient of the conversion carried out at the i -th neuron 131 included in intermediate layer 132. Each neuron 131 of intermediate layer 132 is provided with one weight updating circuit 129.

Referring to Fig. 16, weight updating circuit 129 includes an adder 152 for producing value $(1-x_i)$ from output x_i of the i -th neuron 131, a multiplier 153 for producing value $x_i (1-x_i)$ by multiplying the output of adder 152 by the output of neuron 131, and k weight calculating circuits 154 for calculating the weight

coefficient for the weight applied in neuron 131 for each of the k input signals of b_1-b_k (luminance average data $a_{00}-a_{33}$). As shown in Fig. 16, each weight calculating circuit 154 carries out weight calculation according to data for correction $\delta O_i w_{0_{ii}} - \delta O_n w_{0_{ni}}$ provided from the neuron in output layer 136, the output of multiplier 153, the corresponding input 134, and the respective weight coefficient corresponding to the input included in neuron 131 for updating the weight coefficient.

Fig. 17 is a block diagram of the i -th neuron 131 of the plurality of neuron 131 in intermediate layer 132. Referring to Fig. 17, neuron 131 includes k registers 156 for storing each of weight coefficients $w_{1_{i1}}-w_{1_{ik}}$ corresponding to k inputs 134 (b_1-b_k), k multipliers 155 for multiplying the weight coefficient stored in the corresponding register 156 by each of inputs 134 (b_1-b_k), an adder 157 for taking the total sum of the outputs of the k multipliers, and a function generator 158 for obtaining output x_i by carrying out conversion indicated by the aforementioned sigmoid function f for output w_i of adder 157.

As shown in Fig. 17, weight coefficient $w_{1_{i1}}-w_{1_{ik}}$ stored in each register 156 is updated by weight calculating circuit 154 (Fig. 16). Each weight coefficient $w_{1_{i1}}-w_{1_{ik}}$ is supplied to weight calculating





circuit 154 for respective updating.

Fig. 18 is a block diagram of weight calculating circuit 154 for calculating the weight coefficient for each input. The number of weight calculating circuits 154 corresponds to the number of inputs for neuron 131. The weight calculating circuit shown in Fig. 18 is a circuit for calculating weight coefficient $w_{i,p}$ for the p -th input b_p for the i -th neuron 131. Referring to Fig. 18, weight calculating circuit 154 includes n multipliers 161 for multiplying data $\delta_{01}w_{01i} - \delta_{0n}w_{0ni}$ provided from output layer 136 by value $x_i (1-x_i)$ provided from multiplier 160, an adder 162 for taking the total sum of the outputs of the n multipliers 161 to obtain output δ_{1i} , a multiplier 163 for calculating the product of the p -th input b_p , output δ_{1i} of adder 162 and the small positive real number ϵ to obtain output $\Delta w_{i,p}$, and an adder 164 for taking the difference between weight coefficient $w_{i,p}$ stored in register 156 and the output of multiplier 163 for updating weight coefficient $w_{i,p}$ of register 156 according to the result thereof.

The update process of weight coefficient $w_{i,p}$ by weight calculating circuit 154 is calculated by adding $\Delta w_{i,p}$ obtained by the following equation to weight coefficient $w_{i,p}$.

$$\Delta w_{i,p} = -\epsilon \cdot \delta_{1i} \cdot b_p \quad \dots (6)$$

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$$\text{where } \delta l_i = \sum_{j=1}^n \delta o w_{ji} (1 - x_i) \quad \dots (7)$$

The structure of ANN 107 is as follows. Referring to
5 Fig. 5 again, ANN 107 has the circuit configuration
adapted by learning to obtain an output more represented
by a teacher signal, by updating weight coefficient w_{0ji} (j
 $= 1$ to n , $i = 1$ to m), w_{lji} ($j = 1$ to m , $i = 1$ to k) in
each of neurons 131 and 137 according to the teacher
10 signal provided from A/D converter 115 for various inputs
provided from area dividing and average producing circuit
106.

Switch 109 carries out selection of learning mode and
normal operation mode of ANN 107, in addition to providing
15 an offset value between the output of ANN 107 and the
aperture value desired by the operator for providing a
teacher signal to ANN 107 at the time of learning mode.
The connection of switch 109 to terminal 109b causes
voltage 0 to be applied to adder 114. Therefore, the
20 output of D/A converter 108 is directly provided to
comparator 112. When switch 109 is connected to terminal
109a, a voltage predetermined by variable resistor 110 is
applied to adder 114. This means that the output of adder
114 is offset from the output of D/A converter 108 by the
25 voltage determined by variable resistor 110. By adjusting
variable resistor 110 to obtain a diaphragm the user feels

optimum, a teacher signal is provided to ANN 107 via adder 114 and A/D converter 115. The update of each weight coefficient of ANN 107 is carried out according to this teacher signal.

5 The above-video camera operates as follows. Lens 102 gathers incident light from object 101 to form an optical image of the object on the light receiving plane of image sensor 104. The amount of light entering image sensor 104 is regulated by iris plate 103. Image sensor 104 converts
10 the optical image in the frame of a predetermined screen into a video signal which is an electrical signal by photoelectric conversion. The video signal is provided to preamplifier 116 to be amplified. The amplified signal is provided to a signal processing circuit not shown to be
15 converted into the system of television signal. The video signal provided from preamplifier 116 is also supplied to A/D converter 105 of automatic iris control circuit 117.

 A/D converter 105 converts the luminance value of the input video signal into a digital value. This digital
20 value is provided to area dividing and average producing circuit 106. The luminance data of the video signal provided to area dividing and average producing circuit 106 is the line sequential signal obtained by raster-scanning the two dimensional image formed on the light
25 receiving plane of image sensor 104.

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Referring to Fig. 7, area dividing and average producing circuit 122 generates area pulses h_0-h_3 and v_0-v_3 such as those shown in Figs. 8(B)-(E) and Figs. 9(B)-(E), respectively. The area pulse is provided to
5 integrating circuit 121. Area dividing and average producing circuit 122 generates and provides to each integrating circuit 121 a clear signal CL for clearing the integration of the luminance data for each vertical scanning area.

10 Referring to Fig. 10, integrating circuit 121 for integrating the luminance data of the video signal generated from sub-area a_{ij} shown in Fig. 6 operates as follows. Gate circuit 125 takes the AND of clock signal CLK in synchronization with the input luminance data,
15 vertical area pulse v_i , horizontal area pulse h_j to provide the same to register 123. Register 123 is supplied with clock signal CLK only when both area pulses of v_i and h_j are at the high potential. The luminance data provided from A/D converter 105 is added with the data in register
20 123 by adder 124 to be provided to register 123. Register 123 stores the output of adder 124 in synchronization with the clock signal provided from gate circuit 125. By repeating the above-described operation, the luminance data of the video signal generated from sub-area a_{ij} are
25 integrated in register 123.

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Clear signal CL is provided from area dividing and average producing circuit 122 (refer to Fig. 7) to register 123 for each vertical period. In response to clear signal CL, the integrated value in register 123 is
5 cleared for each one screen, whereby the luminance integrated value of sub-area a_{ij} is provided as the output of register 123 for each screen.

As described above, the area of each sub-area a_{ij} shown in Fig. 6 is equal to each other, with the number of
10 luminance data provided from a sub-area also equal in number. Therefore, the integrated value provided from register 123 is proportional to the luminance average of the sub-area. The integrated value of the luminance data provided from register 123 can be taken as the luminance
15 average of each sub-area. As will be described afterwards, each luminance average is given weight according to learning in ANN 107. It is therefore considered that each sub-area is not necessarily equal to each other and that the luminance average of each sub-area
20 is not necessarily required to be used for iris control. However, it is considered that the learning speed of recognition pattern by ANN 107 is improved with equal areas of each sub-area, as in the present embodiment.

Referring to Fig. 11, luminance average data 134
25 provided from each integrating circuit 121 shown in Fig. 7



is branched in an input layer not shown of ANN 107 to be provided to each neuron 131 of intermediate layer 132. Each neuron 131 carries out a predetermined conversion to the input luminance average data 134. This result is
5 provided to each neuron 137 of output layer 136. The operation of each neuron 131 will be described later.

Each neuron 137 of output layer 136 carries out a predetermined conversion to the data provided from each neuron 131 of intermediate layer 132. The converted data
10 is provided to the positive input terminal of each corresponding comparator 135. The operation of each neuron 137 will also be described later.

Each comparator 135 compares the output of the corresponding neuron 137 with a reference value of 0.5.
15 If the output of neuron 137 is 0.5 or above, 1 is provided to encoder 133. If less than 0.5, 0 is provided to encoder 133. Encoder 133 responds to the output of each comparator 135 to generate as a 8-bit data the number of the neuron 137 corresponding to the comparator 135 that
20 provides 1. The generated 8-bit data is provided to D/A converter 108 (refer to Fig. 6).

In normal operation, switch 109 is connected to terminal 109b. Voltage adder 114 is provided with the output of ANN 107 that has been converted into an analog
25 value by D/A converter 108, and voltage 0. Therefore, the



output of voltage adder 114 is a signal having the digital output of ANN 107 converted into an analog data.

The output of voltage adder 114 is provided to the negative input terminal of comparator 112. Comparator 112 compares reference voltage 111 with the output of voltage adder 114 to control iris motor 113 according to the comparison result. Accordingly, iris motor 113 operates to open iris plate 103 when determination is made that the picture is dark, and operates to close iris plate 103 when determination is made that the picture is too bright, from the result of the pattern recognition by ANN 107.

At the time of normal operation, the output of voltage adder 114 is the analog data provided from ANN 107. If this data is digitalized by A/D converter 115 and provided as teacher signal to ANN 107, update process of weight in ANN 107 will not be carried out since the teacher signal and the output of ANN 107 coincide with each other.

Referring to Fig. 17, neuron 131 of intermediate layer 132 carries out the following operation for the input luminance average data 134. Each multiplier 155 multiplies the input luminance average data 134 ($b_p: p=1$ to k) by weight coefficient $w_{11}-w_{1k}$ stored in each register 156 to provide the output to adder 157. Adder 157 obtains the total sum w_1 of luminance average data b_1-b_k given





weight according to weight coefficients $w_{l_{i1}}-w_{l_{ik}}$ by multiplier 155. The total sum w_l is provided to function generator 158, where conversion according to sigmoid function shown in Fig. 14 is carried out. The converted
 5 result is provided as intermediate layer output 139 (x_i).

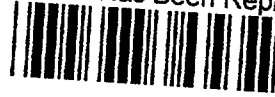
Each neuron 131 of intermediate layer 132 carries out the above-described process to provide outputs x_1-x_m to each neuron 137 in output layer 136.

Referring to Fig. 13, the j -th neuron 137 of the
 10 plurality of neurons 137 in output layer 136 operates as follows. Outputs x_1-x_m from each neuron 131 of intermediate layer 132 are provided to the j -th neuron 137. Each multiplier 142 in neuron 137 multiplies output 139 (x_1-x_m) of intermediate layer 132 by the corresponding weight
 15 coefficient $w_{0_{j1}}-w_{0_{jm}}$ to provide the same to adder 144. Adder 144 provides the total sum w_{0_j} of the outputs of each multiplier 142 to function generator 145. Function generator 145 carries out sigmoid conversion shown in Fig. 14 for output w_{0_j} of adder 144 to obtain output y_j . Output
 20 y_j is provided to the corresponding comparator 135 (refer to Fig. 11).

A similar process is carried out in all the neurons 137 in output layer 136, whereby respective outputs y_1-y_n are provided to the corresponding comparator 135.

25 More briefly, an output for controlling the operation

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of iris plate 103 is obtained as the output of ANN 107,
according to weighting and function conversion carried out
in each neuron 131 of intermediate layer 132, the
weighting and function conversion carried out in each
5 neuron 137 of output layer 136, and the encoding of the
number of the neuron of output layer 136 which outputs 1.
Each weight is recalculated by the learning process of ANN
107 according to a teacher signal that will be described
afterwards, and updated appropriately. Thus, iris control
10 is carried out automatically reflecting for the learning
based on the teacher signal as the output of ANN 107.

The operation of the video camera in the learning
process of ANN 107 will be described hereinafter. This
learning employs error backpropagation rule, as mentioned
15 before, to update each weight coefficient according to
equations (1)-(7). It is known that an output closely
matching the teacher signal is obtained as the output of
the neural network using the learning according to error
backpropagation rule. It is also known that a learned
20 neural network provides an output closely conforming to
the liking of the user who provided the teacher signal
even for inputs that are not yet learned.

The learning process is executed by switching switch
109 to terminal 109a when the user makes determination
25 that the aperture value obtained using ANN 107 is not

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optimum. The user adjusts the resistance of variable resistor 110 to obtain an optimum diaphragm. The voltage obtained by variable resistor 110 is applied to one terminal of voltage adder 114. This means that the output
5 of voltage adder 114 is the analog value of the output of ANN 107 added with the voltage regulated by variable resistor 110. In other words, the output of voltage adder 114 has an offset with respect to the output of ANN 107. The iris control voltage provided from adder 114 is
10 converted into a digital value by A/D converter 115 to be applied to ANN 107 as a teacher signal of, for example, 8 bits.

Referring to Fig. 11, decoder 130 decodes the digital data of 8 bits provided from D/A converter 115 to generate
15 256 of one bit teacher signals of t_0 - t_{255} . Each teacher signal is provided to the corresponding neuron 137 of output layer 136. One of the teacher signals of t_0 - t_{255} take a value of 1, while the other remaining teacher signals all take a value of 0.

20 It is assumed that a luminance average data 134 is provided from area dividing and average producing circuit 106 from ANN 107 in the learning process. The output of output layer 136 is represented as a vector of 256 dimensions expressed by equation (8).

25 $(Y_0, Y_1, \dots, Y_{255}) \dots (8)$



where y_j takes a value of 0-1.

It has been mentioned that the learning process is carried out according to error backpropagation rule. In other words, the update of the weight coefficient is carried out from the stage of output layer 136 to intermediate layer 132 which is the direction opposite to that of the input signal. Therefore, the process carried out in each neuron 137 of output layer 136 will first be described hereinafter.

Referring to Figs. 12-15, weight coefficient w_{0ji} corresponding to the i -th output x_i provided from intermediate layer 132 of the j -th neuron 137 of output layer 136 is updated as follows. With particular reference to Fig. 15, value $(y_j - t_j)$ is produced using output y_j of the j -th neuron 137 and the j -th teacher signal t_j by adder 146. Value $(1 - y_j)$ is produced by adder 147. Multiplier 148 multiplies the outputs of adders 146 and 147 by output y_j of neuron 137 to produce value δ_{0j} , expressed by equation (5).

Multiplier 150 produces and provides to adder 149 the value Δw_{0ji} expressed by equation (4) by multiplying the provided small positive real number (threshold value) ϵ by output δ_{0j} of multiplier 148 by the i -th input x_i . Adder 149 calculates and provides to register 143 a new weight coefficient w_{0ji} by reducing output Δw_{0ji} of multiplier 150

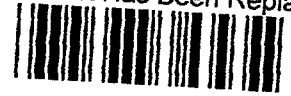


from weight coefficient w_{0ji} stored in register 143. The update of the weight coefficient of neuron 137 is completed by the new weight coefficient w_{0ji} being stored in register 143.

5 In multiplier 151 of weight calculating circuit 141, output 80_j of multiplier 148 is multiplied by weight coefficient w_{0ji} to produce value $80_j \cdot w_{0ji}$ which is provided to intermediate layer 132. This value is used for updating the weight coefficient in the intermediate layer.

10 Referring to Figs. 16-18, the update process of weight coefficient carried out in the i -th neuron 131 of intermediate layer 132 is carried out as follows. It is assumed that output x_i is obtained as the output of the i -th neuron 131 for the input luminance average data 134 (b_1 -
15 b_k). With reference particularly to Fig. 18, adder 159 produces value $(1-x_i)$ from output x_i of neuron 131 to provide the same to multiplier 160. Multiplier 160 produces value $x_i (1-x_i)$ from output x_i of neuron 131 and the output of adder 159. This value is provided to each
20 weight calculating circuit 154.

The update process of weight coefficient w_{1ip} for the p -th luminance average data b_p of the i -th neuron 131 is carried out as follows. With particular reference to Fig. 14, weight calculating circuit 154 is provided with
25 learning data $80_1 w_{01i}$, $80_2 w_{02i}$, ..., $80_n w_{0ni}$ from each neuron



137 of output layer 136. In other words, each weight
calculating circuit 154 of the i -th neuron x_i is provided
with data $\delta 0_j \cdot w 0_{ji}$ ($j=1$ to n) provided from the weight
calculating circuit of the weight coefficient for the i -th
5 input to each neuron 137, out of the data from each neuron
137 of output data layer 136.

Each multiplier 161 multiplies the corresponding
input $\delta 0_1 w 0_{1i} - \delta 0_n w 0_{ni}$ by output $x_i (1-x_i)$ of multiplier 160
to provide the same to adder 162. Adder 162 takes the
10 total sum of the outputs of multiplier 161 to produce
value $\delta 1_i$ expressed by equation (7). This value is
provided to multiplier 163.

Multiplier 163 multiplies the provided small positive
real number ϵ by output $\delta 1_i$ of adder 162 by the p -th
15 luminance average data b_p to produce and provide to adder
164 $\Delta w 1_{ip}$ expressed by equation (6). Adder 164 reads out
the value of the corresponding weight coefficient $w 1_{ip}$ from
register 156 to carry out operation with output $\Delta w 1_{ip}$ of
multiplier 163 to produce a new weight coefficient $w 1_{ip}$
20 which is provided to register 156. The update of weight
coefficient $w 1_{ip}$ for the p -th luminance average data b_p of
the i -th neuron 131 of intermediate layer 132 is completed
by a new weight coefficient $w 1_{ip}$ being stored in register
156.

25 One cycle of learning is completed by carrying out

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the above described update process of weight coefficient for all registers 143 in each neuron 137 of output layer 136, and for all registers 156 in each neuron 131 of intermediate layer 132.

5 Thus, ANN 107 is made adaptive to provide a more optimum diaphragm for the object specified by the user from variable resistor 110 by repeating the learning of all weight coefficients in ANN 107 for various teacher signals.

10 Even in the case of an object state where optimum aperture value can not be obtained in conventional automatic iris control using center-weighted metering and foot-weighted metering, adjustment of the diaphragm is carried out automatically conforming to the usage
15 condition by adjusting manually the diaphragm a few times. The disadvantage of having to adjust manually the diaphragm frequently encountered in a video camera just having a conventional manual iris control is eliminated to realize a video cameral having automatic iris control
20 conforming to the usage purpose of the operator.

 The network is input with the luminance of all angles of view, and a value selected by the operator taking into consideration the balance of luminance of the main object and the background scenery is provided as the teacher
25 signal to the network. According to this network, an iris

control signal is obtained for appropriately adjusting the luminance balance of the main object and the background for all angles of view.

5 In the video camera that carries out automatic iris control using the above-described neural network, the output of the neural network is always fed back to the iris driving portion even when the operation mode is switched to the manual iris control mode by switch 109. There is possibility of an unstable iris control if the
10 weight coefficient in ANN 107 is constantly updated. To avoid such a problem, only calculation of updating the weight coefficient of the neurons is carried out at the time of manual iris control operation mode, wherein the contents of each weight coefficient register in the neuron
15 is updated when the operation mode is switched from manual to automatic iris control. This prevents the generation of an unstable state of iris control occurring from the update of the coefficients.

The values learned by center-weighted metering and
20 foot-weighted metering is previously written in as the initial value of the weight coefficient in ANN 107. Therefore, the problem does not occur of the operation of the video camera becoming unstable when the video camera is used with automatic iris control right after the
25 purchase of the video camera.

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In the above described embodiment, conversion was carried out so that the value of outputs y_j , x_i becomes 0.5 when the values of w_{0j} , w_{1i} supplied to the function generator is exactly 0 by the function conversion carried out in each neuron. Each neuron should be implemented as follows if the input value to the function generator is a value other than 0 (threshold value) for an output value of 0.5. One terminal that inputs a fixed value of "1" is added to each neuron. A new weight coefficient is defined for this input, whereby this weight coefficient is multiplied by fixed value "1" to be provided to adders 144 and 157. The characteristics of each of function generators 145 and 158 is that shifted by the threshold value in the left and right directions of the graph of Fig. 10. This allows the provision of a threshold value for each neuron. This weight coefficient may be updated any time by the learning process.

Although the present invention has been described and illustrated in detail, it is clearly understood that the same is by way of illustration and example only and is not to be taken by way of limitation, the spirit and scope of the present invention being limited only by the terms of the appended claims.

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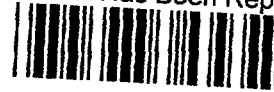


5 There are described above novel features which
the skilled man will appreciate give rise to
advantages. These are each independent aspects of the
invention to be covered by the present application,
irrespective of whether or not they are included
within the scope of the following claims.

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CLAIMS:

1. A video camera comprising:

an optical system (102) for gathering the incident light from an object to form an image on a predetermined image formation plane,

5 incident light amount regulating means (103, 113) responsive to a provided incident light amount regulating signal for regulating said incident light amount,

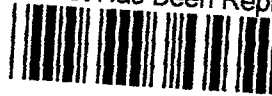
image sensing means (104, 116) for image sensing the optical image of the object within a predetermined frame
10 (251) formed by said optical system (102) to provide a luminance signal,

luminance distribution characteristics extraction means (105, 106) connected to said image sensing means to divide said frame (251) into a predetermined plurality of
15 sub-areas for extracting the characteristics of the luminance distribution of said optical image in said frame (251) as the luminance for each said sub-area according to said luminance signal to provide the same as a plurality of luminance distribution signals,

20 means for generating a target value signal for determining the target value of said incident light amount regulating signal that can be arbitrarily set by an user according to said luminance distribution of said optical



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image,

25 adaptive means (107, 108, 115) using an artificial
neural network (107) to which said plurality of luminance
distribution signals are input and from which said
incident light amount regulating signal by a predetermined
conversion is output for making adaptive said conversion
30 so that the offset is minimized between a provided teacher
signal and the output of said artificial neural network
(107), and

manually operable selecting means responsive to the
operation of the user for selecting either said target
35 value signal or the output of said adaptive means (107,
115) which is provided as said teacher signal to said
adaptive means.

2. The video camera according to claim 1, wherein
said luminance distribution characteristics extraction
means (105, 106) comprises

5 A/D converting means (105) for A/D converting said
luminance signal provided from said image sensing means
(104, 116) to provide the same as a digital luminance data
indicating the luminance of said optical image, and

average luminance calculating means (106) to
integrate said luminance data for a predetermined time
10 period for each said sub-area for calculating an average



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luminance for each said sub-area to provide the same as said luminance distribution signals.

3. The video camera according to claim 2, wherein said average luminance calculating means (106) comprises area dividing signal generating means (122) for producing a plurality of horizontal dividing signals h0-h3 taking a predetermined first value only during respective predetermined time periods in the horizontal scanning period of each of said luminance signals and taking a second value different from said first value during the period other than said predetermined time period of said horizontal scanning period of each of said luminance signals, and a plurality of vertical dividing signals v0-v3 taking a predetermined third value only during respective predetermined time periods in the vertical scanning period of each of said luminance signals and taking a fourth value different from said third value during the time period other than said predetermined time period of the vertical scanning period of each of said luminance signals, and for generating a clear pulse CL for each of said integrating period, and a plurality of integrating means (121) each connected to the output of said A/D converting means (105), and responsive to a predetermined one of said horizontal





dividing signals h0-h3 taking said first value, and a
predetermined one of said vertical dividing signals v0-v3
25 taking said third value to sample said luminance data at a
predetermined timing for integrating the same during said
integrating period for providing an integrated result.

4. The video camera according to claim 3, wherein
each of said integrating means (121) comprises

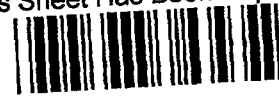
gate means (125) connected to receive a predetermined
clock signal CLK, said one of said horizontal dividing
5 signals, and said one of said vertical dividing signals
for passing said clock signal only when said one of said
horizontal dividing signals takes said first value and
said one of said vertical dividing signals takes said
third value,

10 holding means (123) responsive to said clock signal
CLK gated by said gate means (125) for holding and
providing the value of an input digital signal, and for
clearing the integrated result in response to said clear
pulse CL, and

15 digital adding means (124) connected to the output of
said holding means (123) and to the output of said A/D
converting means (105) for adding and providing to said
holding means (123) the value of the digital signal
provided from said holding means (123) and said luminance



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data.

5. The video cameral according to claim 3, wherein said horizontal dividing signals h0-h3 alternately take said first value.

6. The video camera according to claim 5, wherein the time length of said horizontal dividing signals h0-h3 taking said first value is equal to each other.

7. The video camera according to claim 5, wherein said vertical dividing signals v0-v3 alternately take said third value.

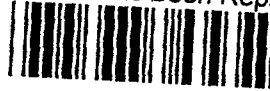
8. The video camera according to claim 7, wherein the time length of said vertical dividing signals v0-v3 taking said third value is equal to each other.

9. The video camera according to claim 7, wherein the time length of said horizontal dividing signals h0-h3 taking said first value is equal to each other.

10. The video camera according to claim 9, wherein the time length of said vertical dividing signals v0-v3 taking said third value is equal to each other.



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11. The video camera according to claim 1, wherein said artificial neural network (107) of said adaptive means (107, 108, 115) comprises

an intermediate layer (132) including a plurality of
5 converting means (131), each for obtaining one output by carrying a predetermined conversion to said luminance distribution signals (134),

an output layer (136) having a plurality of
10 converting means (137), each for obtaining one output by carrying a predetermined conversion to the output of said intermediate layer (132),

output converting means (133, 135) for carrying out a
predetermined conversion to the output of said output
layer (136) to obtain a single said incident light amount
15 regulating signal, and

conversion updating means (129, 130, 138, 141) for
updating adaptively according to the offset between the
output of said artificial neural network and said teacher
signal said conversion being carried out in each of said
20 converting means (131, 137) in said intermediate layer
(132) and said output layer (136) so that said offset is
minimized.

12. The video camera according to claim 11, wherein each of said converting means (131, 137) of said



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intermediate layer (132) and said output layer (136)
comprises

5 weighting means (142, 143, 155, 156) for carrying out
a predetermined weighting to each of the input signal,
 adding means (144, 157) for adding all the signals
given weight by said weighting means (142, 143, 155, 156),
and

10 function converting means (145, 158) for converting
the output of said adding means (144, 157) according to a
predetermined monotone increasing function,

 wherein said conversion updating means (138, 129)
comprises weight updating means (141, 154) for updating
15 the weight of each of said weighting means (142, 143, 155,
156), according to an offset between the output of said
output converting means (135, 133) and said teacher
signal, by recalculating each of said weighting so that
the root mean square of said offset is minimized.

13. The video camera according to claim 1, wherein
said means for generating said target value signal
comprises

 manually operable offset generating means (109a, 110)
5 for generating an arbitrary offset for the output of said
artificial neural network (107), and

 adding means (108, 114) for generating said target





value signal by adding said offset provided from said
offset generating means (118) with the output of said
10 artificial neural network (107).

14. The video camera according to claim 13, wherein
said selecting means comprises

zero offset generating means (109b) for generating
zero offset, and

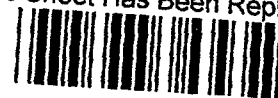
5 switching means (109) inserted between said offset
generating means (109a, 110) and the input of said adding
means (114) for selecting and providing to the input of
said adding means (114) either the output of said offset
generating means (109a, 110) or the output of said zero
10 offset generating means (109b) in response to the manual
operation of the operator.

15. The video camera according to claim 13, wherein
the output of said artificial neural network (107,
108) comprises an analog signal,

said offset generating means (109a, 110) comprises
5 offset voltage generating means (109a, 110) for generating
an arbitrary offset voltage, and

said adding means (108, 114) comprises voltage adding
means (114) for adding said offset voltage generated by
said offset voltage generating means (109a, 110) to said





10 analog signal provided from said artificial neural network
(107, 108).

16. The video camera according to claim 15, wherein
said selecting means comprises

zero offset voltage generating means (109b) for
generating zero offset voltage, and

5 switching means (109) inserted between the output of
said offset voltage generating means (109a, 110) and the
input of said voltage adding means (114) for selecting and
providing to the input of said voltage adding means (114)
either said offset voltage or said zero offset voltage in
10 response to the manual operation of the operator.

17. The video camera according to claim 16, wherein
said offset voltage generating means (109a, 110) comprises
voltage dividing means (109a, 110) connected between
predetermined first and second potentials for dividing the
5 potential difference between said first and second
potentials to provide an arbitrary potential.

18. The video camera according to claim 15, wherein
said means for generating said target value signal
comprises quantizing means (115) for quantizing the output
of said voltage adding means to one of a predetermined





plurality of discrete values.

19. The video camera according to claim 18,
wherein said quantizing means (115) comprises
digitizing means (115) for digitizing the output of
5 said voltage adding means (114) into a binary signal
of a predetermined number of bits.

20. A video camera comprising:
an objective optical system (102);
light control means (103) for controlling the
10 amount of light entering the camera and a driving
circuit (113) therefor;

an image sensor (104) for sensing an image formed
by the objective from the controlled light;

15 a circuit (105, 106) for processing a signal
derived from the output of said image sensor so as to
divide the image into a plurality of sub-areas and to
provide a luminance distribution signal representing
the luminance distribution in said divided image;

20 a circuit (114) for generating a signal defining
a target value of a driving signal for the light
control means;

an adaptive circuit (107) to which the luminance
distribution signal is input for carrying out adaptive
conversion so as to minimise the offset between a



provided teacher signal and its own output, and a switch (109) for selecting either the target value signal or the output of the adaptive circuit to provide the same as a teacher signal to the adaptive circuit.

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