

SYSTEM AND METHOD FOR REDUCING POWER CONSUMPTION IN AN ELECTRONIC CIRCUIT

ABSTRACT

While dispatch circuitry (74) operates in a first power mode, per cycle of the dispatch circuitry (74), up to N number of instructions are dispatched to execution circuitry for execution, where N is an integer number and $N > 1$. While the dispatch circuitry (74) operates in a second power mode, per cycle of the dispatch circuitry (74), up to M number of instructions are dispatched to the execution circuitry for execution, where M is an integer number and $0 < M < N$.

Fig. 2