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THOMAS & BETTS LEGAL DEPT.

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No. 9241 P. 4/12

**IMPROVEMENTS IN OR RELATING TO
ELECTRICAL CONNECTION APPARATUS**

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FIELD OF THE INVENTION

5 The invention relates to electrical connection apparatus and more particularly but not exclusively to pin grid array (hereinafter referred to by the usual term of art acronym PGA) sockets which are used for connection with, for example, central processing unit (CPU) chips which normally have in excess of 144 pins.

10 **BACKGROUND OF THE INVENTION**

15 A problem arises in connectors with a large number of pins, for example in PGA sockets for connection of CPU chips, in that forces involved in inserting the pins into the sockets are high; this can lead both to difficulty in engagement and to damage. There is also a trend towards increasing the number of pins; for example a 586 chip has in excess of 200 pins.

20 Attempts have been made to reduce the insertion force of pins into PGA sockets, for example by arranging socket contacts in two staggered rows. This will reduce insertion force by approximately 50% compared to unstaggered socket contacts. An alternative approach has been to design socket contacts with staggered contact points, such that a pin does not start engagement with both contact arms at the same time, thereby reducing the insertion force. However, such alternatives have not succeeded in reducing insertion force adequately and this invention addresses this problem.

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SUMMARY OF THE INVENTION

5 According to the invention, there is provided a socket
connector for connection to a plurality of pins, which
socket connector has a plurality of socket contacts for
engagement by the plurality of pins, each socket contact
having a pair of contact arms, each arm having a profile
to provide a contact points of each contact being
10 staggered such that, in use, a particular pin does not
make initial contact with both the contact points of the
associated contact simultaneously, and adjacent contacts
being positionally staggered with respect to each other
such that, in use, the associated pair of adjacent pins
does not make initial contact simultaneously with more
than one contact point.

15 The staggering of the contact points is preferably such
that a first contact is made between a first pin of the
adjacent pair and one contact point of the contact
associated with the first pin, and the next contact is
made between the second pin of the adjacent pair and one
20 contact point of the contact associated with the second
pin.

The third contact is preferably made between the first
pin and the other contact point of the contact
associated with the first pin, and the fourth contact is
25 preferably between the second pin and the other contact
point of the contact associated with the second pin.

Each contact arm preferably has an end portion extending
from the contact point to the free end, which end
portion lies obliquely to the direction of pin travel to
30 each the entry of a pin into the associated contact.

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Each contact preferably has a base, the contact arms extending from the base in cantilever fashion. Each contact may be formed from sheet material. The bases of the contacts may be coplanar, and different contact point spacing from the base plane may be achieved by different lengths of the contact arms.

BRIEF DESCRIPTION OF THE SOLE DRAWING FIGURE

By way of example, one embodiment of a socket connector according to the invention will now be described with reference to the accompanying drawing, which is a sectional side view of a pair of adjacent contacts in an array of contacts.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

A CPU chip is illustrated at 10 and only a small portion is shown from which two pins 11, 12 extend. As discussed previously, a CPU chip has a large number of pins, typically more than 144 and, with further developments in chips, in excess of 200 pins.

Part of a pin grid array (GPA) socket is shown at 13, the socket 13 having a body portion 14 having contact recesses 15, 16 and pin engagement holes 17, 18. It will be appreciated that the number of contact recesses and engagement holes will correspond to the number of pins in the CPU chip.

As can be seen clearly in the drawing, the contact recesses 15 and 16 are of different depth, the contact recess 16 being deeper than contact recess 15. The contact recesses 15 and 16 locate contacts 19, 20 respectively. Again, it would be appreciated that the number of contacts will correspond to the number of pins to the CPU chip. The contacts 19 and 20 have pins, 21,



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22, although it will be appreciated that other electrical connection could be used.

Not only are the contact recesses 15 and 16 of different depths but each recess has a two height ceiling. This provides for accommodation of contact 19 and 20, both of which have contacts arms 23, 24 and 25, 26 respectively of different lengths.

The contact arms 23 and 24 are of bent metal stamped from a sheet (other way of producing the contacts are possible) and are formed with contact points 30, 31 respectively. Similarly, the contact arms 25 and 26 of the contact 16 are formed with contact points 32, 33 respectively. All four contact points 30, 31, 32 and 33 lie at different distances from upper pin engagement surface 35 of the body portion 14.

The configuration of the contacts and location of the contact points 30, 31, 32 and 33 are such that as the CPU chip is lowered into engagement with the socket 13, the pin 12 first makes contact with the contact point 32 of the contact 20. With further downward movement of the CPU chip, the pin 11 makes contact with the contact point 30 of the contact 19. Further downward movement of the CPU chip results in contact between the pin 12 and the contact point 33 of the contact 20 and, finally, the pin 11 makes contact with the contact point 31 of the contact 19. In this way, the insertion force of the CPU chip pins into the socket 13 is reduced to approximately a quarter of the force necessary for contacts in which all contact points are level, the greatest force being necessary at initial engagement of a pin with a contact point.

It will be appreciated that the contact sequence between the CPU chip pins and the contact points on the contact



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arms may be different. Furthermore, it will be appreciated that the invention is not limited to engagement of pin grid arrays on CPU chips; indeed, the invention is applicable to any context in which insertion force is a problem in sockets for engagement with multi-pin male connectors.

It will be appreciated that different contact point positions can be achieved not only by varying contact arm length but also by locating similar contacts at different positions relative to the pin engagement surface.

Variations and modifications to the embodiment shown may be made; the embodiment described is by way of example only and the scope of the invention is to be determined by the appended claims.



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WHAT IS CLAIMED IS:

1. A socket connector for connection to a plurality of pins, which socket connector has a plurality of socket contacts for engagement by the plurality of pins, each socket contact having a pair of contact arms, each arm having a contact point adjacent a free end thereof, the contact points of each contact being staggered such that, in use, a particular pin does not make initial contact with both the contact points of the associated contact simultaneously, and adjacent contacts being positionally staggered with respect to each other such that, in use, the associated pair of adjacent pins does not make initial contact simultaneously with more than one contact point.
2. A socket connector as claimed in claim 1, wherein the staggering of the contact points is such that a first contact is made between a first pin of the adjacent pair and one contact point of the contact associated with the first pin, and the next contact is made between the second pin of the adjacent pair and one contact point of the contact associated with second pin.
3. A socket connector as claimed in claim 2, wherein the third contact is made between the first pin and the other contact point of the contact associated with the first pin and the fourth contact is between the second pin and the other contact pin of the contact associated with the second pin.
4. A socket connector as claimed in claim 1, wherein each contact arm has an end portion extending from the contact point to the free end, which end portion lies obliquely to the direction of pin travel.
5. A socket connector as claimed in claim 1, wherein each contact has a base, the contact arms extending from

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the base in cantilever fashion.

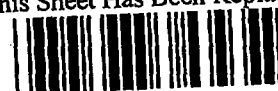
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6. A socket connector as claimed in claim 5, wherein the bases of the contact are coplanar, different contact point spacing from the base being achieved by different lengths of the contact arms.

7. A socket connector as claimed in claim 1, wherein each contact is formed from sheet material.



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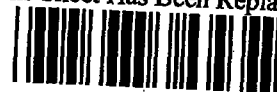
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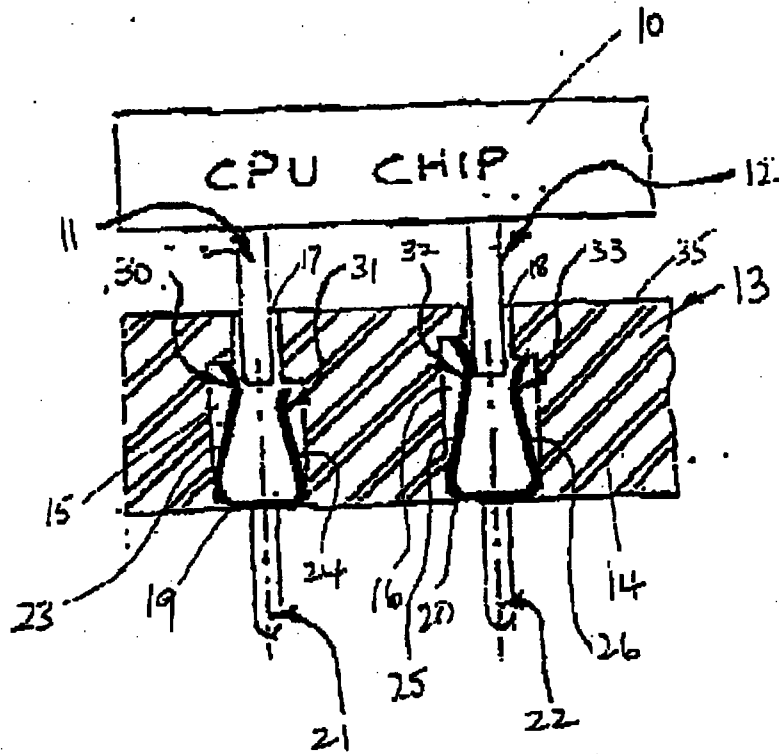
THIS SHEET HAS BEEN
REPLACED**IMPROVEMENTS IN OR RELATING TO
ELECTRICAL CONNECTION APPARATUS**

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**ABSTRACT OF THE DISCLOSURE**

5 A pin grid array socket connector for connection to a
plurality of pins in a CPU chip has a plurality of
socket contacts, each socket contact having a pair of
contact arms. Each arm has a contact point adjacent a
free end thereof, the contact points of each contact
being staggered such that, in use, a particular pin of
10 the CPU chip does not make initial contact with both the
contact points of the associated contact simultaneously.
Adjacent contacts are positionally staggered with
respect to each other such that, in use, the associated
pair of CPU chip pins does not make initial contact
15 simultaneously with more than one contact point.

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IMPROVEMENTS IN OR RELATING TO
ELECTRICAL CONNECTION APPARATUS

FIELD OF THE INVENTION

The invention relates to electrical connection apparatus and more particularly but not exclusively to pin grid array (hereinafter referred to by the usual term of art acronym PGA) sockets which are used for connection with, for example, central processing unit (CPU) chips which normally have in excess of 144 pins.

BACKGROUND OF THE INVENTION

A problem arises in connectors with a large number of pins, for example in PGA sockets for connection of CPU chips, in that forces involved in inserting the pins into the sockets are high; this can lead both to difficulty in engagement and to damage. There is also a trend towards increasing the number of pins; for example a 586 chip has in excess of 200 pins.

Attempts have been made to reduce the insertion force of pins into PGA sockets, for example by arranging socket contacts in two staggered rows. This will reduce insertion force by approximately 50% compared to unstaggered socket contacts. An alternative approach has been to design socket contacts with staggered contact points, such that a pin does not start engagement with both contact arms at the same time, thereby reducing the insertion force. However, such alternatives have not succeeded in reducing insertion force adequately and this invention addresses this problem.

SUMMARY OF THE INVENTION

According to the invention, there is provided a socket connector for connection to a plurality of pins, which socket connector has a plurality of socket contacts for engagement by the plurality of pins, each socket contact having a pair of contact arms, each arm having a profile to provide a contact points of each contact being staggered such that, in use, a particular pin does not make initial contact with both the contact points of the associated contact simultaneously, and adjacent contacts being positionally staggered with respect to each other such that, in use, the associated pair of adjacent pins does not make initial contact simultaneously with more than one contact point.

The staggering of the contact points is preferably such that a first contact is made between a first pin of the adjacent pair and one contact point of the contact associated with the first pin, and the next contact is made between the second pin of the adjacent pair and one contact point of the contact associated with the second pin.

The third contact is preferably made between the first pin and the other contact point of the contact associated with the first pin, and the fourth contact is preferably between the second pin and the other contact point of the contact associated with the second pin.

Each contact arm preferably has an end portion extending from the contact point to the free end, which end portion lies obliquely to the direction of pin travel to each the entry of a pin into the associated contact.

Each contact preferably has a base, the contact arms extending from the base in cantilever fashion. Each contact may be formed from sheet material. The bases of the contacts may be coplanar, and different contact point spacing from the base plane may be achieved by different lengths of the contact arms.

BRIEF DESCRIPTION OF THE SOLE DRAWING FIGURE

By way of example, one embodiment of a socket connector according to the invention will now be described with reference to the accompanying drawing, which is a sectional side view of a pair of adjacent contacts in an array of contacts.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

A CPU chip is illustrated at 10 and only a small portion is shown from which two pins 11, 12 extend. As discussed previously, a CPU chip has a large number of pins, typically more than 144 and, with further developments in chips, in excess of 200 pins.

Part of a pin grid array (GPA) socket is shown at 13, the socket 13 having a body portion 14 having contact recesses 15, 16 and pin engagement holes 17, 18. It will be appreciated that the number of contact recesses and engagement holes will correspond to the number of pins in the CPU chip.

As can be seen clearly in the drawing, the contact recesses 15 and 16 are of different depth, the contact recess 16 being deeper than contact recess 15. The contact recesses 15 and 16 locate contacts 19, 20 respectively. Again, it would be appreciated that the number of contacts will correspond to the number of pins to the CPU chip. The contacts 19 and 20 have pins, 21,

22, although it will be appreciated that other forms of electrical connection could be used.

Not only are the contact recesses 15 and 16 of different depths but each recess has a two height ceiling. This provides for accommodation of contact 19 and 20, both of which have contacts arms 23, 24 and 25, 26 respectively of different lengths.

The contact arms 23 and 24 are of bent metal stamped from a sheet (other way of producing the contacts are possible) and are formed with contact points 30, 31 respectively. Similarly, the contact arms 25 and 26 of the contact 16 are formed with contact points 32, 33 respectively. All four contact points 30, 31, 32 and 33 lie at different distances from upper pin engagement surface 35 of the body portion 14.

The configuration of the contacts and location of the contact points 30, 31, 32 and 33 are such that as the CPU chip is lowered into engagement with the socket 13, the pin 12 first makes contact with the contact point 32 of the contact 20. With further downward movement of the CPU chip, the pin 11 makes contact with the contact point 30 of the contact 19. Further downward movement of the CPU chip results in contact between the pin 12 and the contact point 33 of the contact 20 and, finally, the pin 11 makes contact with the contact point 31 of the contact 19. In this way, the insertion force of the CPU chip pins into the socket 13 is reduced to approximately a quarter of the force necessary for contacts in which all contact points are level, the greatest force being necessary at initial engagement of a pin with a contact point.

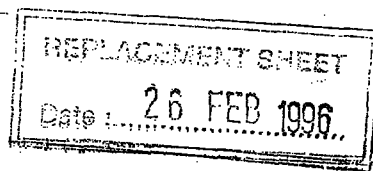
It will be appreciated that the contact sequence between the CPU chip pins and the contact points on the contact

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arms may be different. Furthermore, it will be appreciated that the invention is not limited to engagement of pin grid arrays on CPU chips; indeed, the invention is applicable to any context in which insertion force is a problem in sockets for engagement with multi-pin male connectors.

It will be appreciated that different contact point positions can be achieved not only by varying contact arm length but also by locating similar contacts at different positions relative to the pin engagement surface.

Variations and modifications to the embodiment shown may be made; the embodiment described is by way of example only and the scope of the invention is to be determined by the appended claims.



WHAT IS CLAIMED IS:

1. A socket connector for connection to a plurality of pins, which socket connector has a plurality of socket contacts for engagement by the plurality of pins, each socket contact having a pair of contact arms, each arm having a contact point adjacent a free end thereof, the contact points of each contact being staggered such that, in use, a particular pin does not make initial contact with both the contact points of the associated contact simultaneously, and adjacent contacts being positionally staggered with respect to each other such that, in use, the associated pair of adjacent pins does not make initial contact simultaneously with more than one contact point.

2. A socket connector as claimed in claim 1, wherein the staggering of the contact points is such that a first contact is made between a first pin of the adjacent pair and one contact point of the contact associated with the first pin, and the next contact is made between the second pin of the adjacent pair and one contact point of the contact associated with second pin.

3. A socket connector as claimed in claim 2, wherein the third contact is made between the first pin and the other contact point of the contact associated with the first pin and the fourth contact is between the second pin and the other contact pin of the contact associated with the second pin.

4. A socket connector as claimed in claim 1, wherein each contact arm has an end portion extending from the contact point to the free end, which end portion lies obliquely to the direction of pin travel.

5. A socket connector as claimed in claim 1, wherein each contact has a base, the contact arms extending from

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the base in cantilever fashion.

6. A socket connector as claimed in claim 5, wherein the bases of the contact are coplanar, different contact point spacing from the base being achieved by different lengths of the contact arms.

7. A socket connector as claimed in claim 1, wherein each contact is formed from sheet material.

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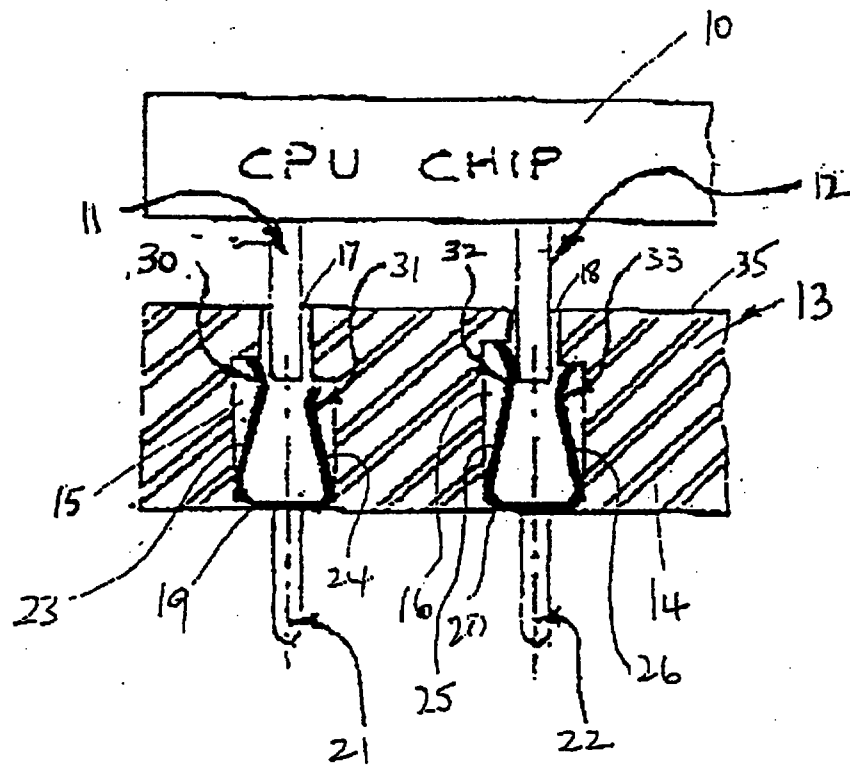
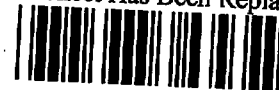


Fig. 1

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IMPROVEMENTS IN OR RELATING TO
ELECTRICAL CONNECTION APPARATUS

ABSTRACT OF THE DISCLOSURE

A pin grid array socket connector for connection to a plurality of pins in a CPU chip has a plurality of socket contacts, each socket contact having a pair of contact arms. Each arm has a contact point adjacent a free end thereof, the contact points of each contact being staggered such that, in use, a particular pin of the CPU chip does not make initial contact with both the contact points of the associated contact simultaneously. Adjacent contacts are positionally staggered with respect to each other such that, in use, the associated pair of CPU chip pins does not make initial contact simultaneously with more than one contact point.

Figure 1

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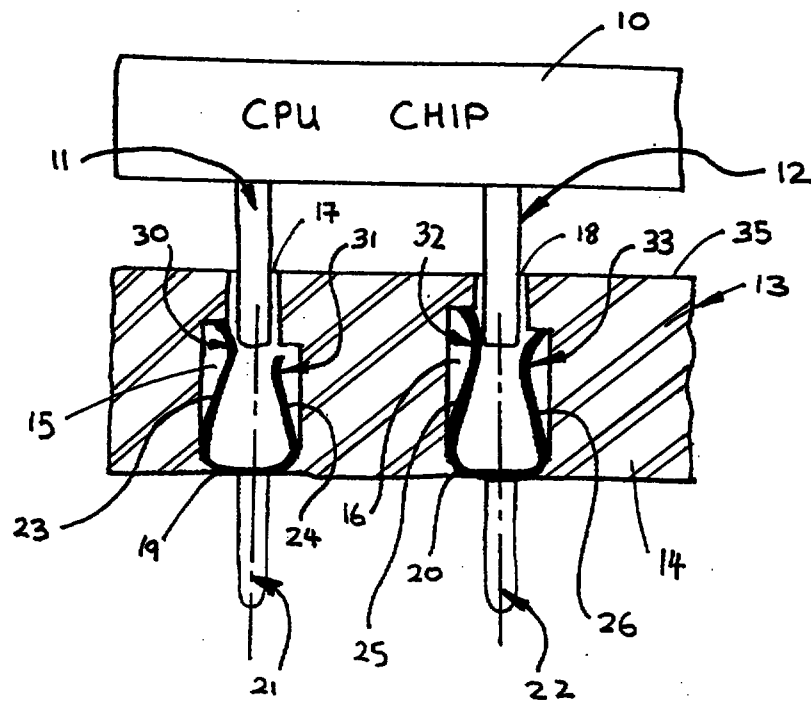


Fig. 1