

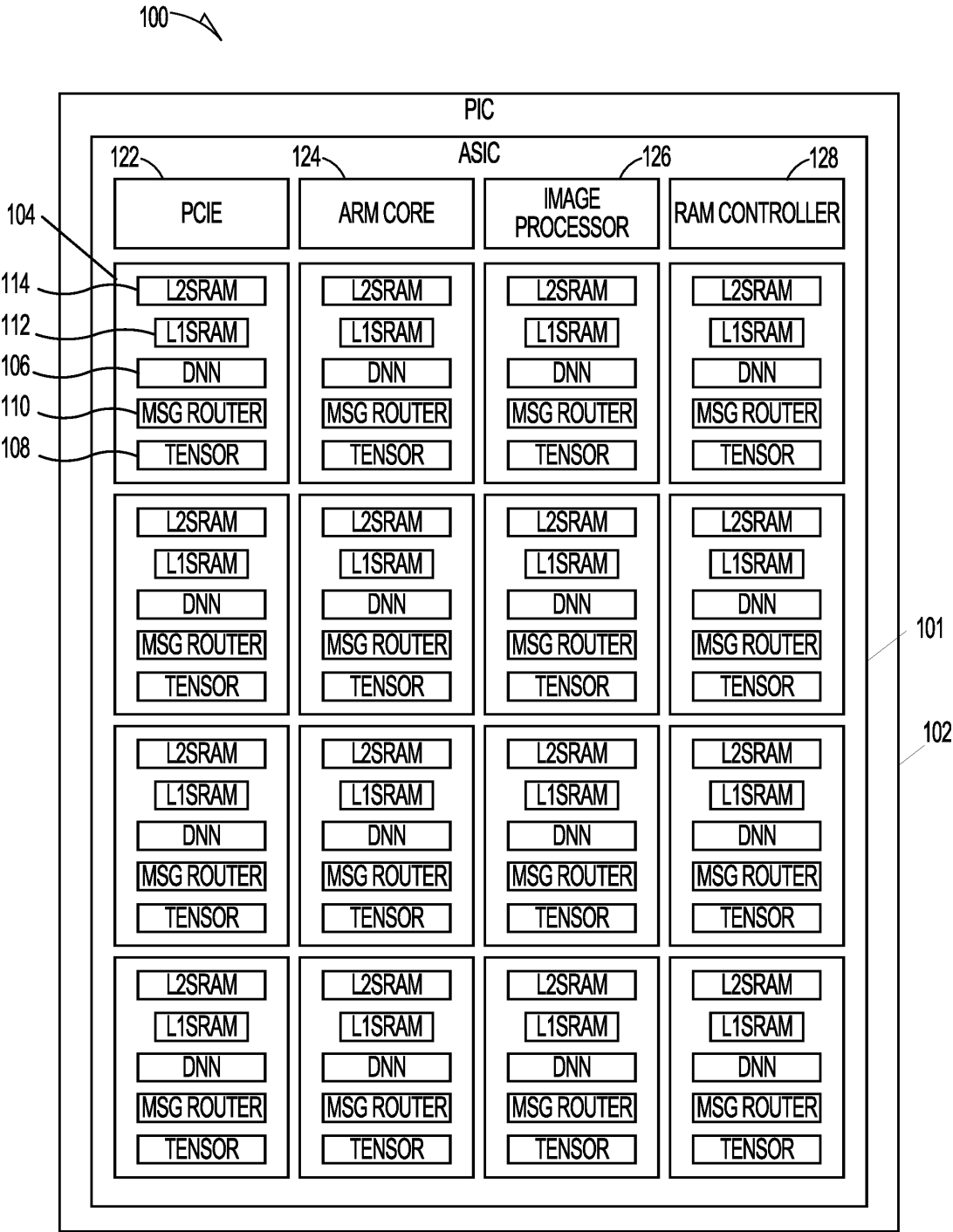


FIG. 1A

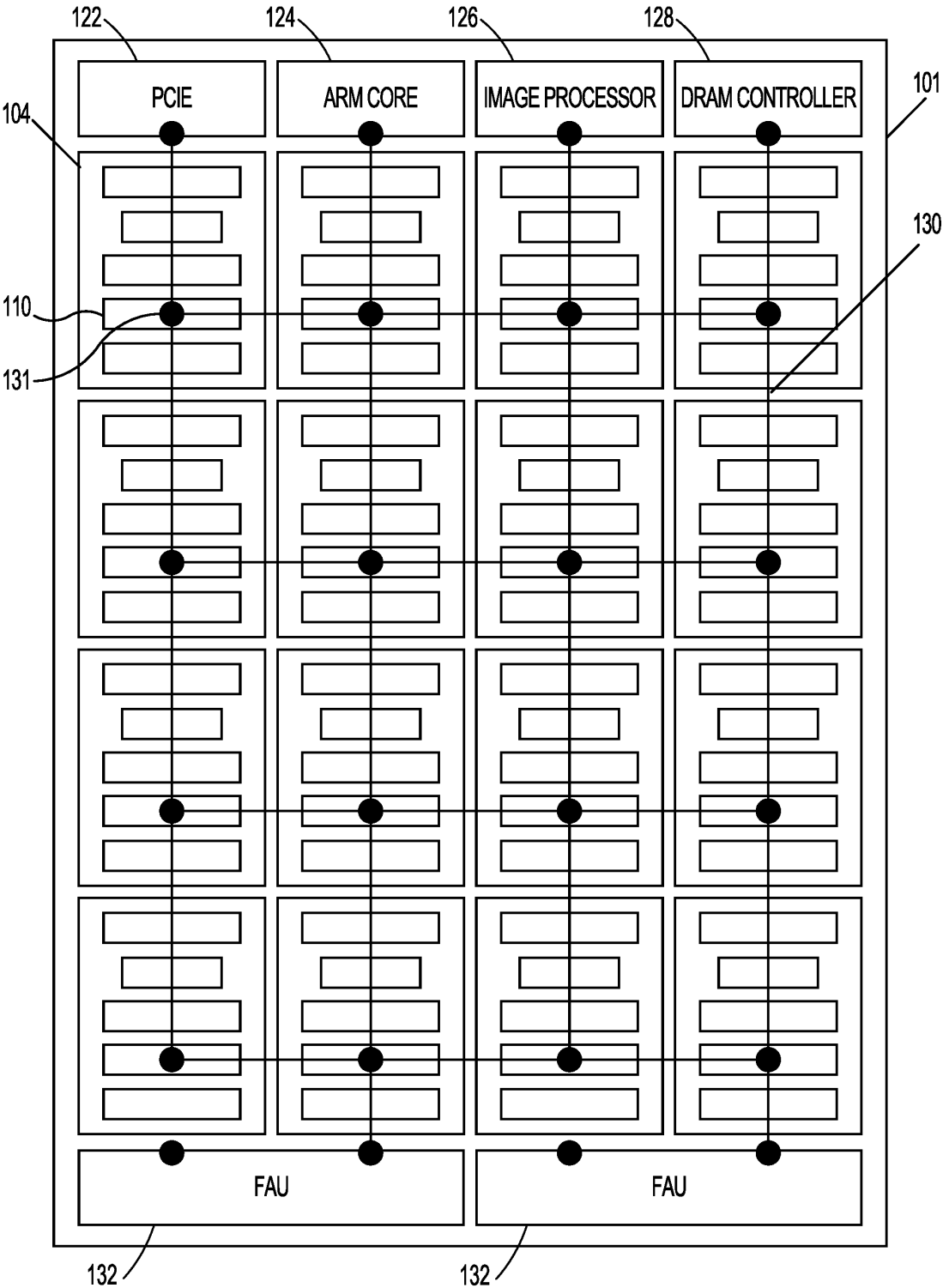


FIG. 1B

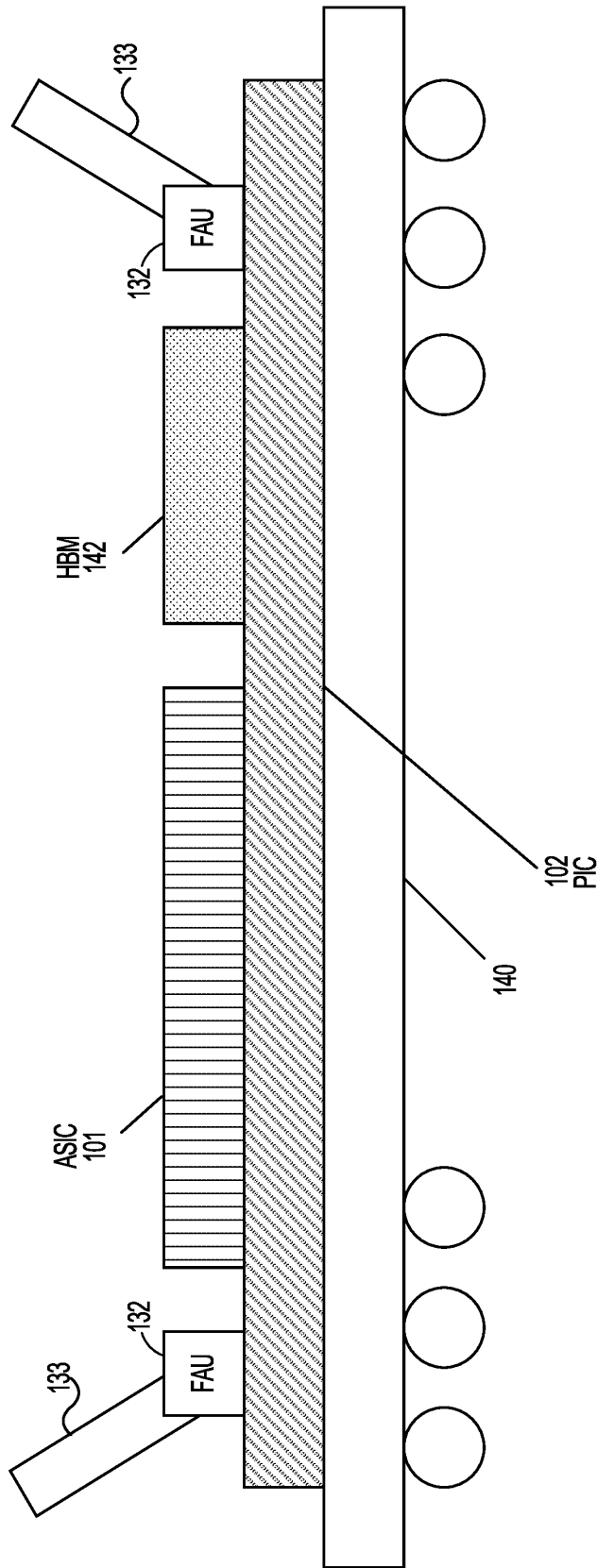


FIG. 1C

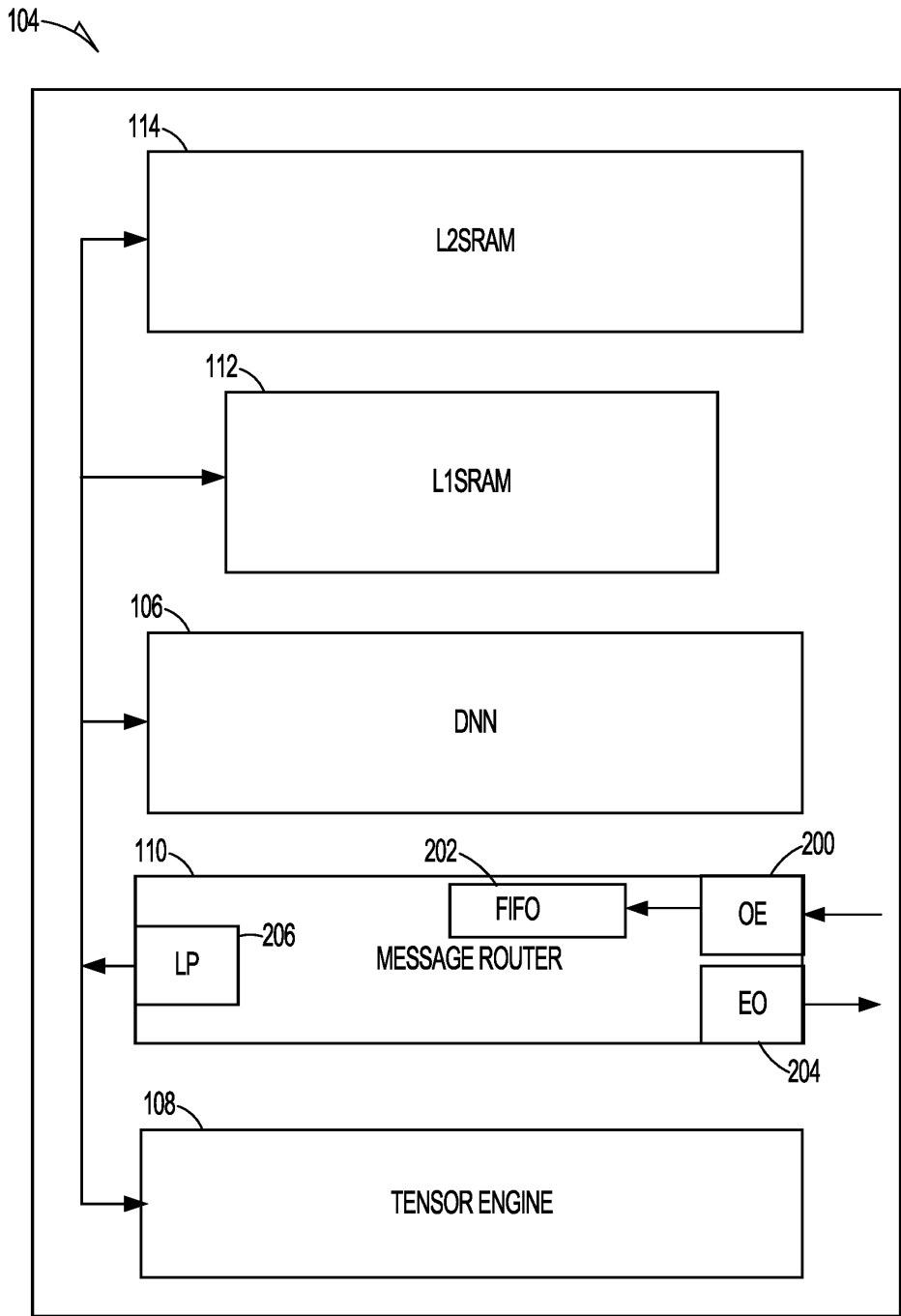


FIG. 2A

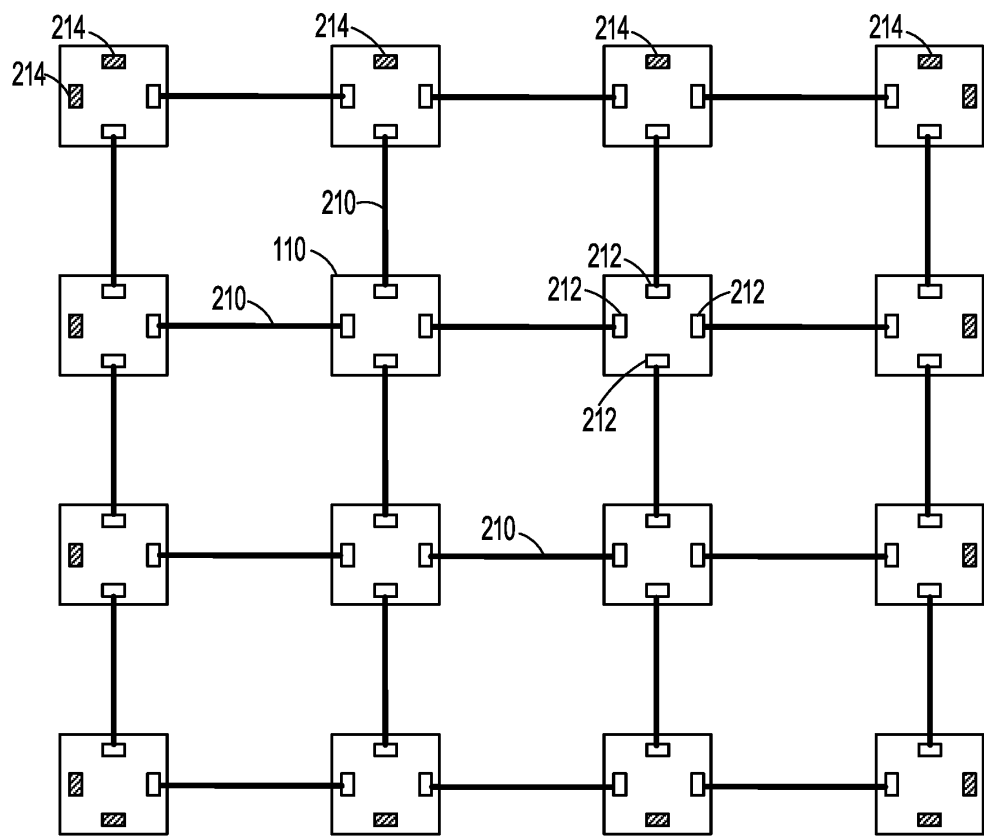


FIG. 2B

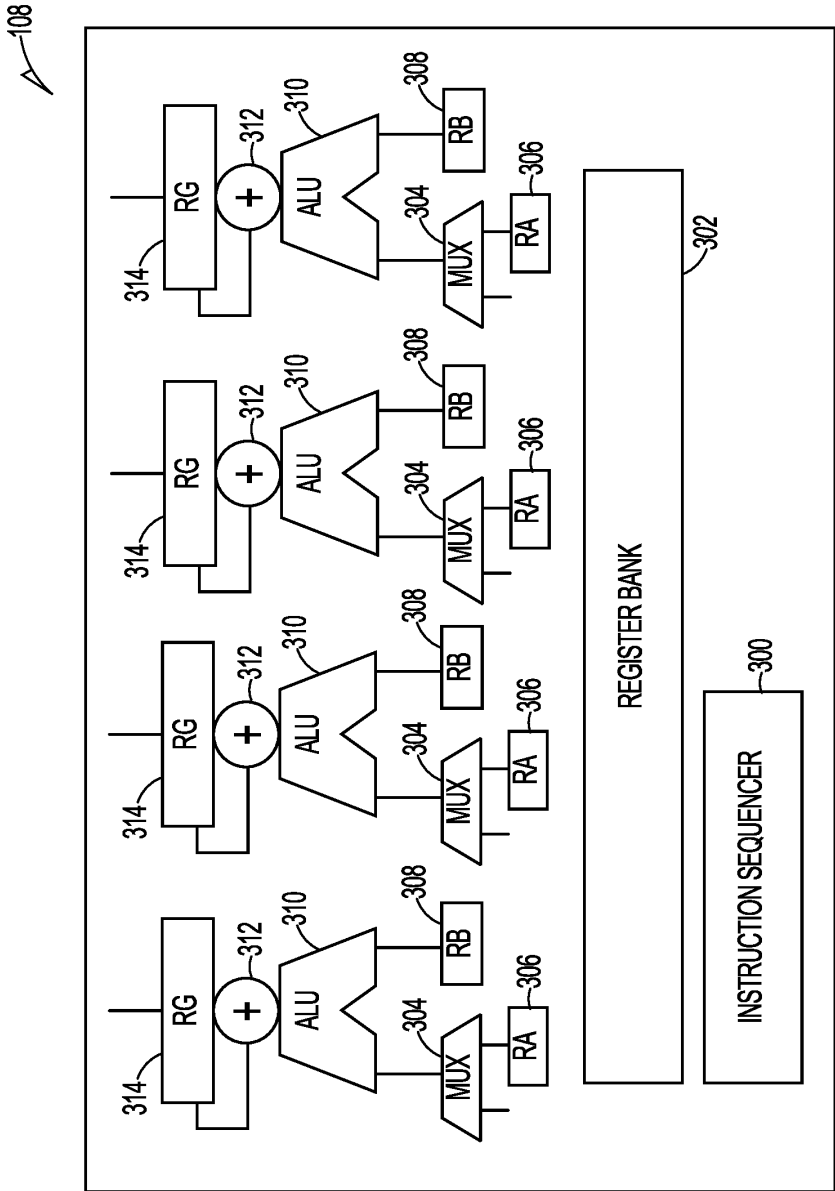


FIG. 3

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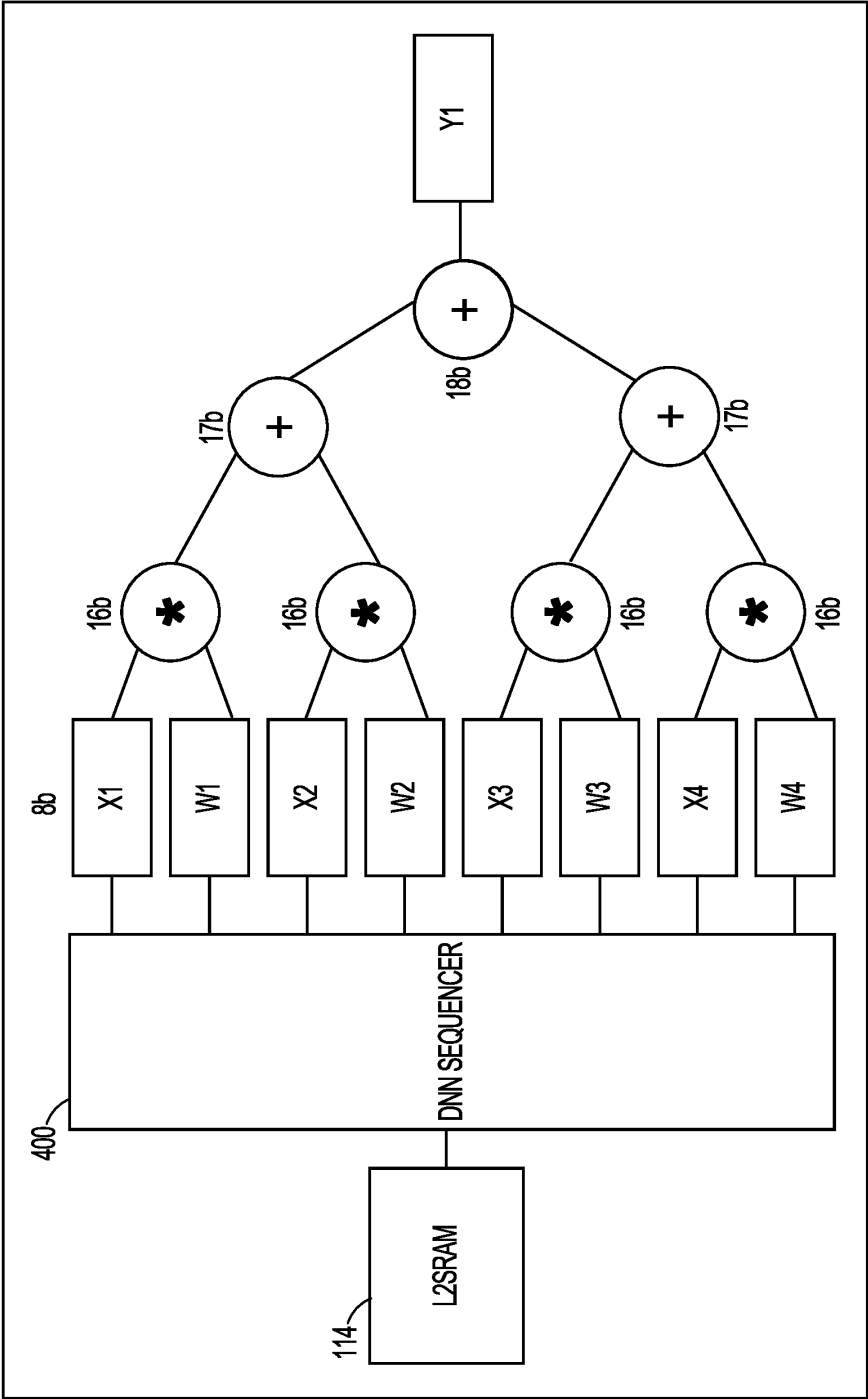


FIG. 4

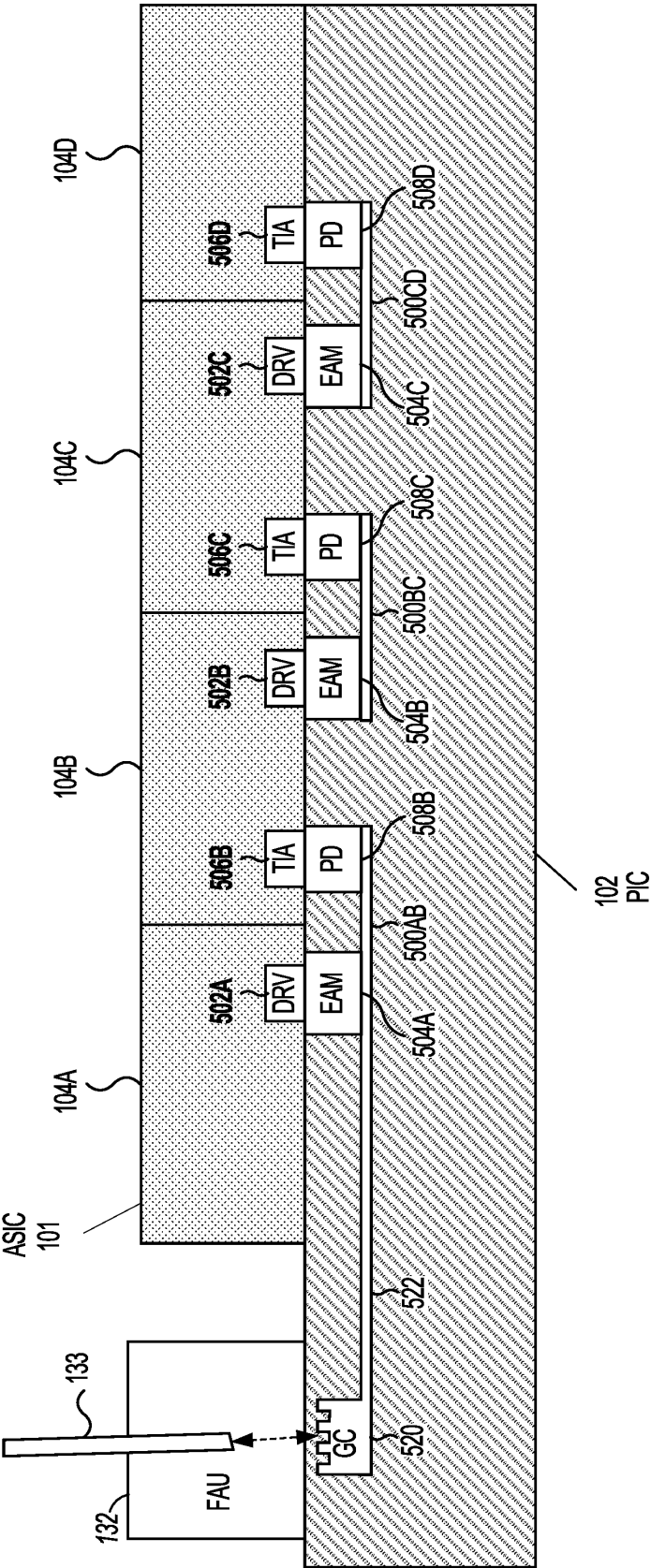
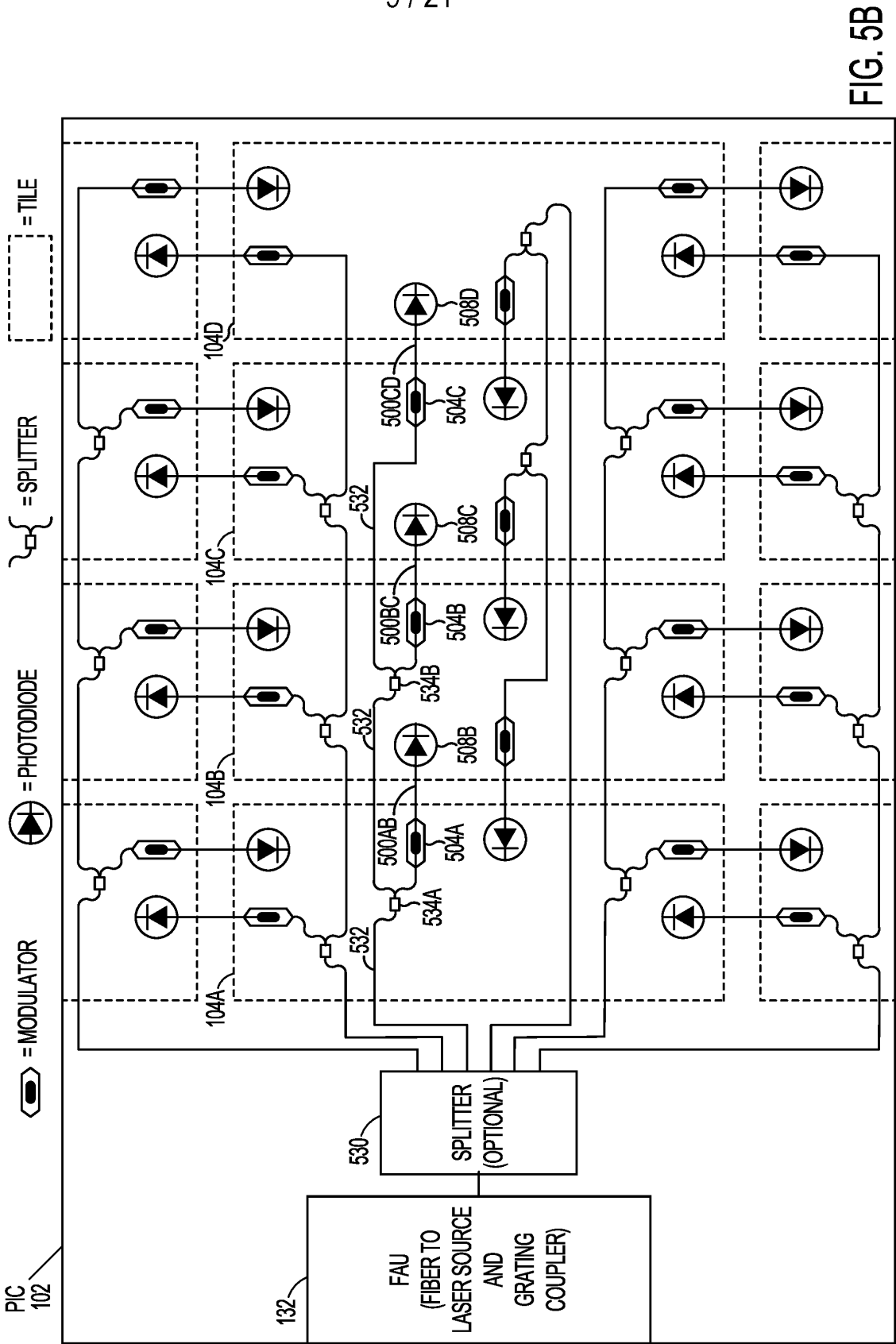


FIG. 5A



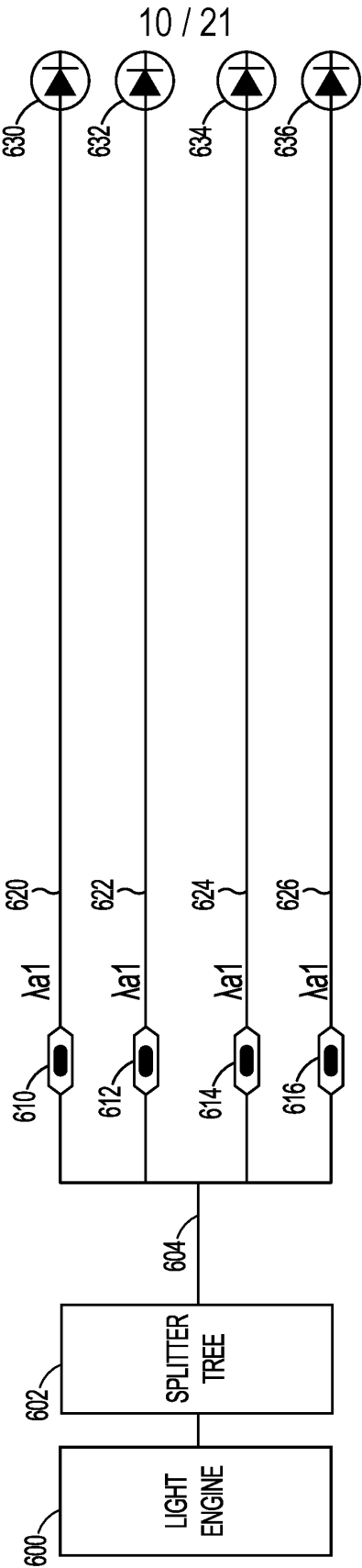


FIG. 6A
(ON-CHIP, NO WDM)

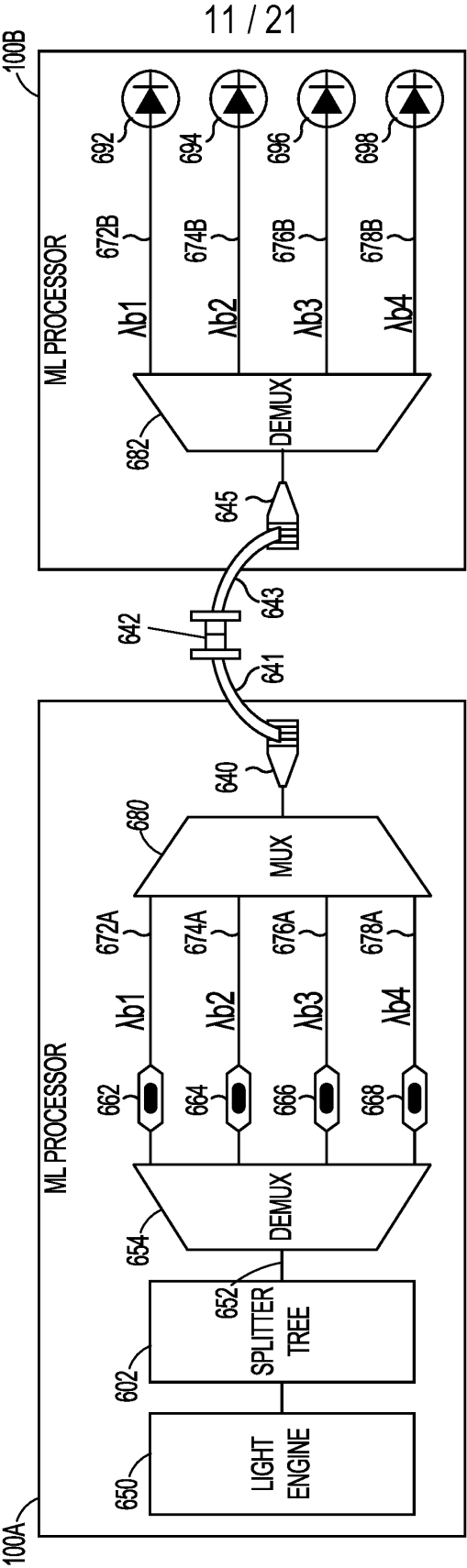


FIG. 6B
(CHIP-TO-CHIP, WDM)

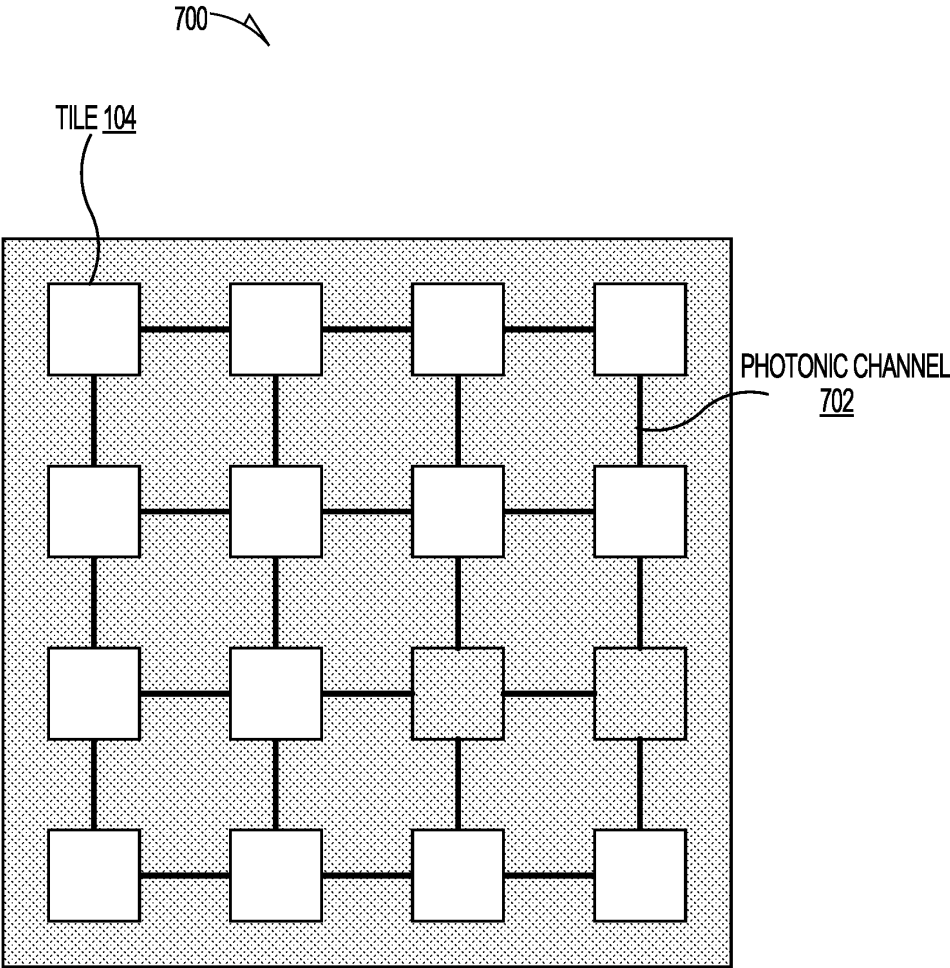


FIG. 7

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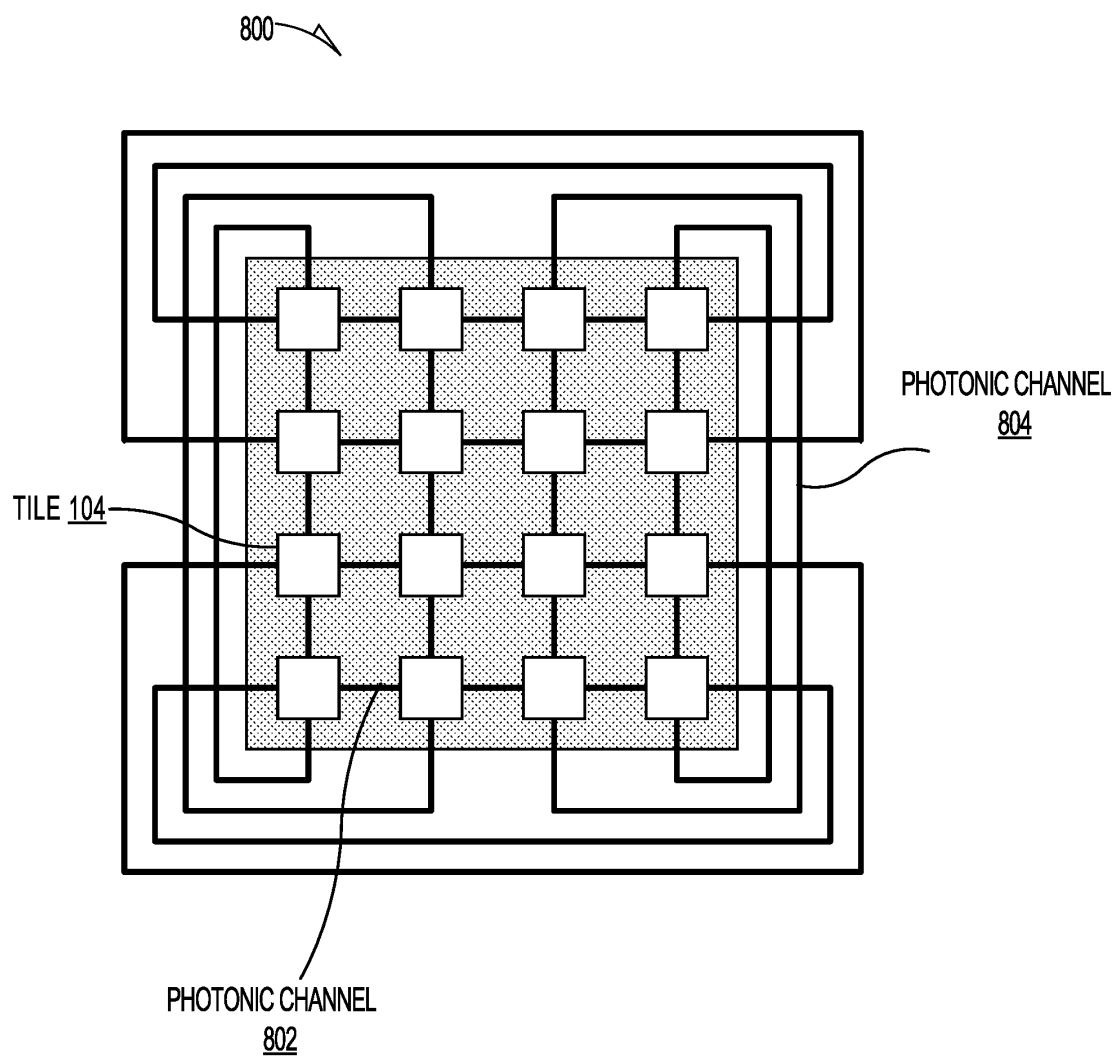


FIG. 8

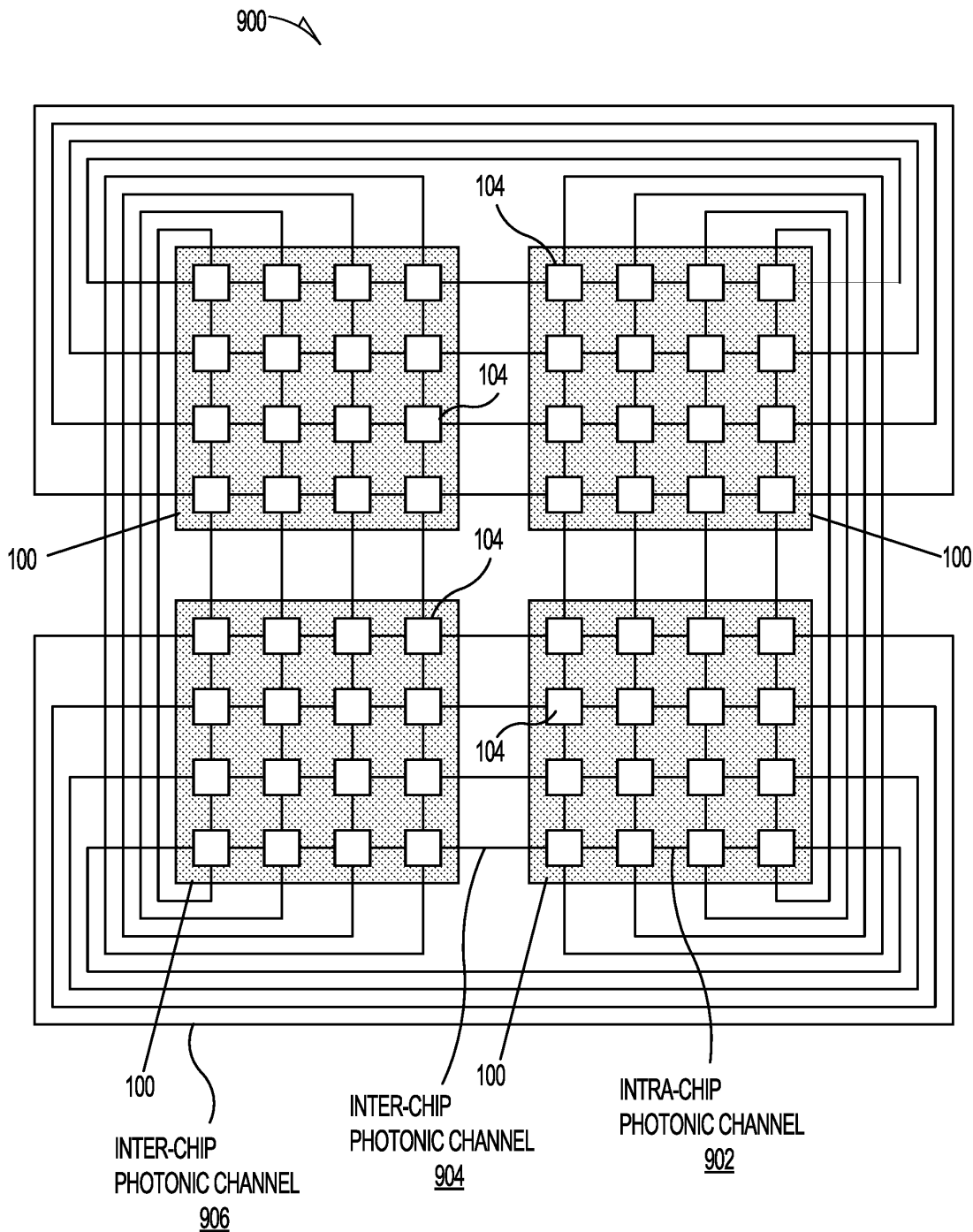


FIG. 9

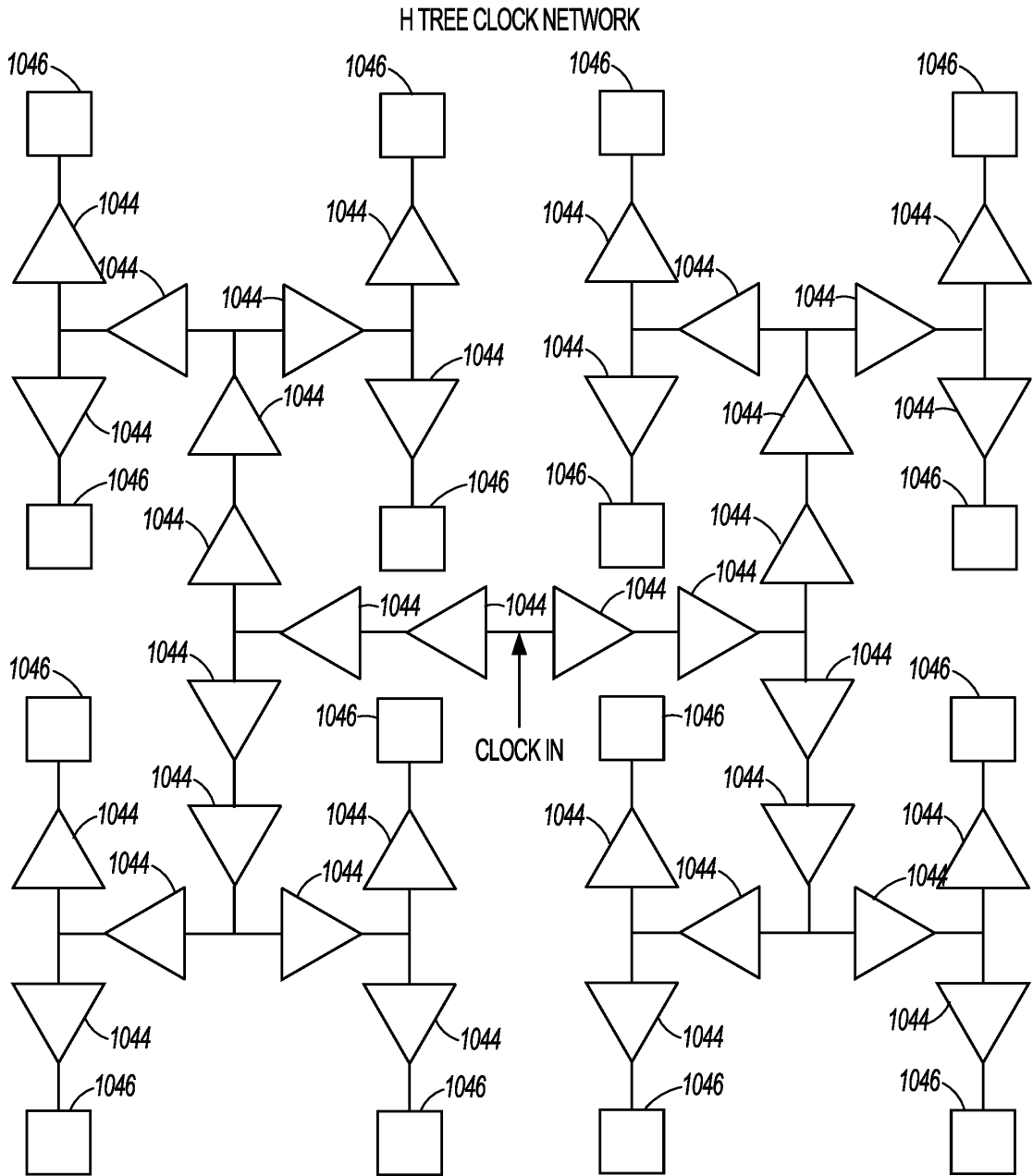


FIG. 10
(PRIOR ART)

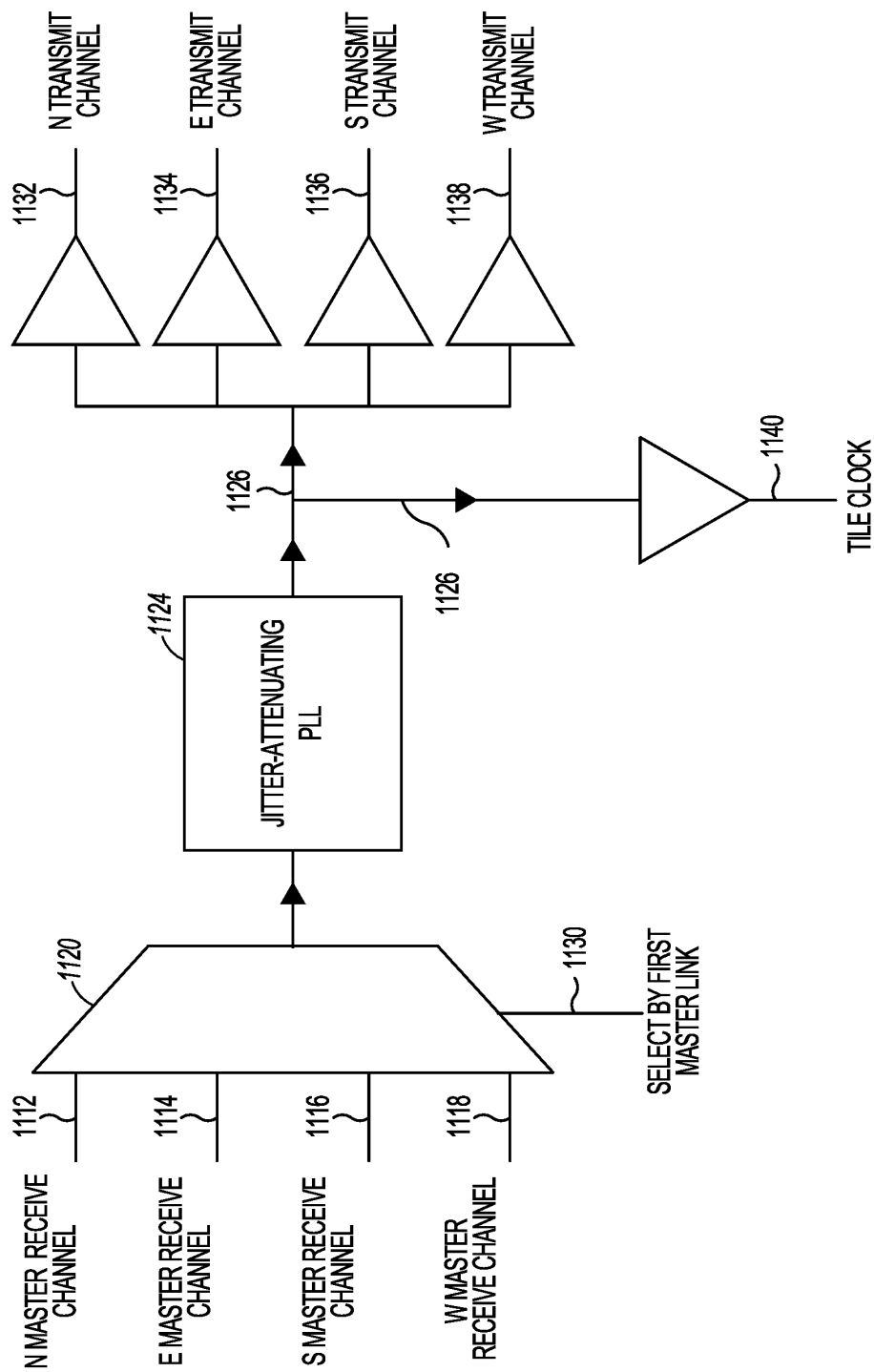


FIG. 11

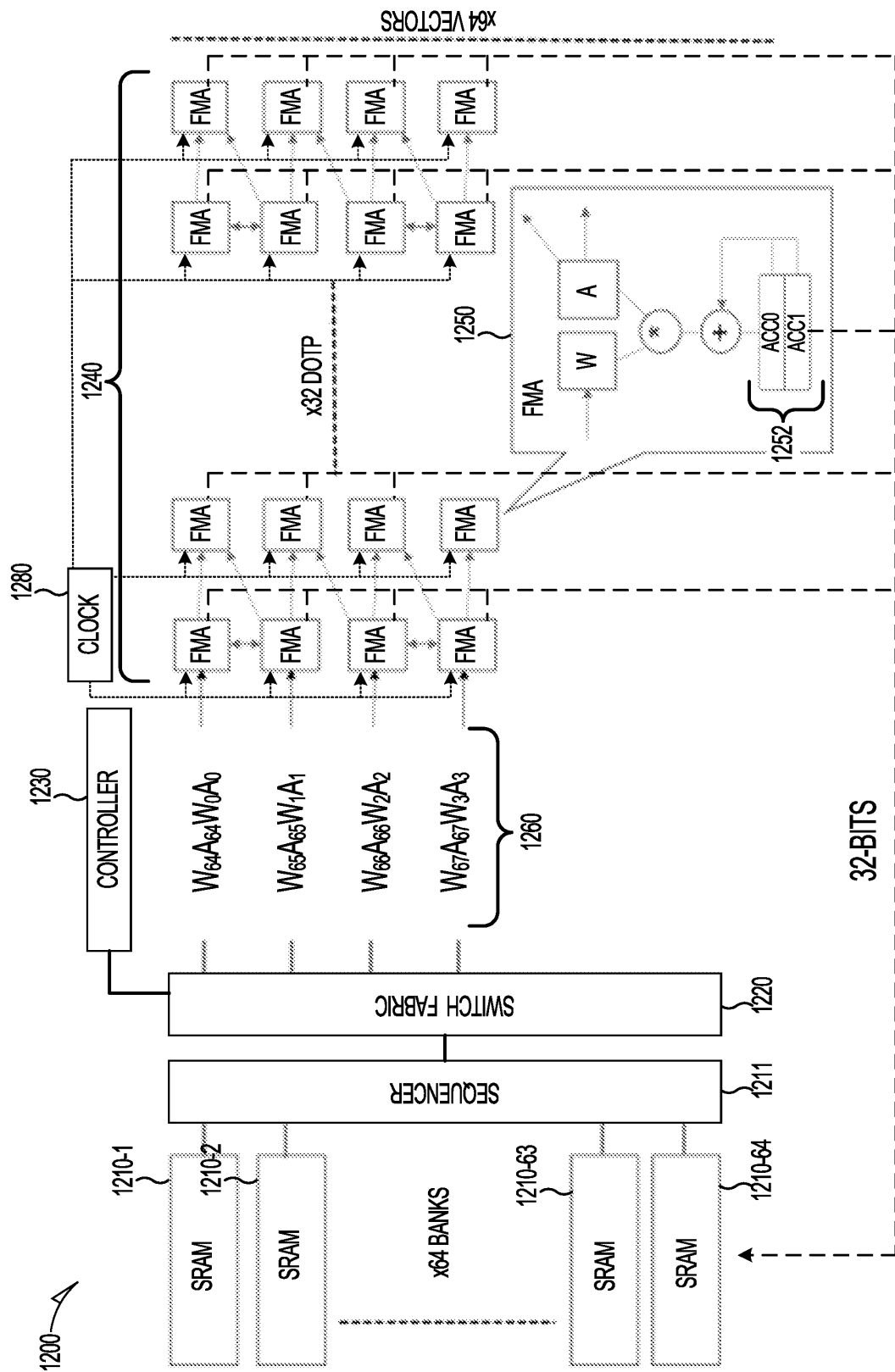


FIG. 12

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1300

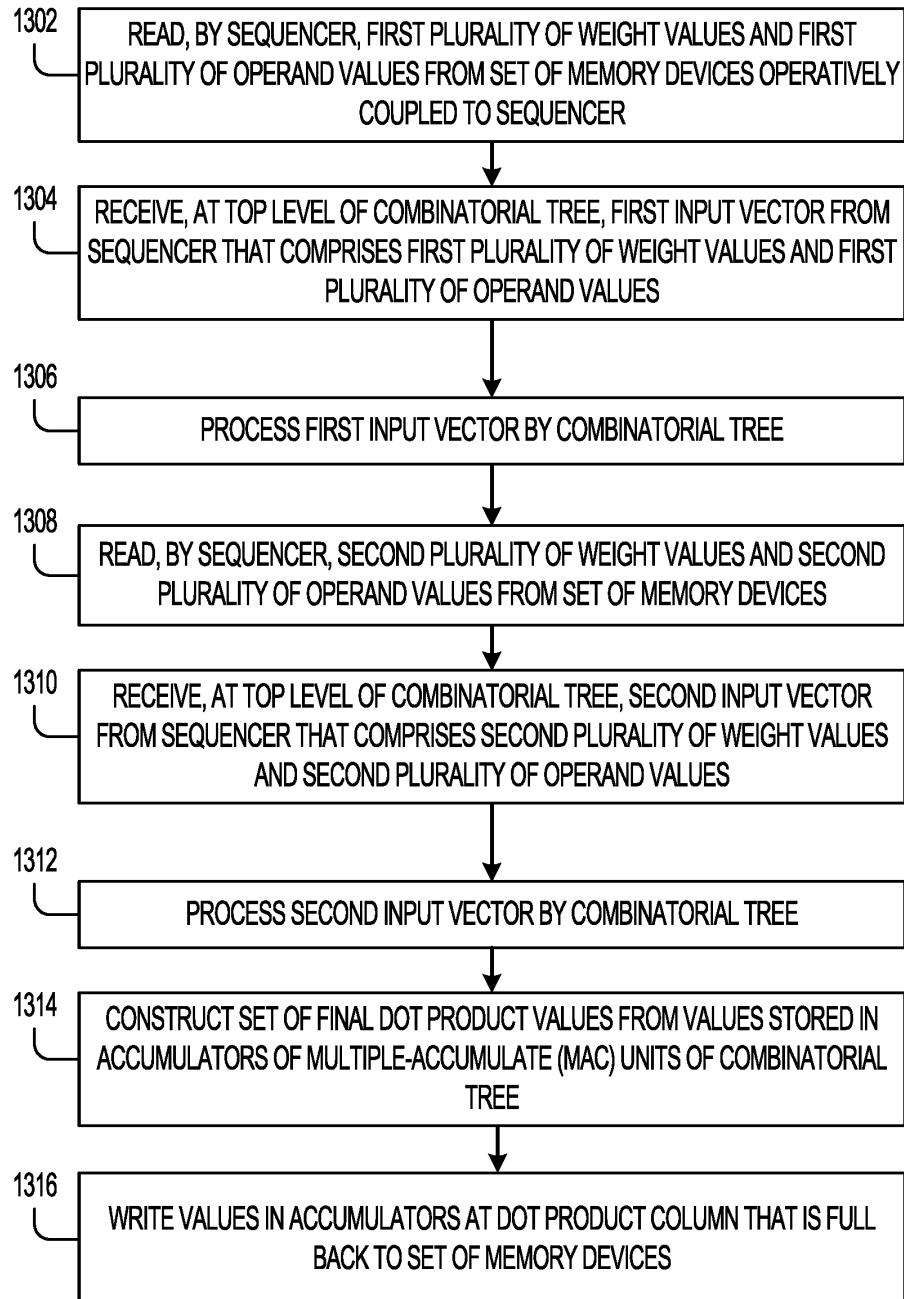


FIG. 13

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1400

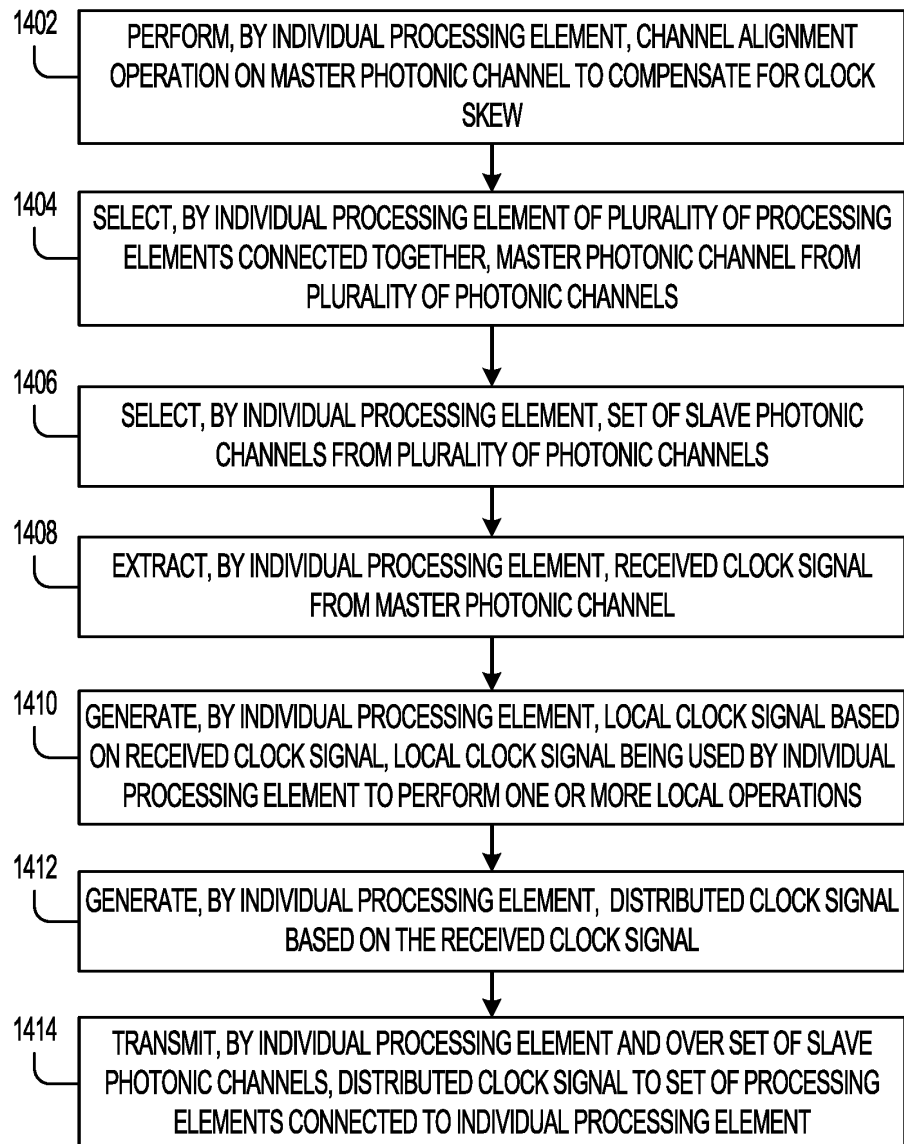


FIG. 14

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1500

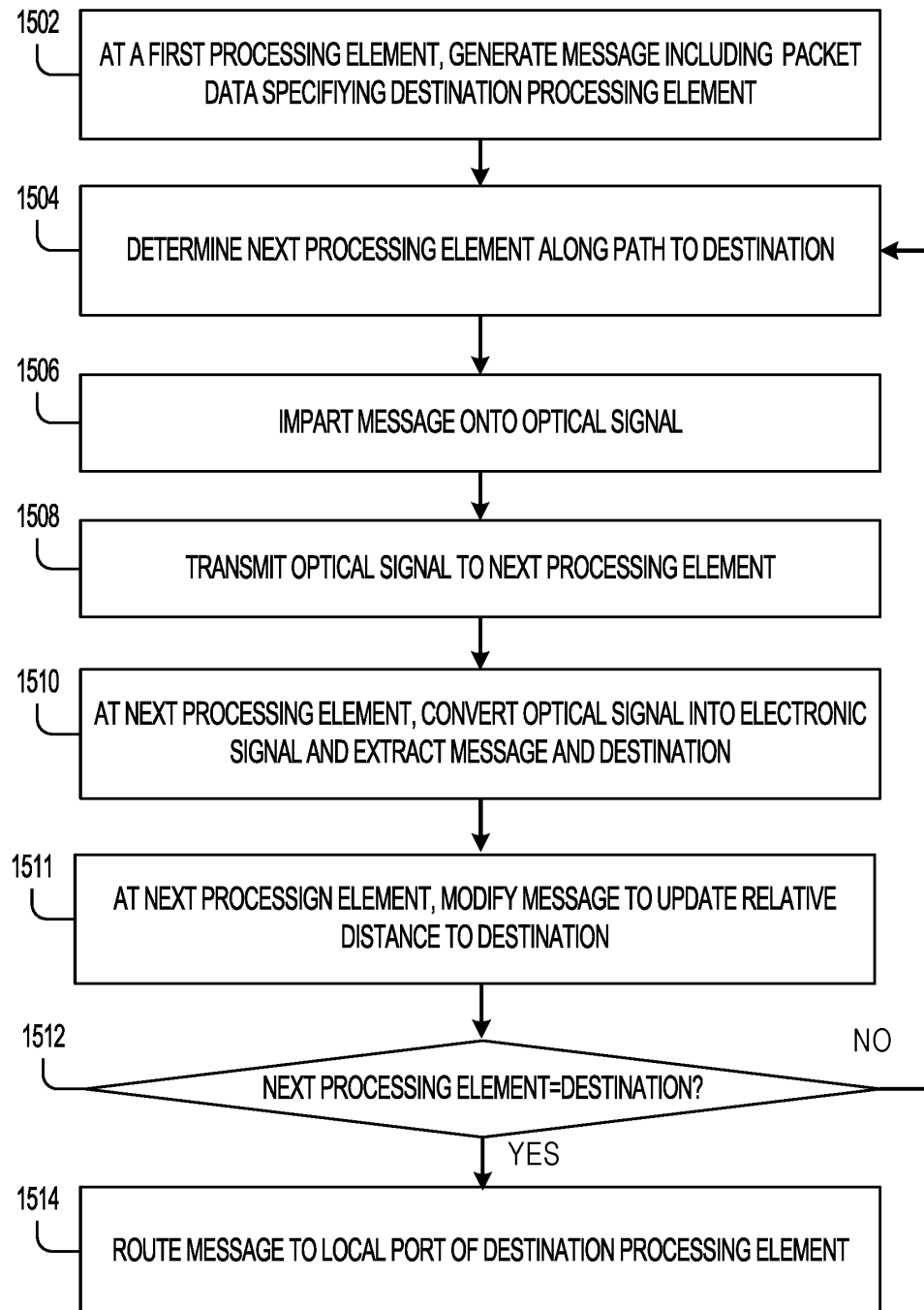


FIG. 15

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1600

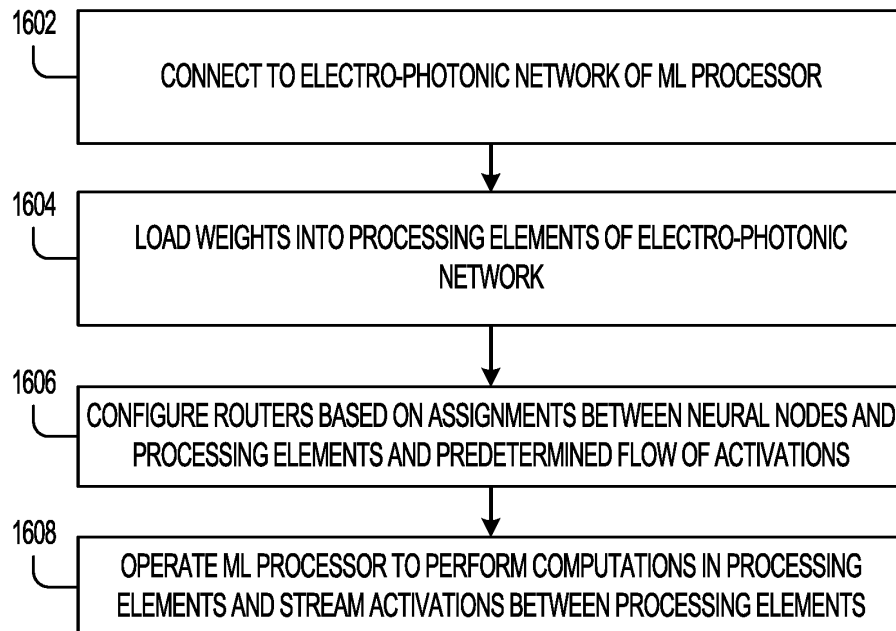


FIG. 16