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Fig. 2 – Part 1

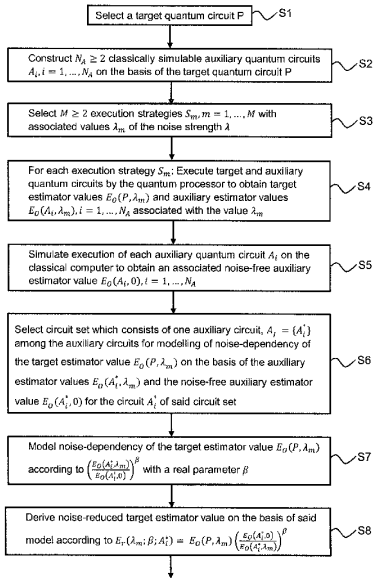
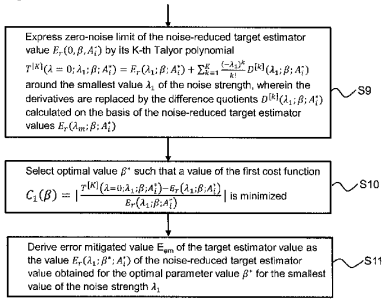


Fig. 2 – Part 2



(57) **Abstract:** The present invention is related to a method for mitigating errors caused by noise in a target quantum circuit *P* executed by a quantum processor comprising a number of N qubits, said target quantum circuit P comprising an instruction to apply a target quantum gate sequence to an initial state of said N qubits to thereby obtain a final state thereof, and to apply a measurement to the final state for deriving a target expectation value of an observable O.

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