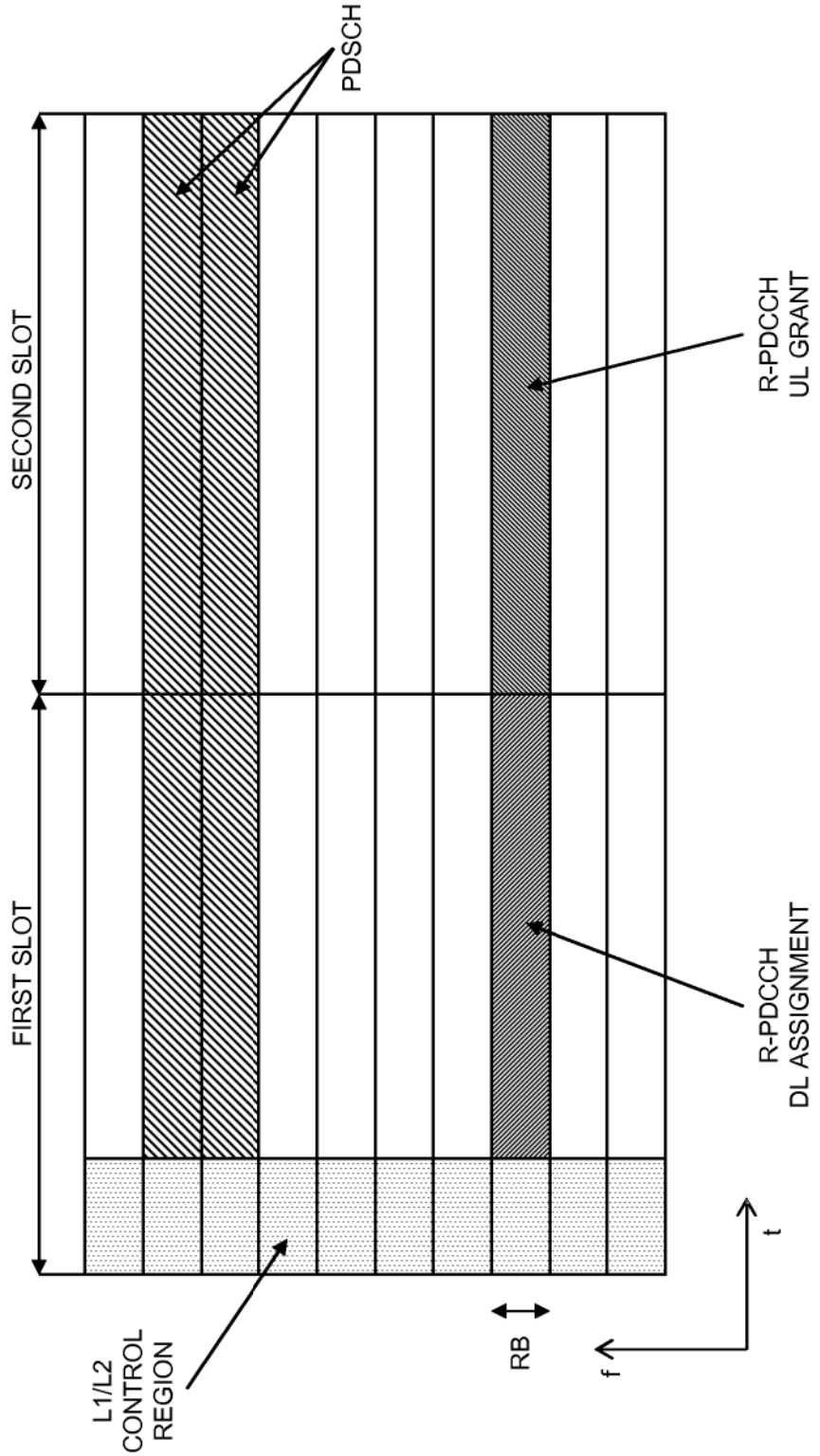
PDSCH
SECOND SLOT
FIRST SLOT
R-PDCCH
UL GRANT
R-PDCCH
DL ASSIGNMENT
L1/L2
CONTROL
REGION
RB
f
t

FIG. 1

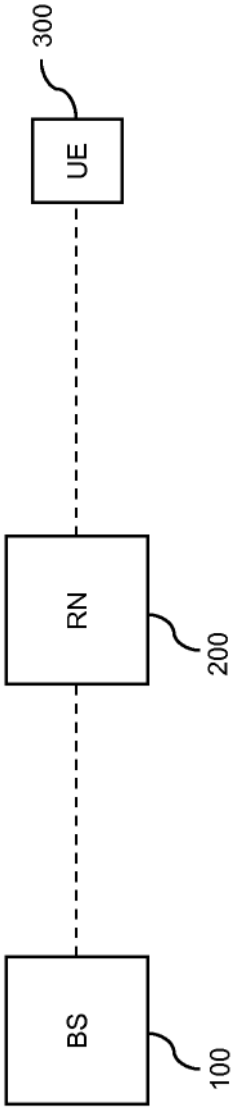

FIG. 2

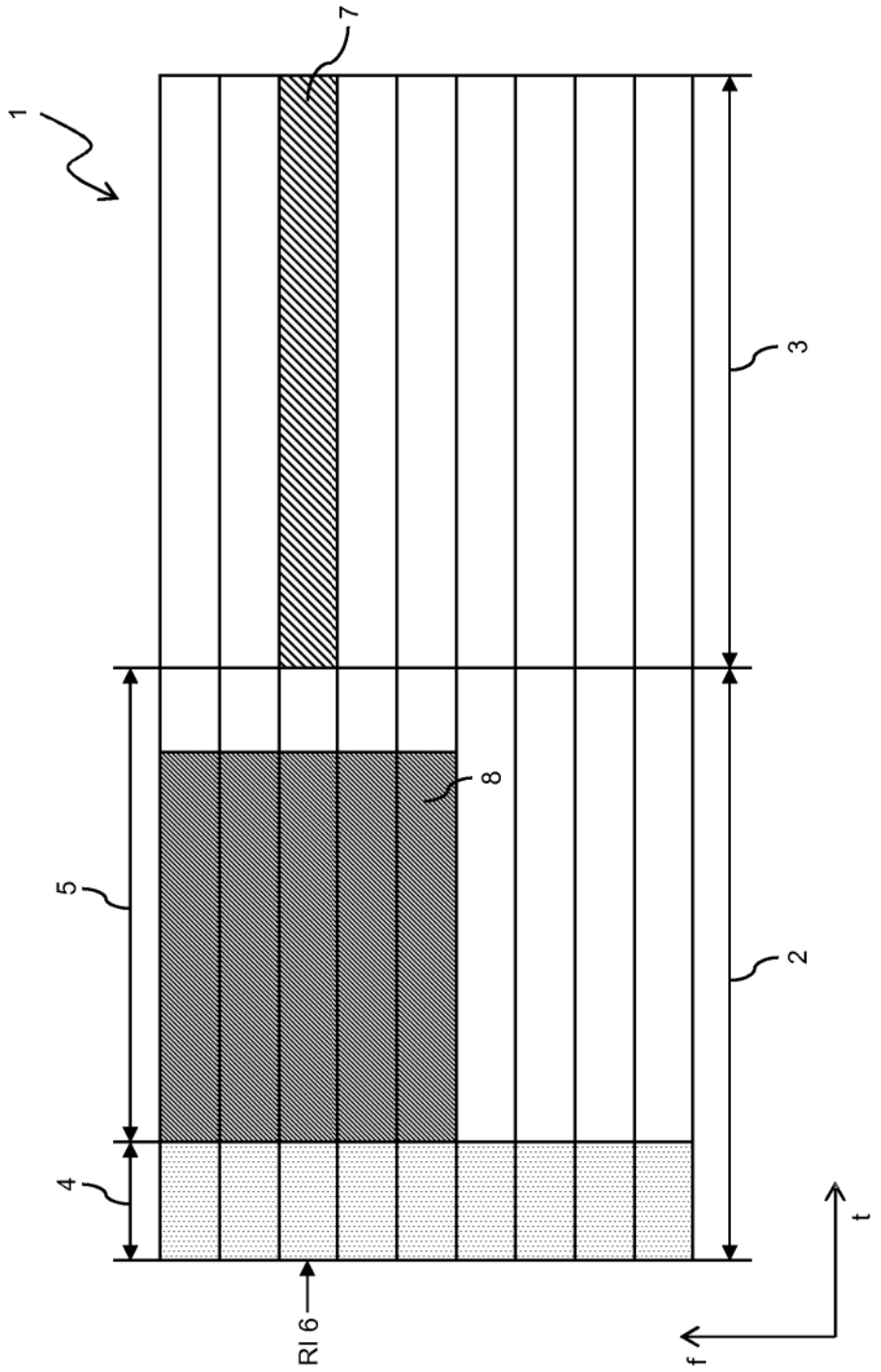

FIG. 3

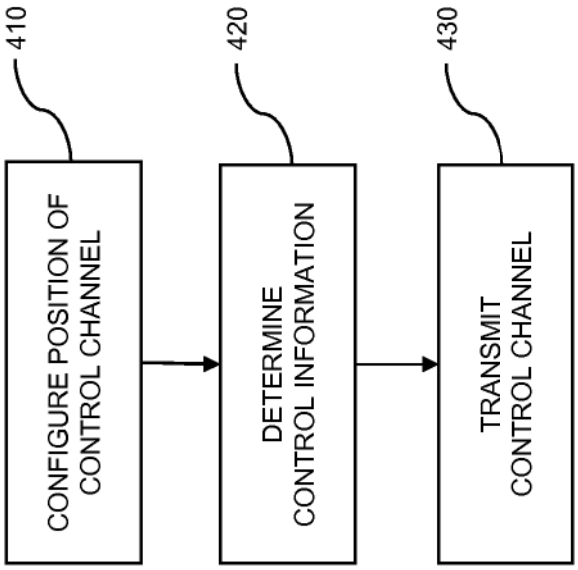
410
CONFIGURE POSITION OF CONTROL CHANNEL
420
DETERMINE CONTROL INFORMATION
430
TRANSMIT CONTROL CHANNEL

FIG. 4

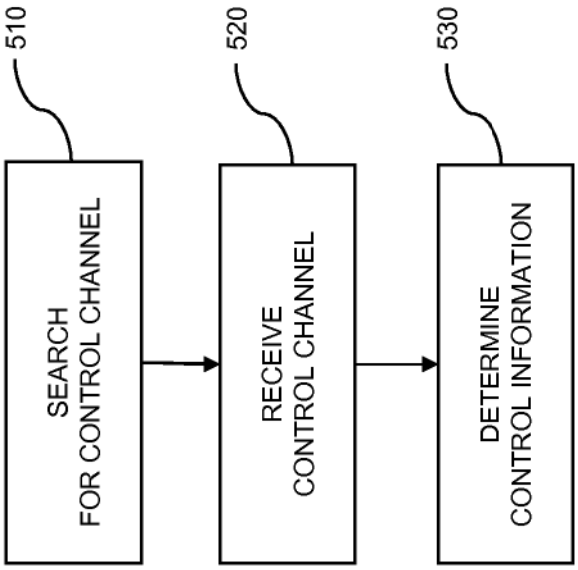
510
SEARCH
FOR CONTROL CHANNEL
520
RECEIVE
CONTROL CHANNEL
530
DETERMINE
CONTROL INFORMATION

FIG. 5

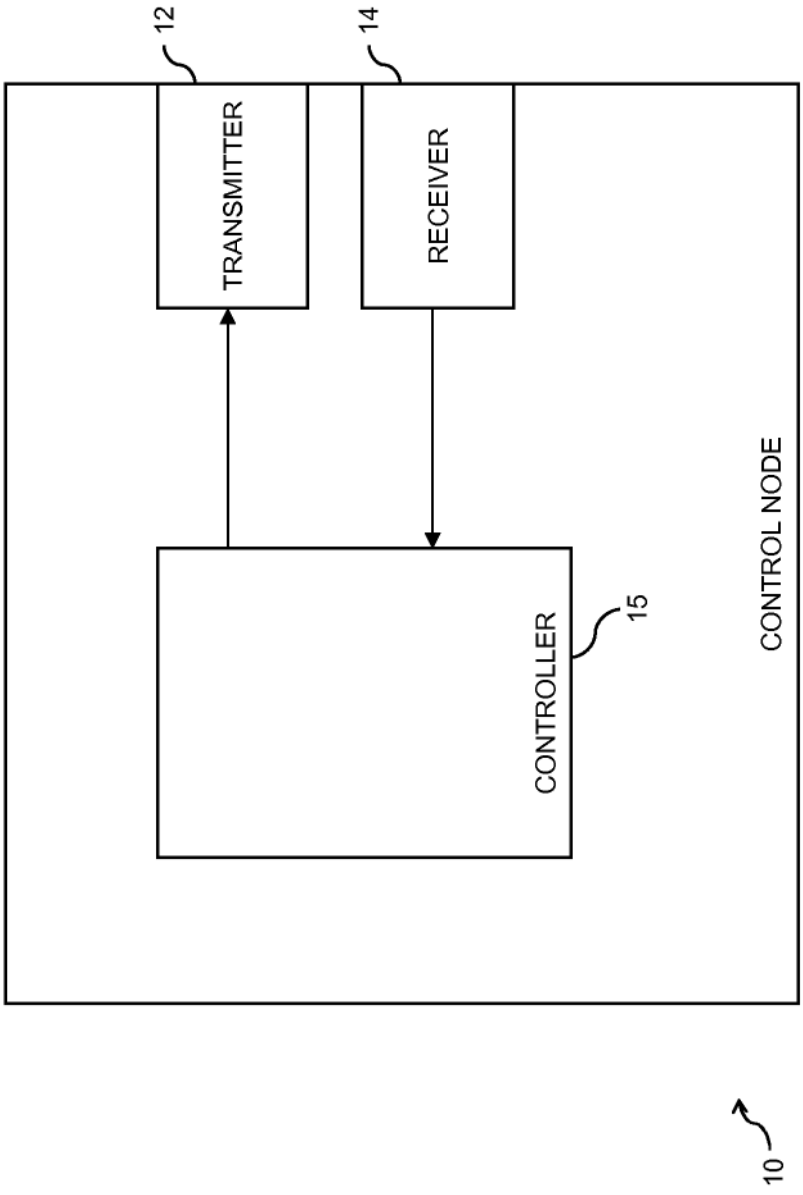

FIG. 6

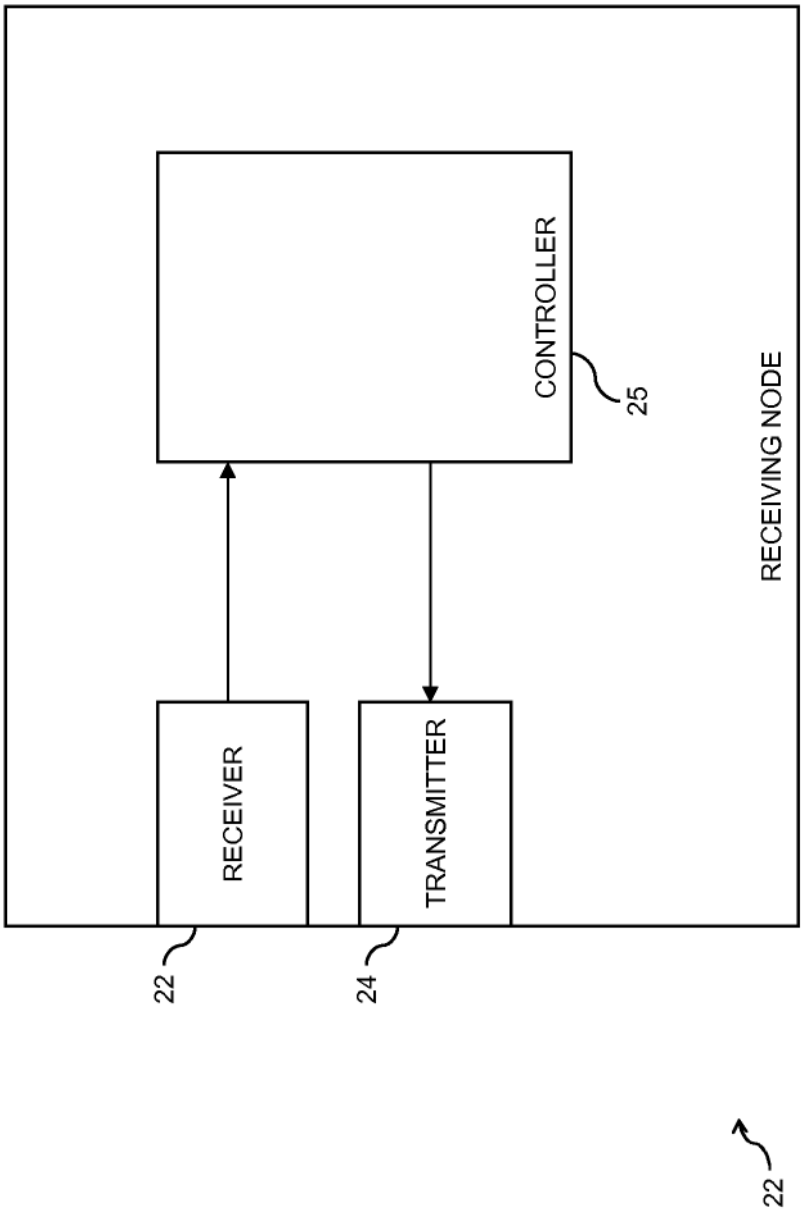
RECEIVING NODE
CONTROLLER
25
RECEIVER
TRANSMITTER
22
24
22

FIG. 7

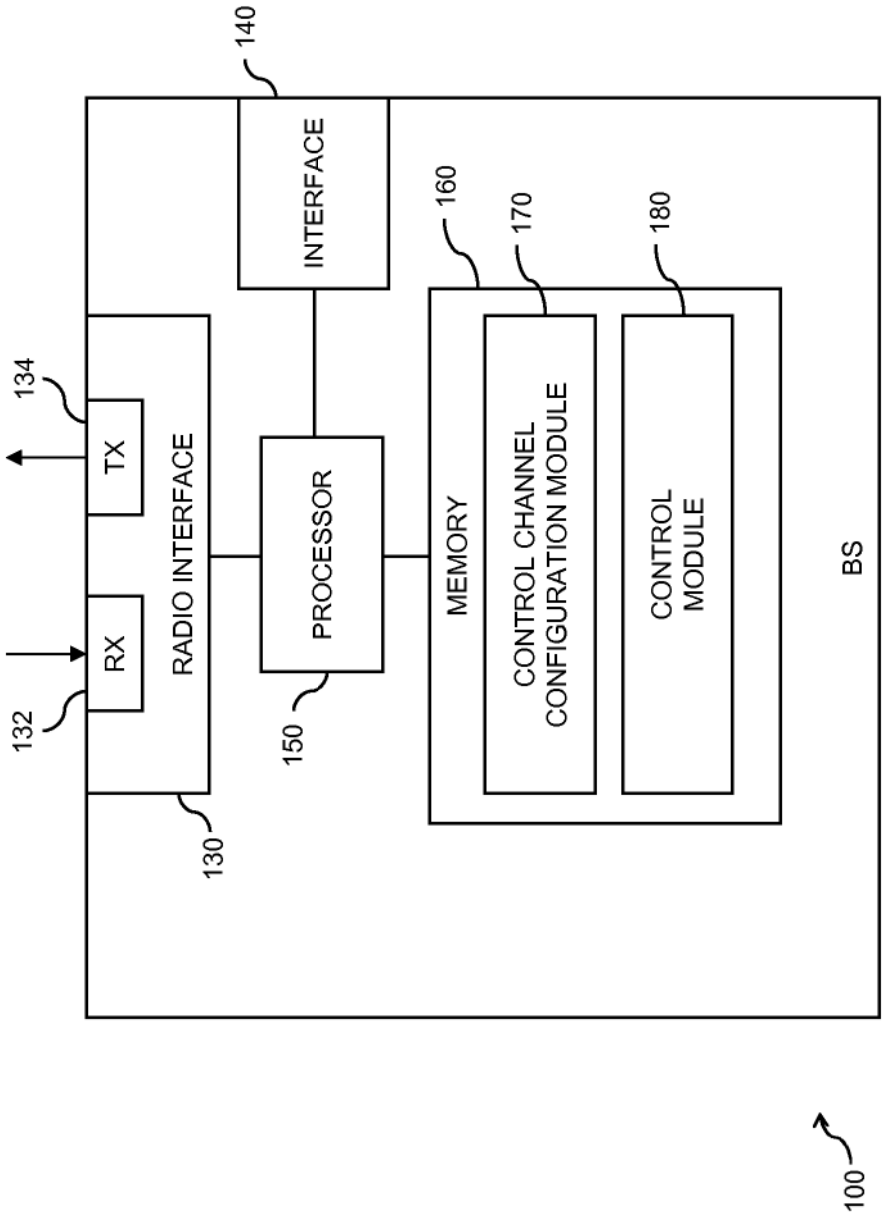
140
INTERFACE
160
170
180
134
TX
RADIO INTERFACE
PROCESSOR
MEMORY
CONTROL CHANNEL CONFIGURATION MODULE
CONTROL MODULE
BS
RX
132
150
130
100

FIG. 8

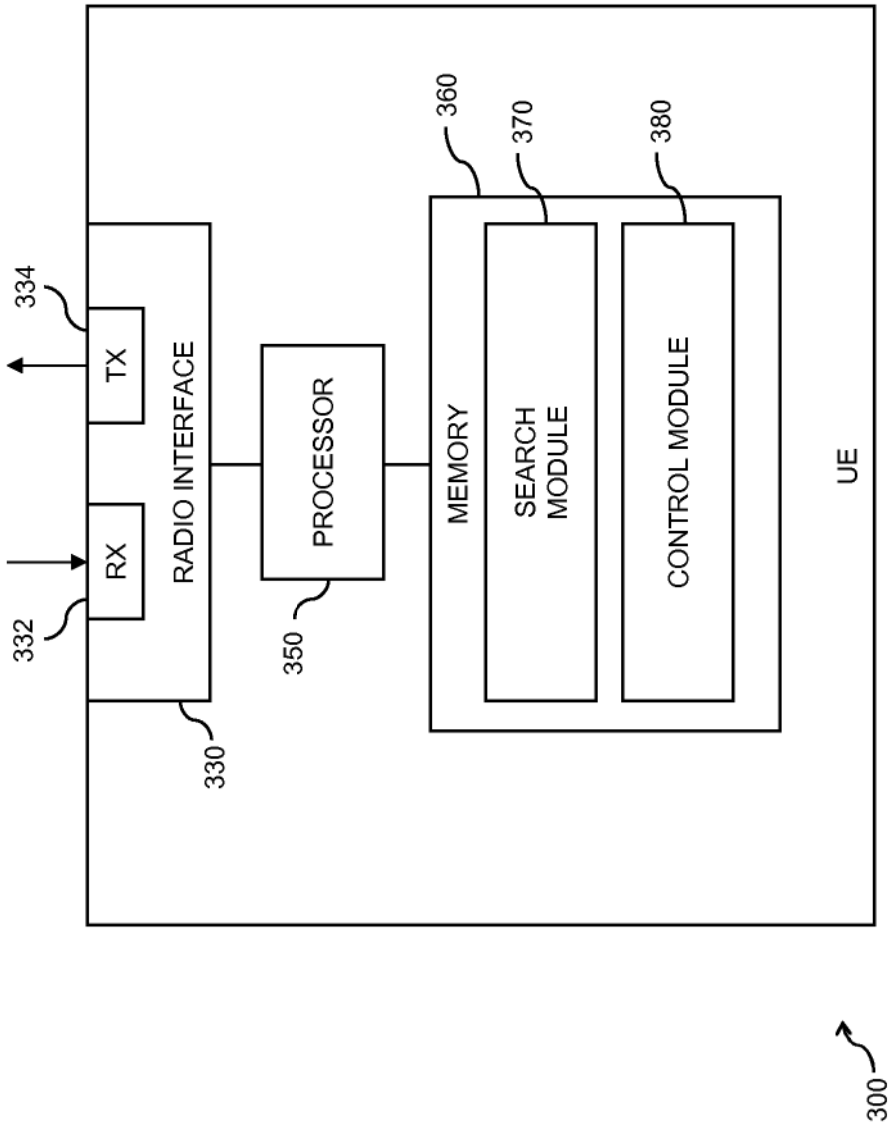
UE
MEMORY
SEARCH MODULE
CONTROL MODULE
PROCESSOR
RADIO INTERFACE
TX
RX
360
370
380
334
332
330
350
300

FIG. 9

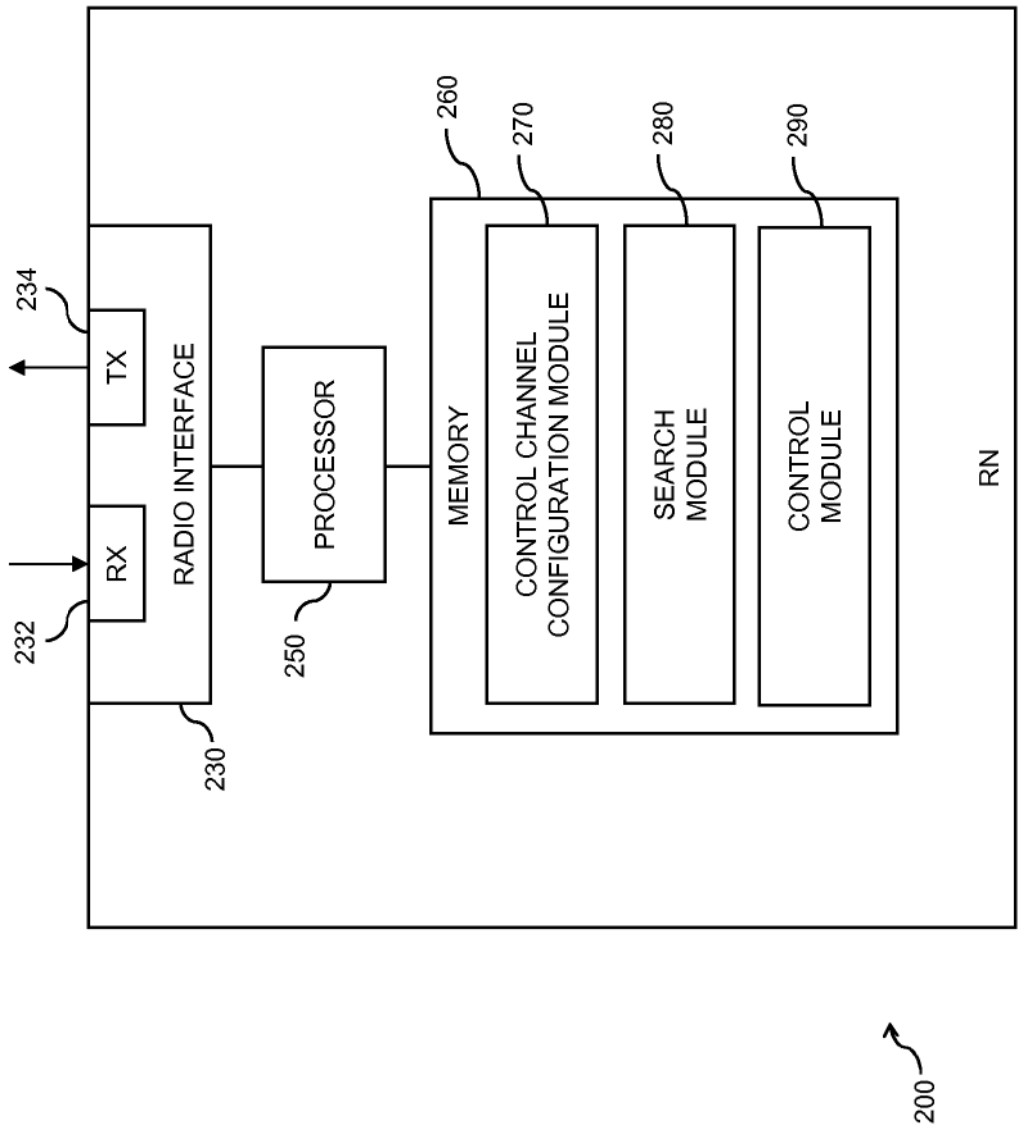
RN
RADIO INTERFACE
TX
RX
PROCESSOR
MEMORY
CONTROL CHANNEL CONFIGURATION MODULE
SEARCH MODULE
CONTROL MODULE
230
232
234
250
260
270
280
290
200

FIG. 10