ABSTRACT

STORAGE DEVICE, MEMORY DEVICE, SEMICONDUCTOR SYSTEM AND OPERATING METHOD OF MEMORY SYSTEM

The present disclosure relates to a semiconductor system including a semiconductor device and a controller. The semiconductor device outputs a temperature code corresponding to an internal temperature thereof. The controller controls, based on the temperature code, the semiconductor device to set a temperature measurement mode among at least two temperature measurement modes having different temperature measurement periods and to measure the internal temperature in the set temperature measurement mode.

FIG. 2