

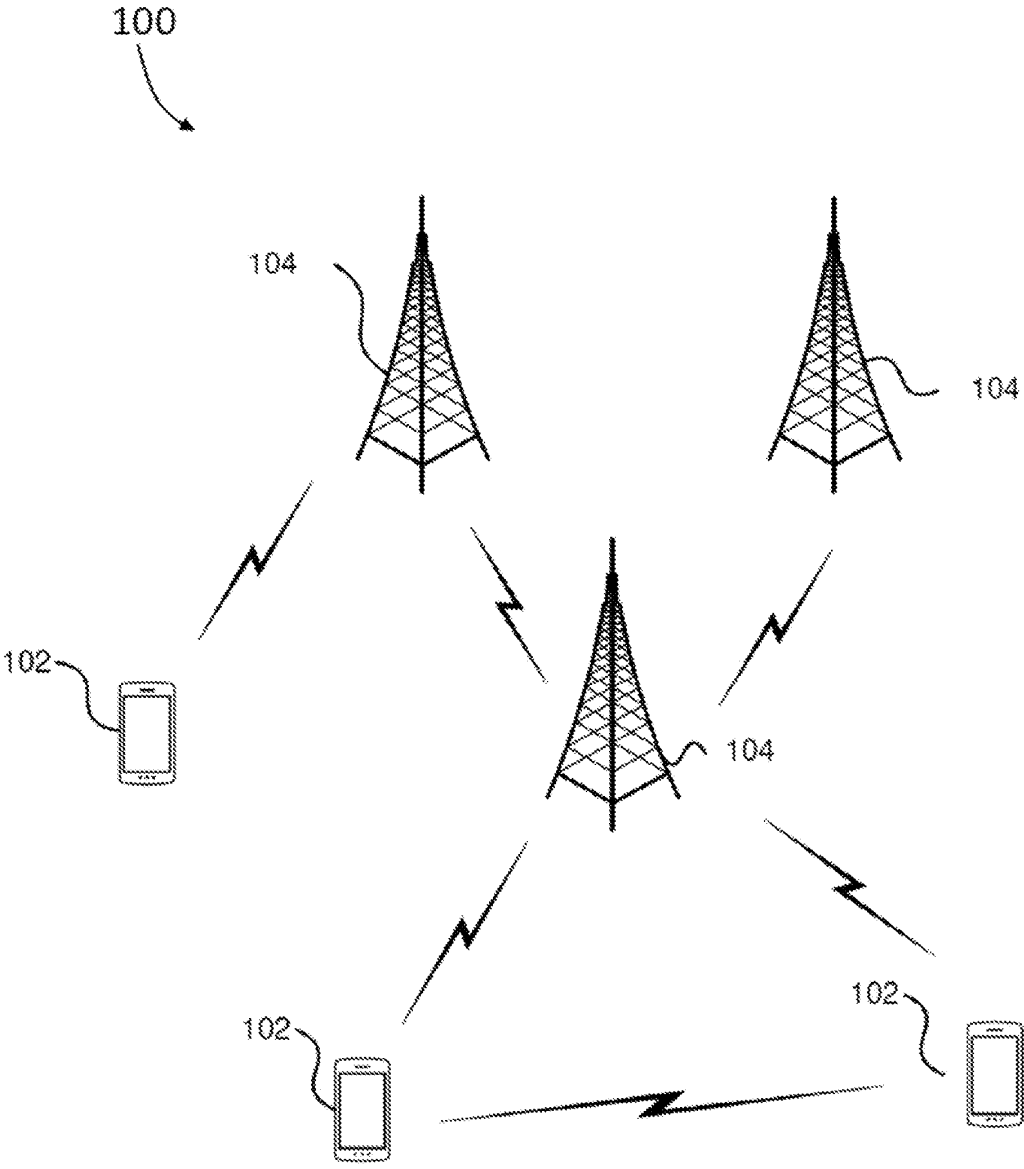


Figure 1

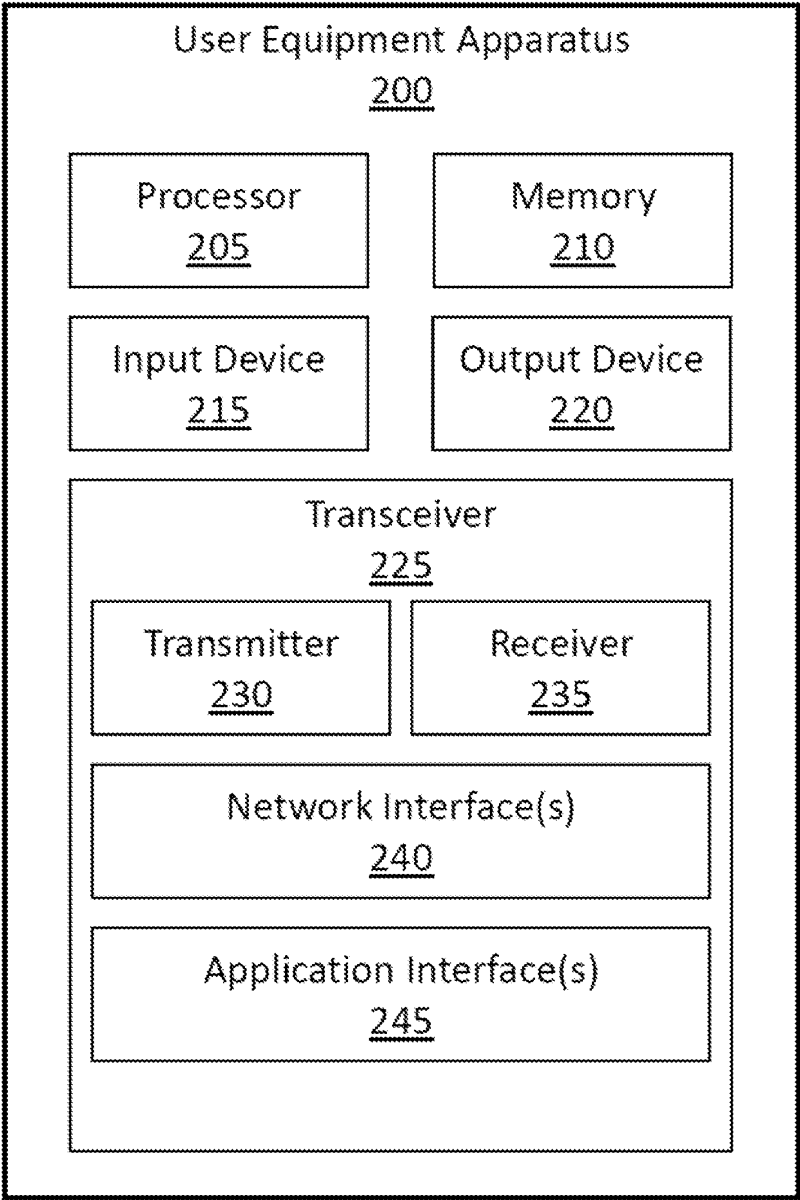


Figure 2

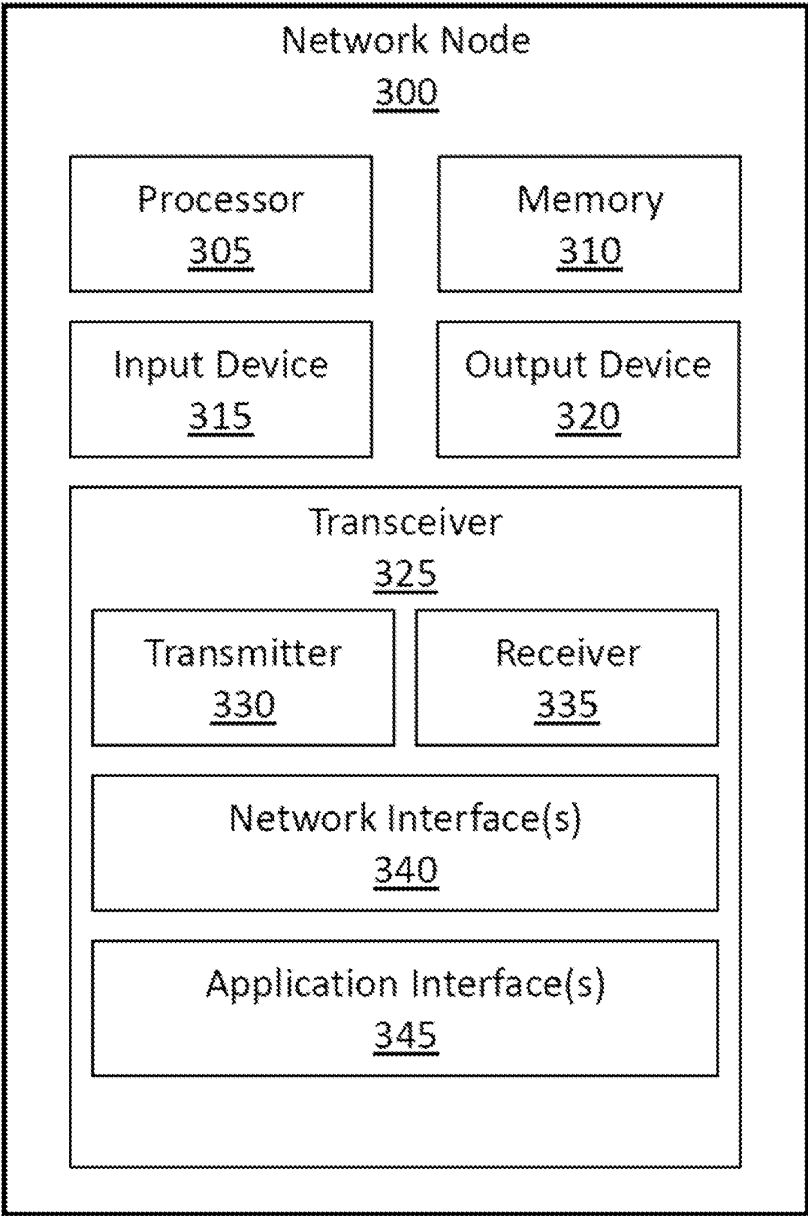


Figure 3

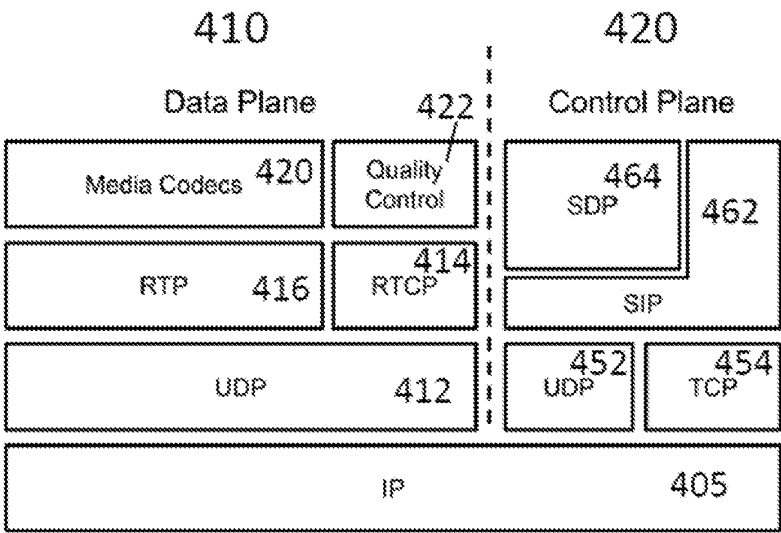


Figure 4

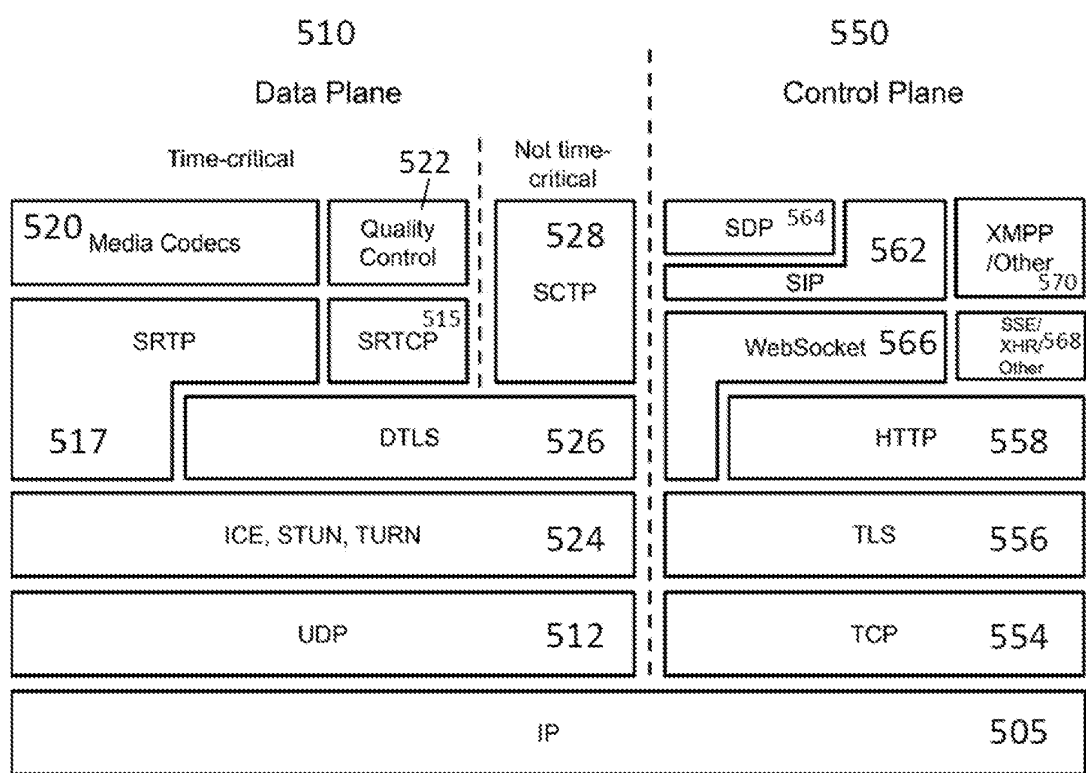


Figure 5

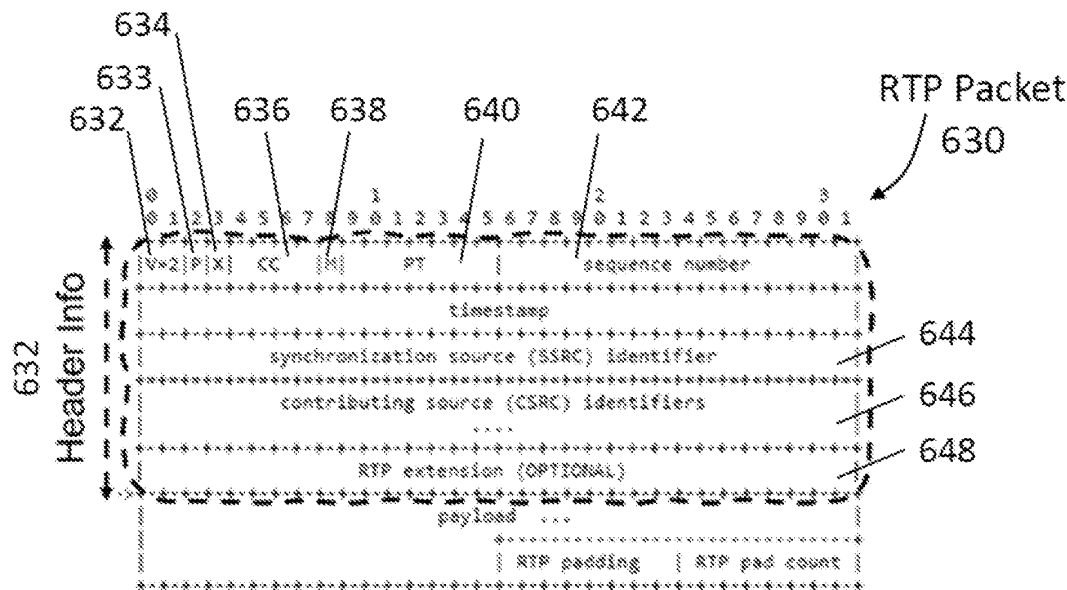


Figure 6a

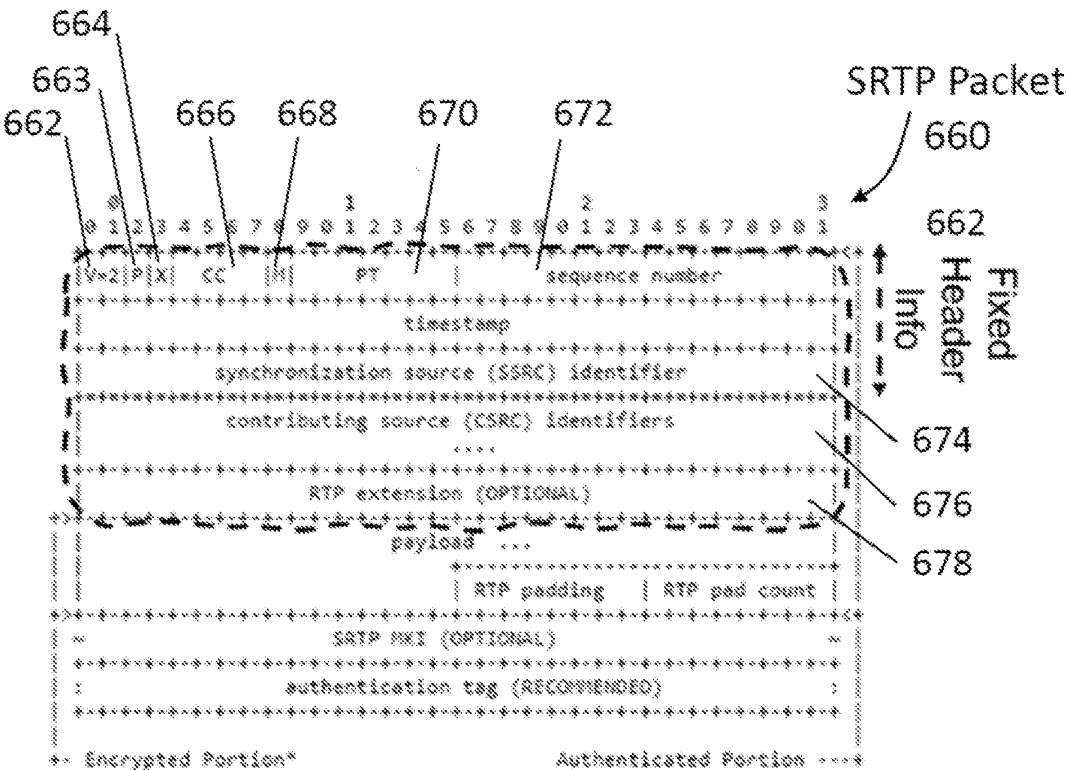


Figure 6b

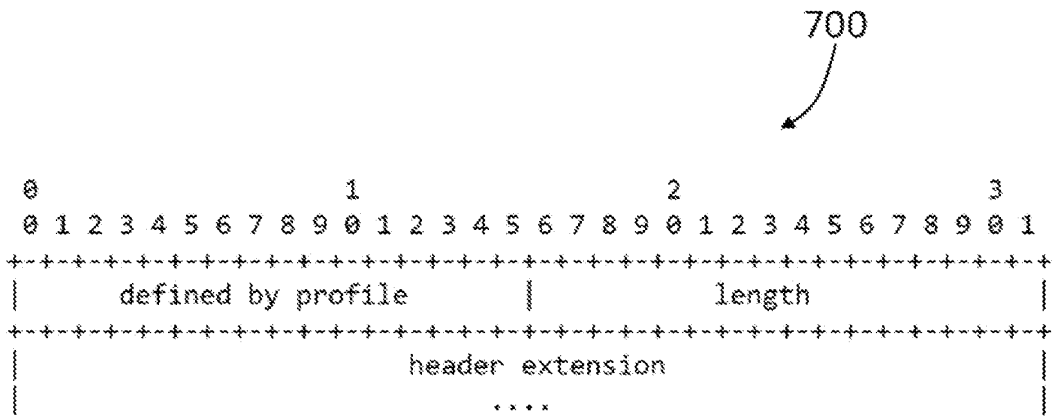


Figure 7

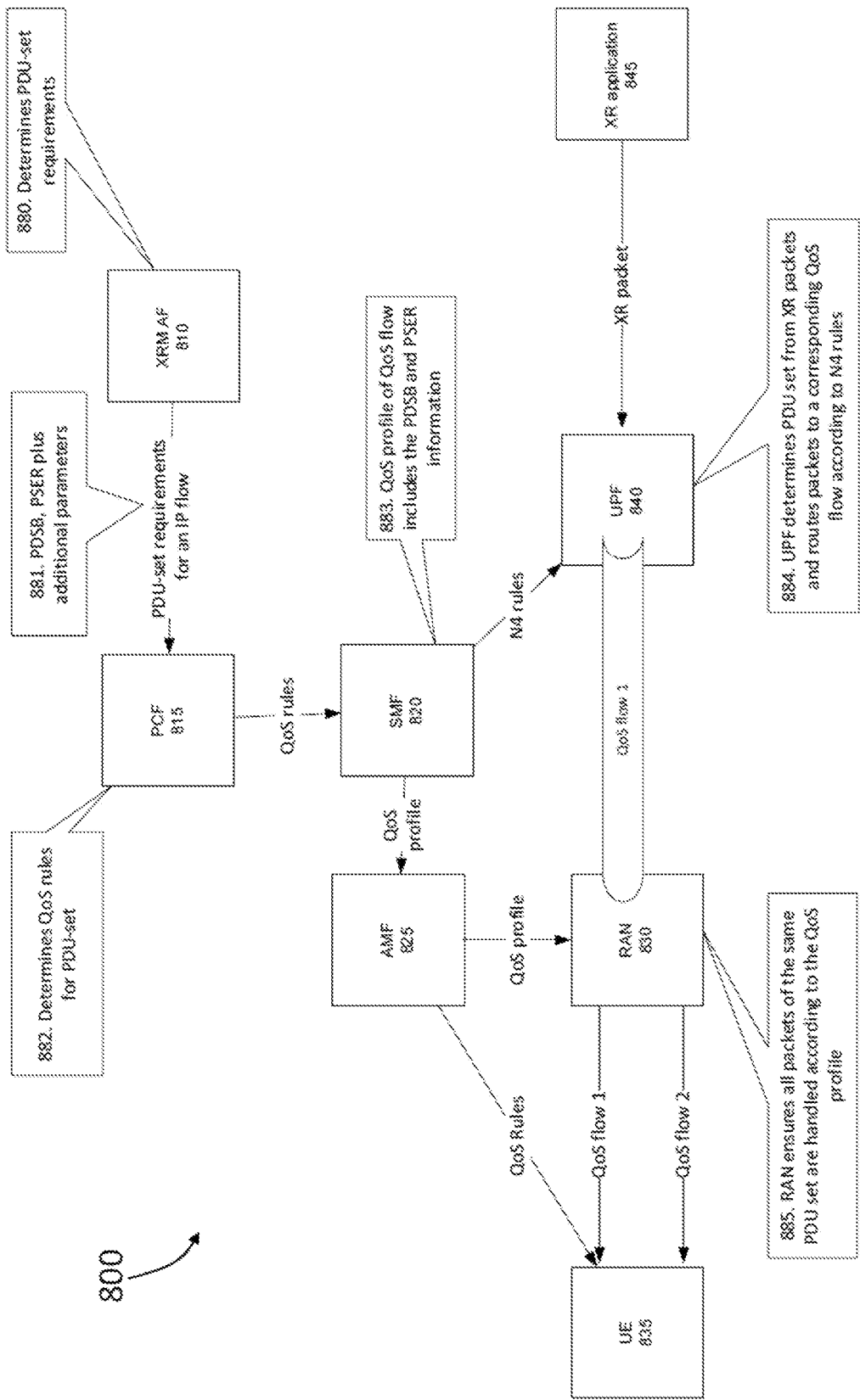
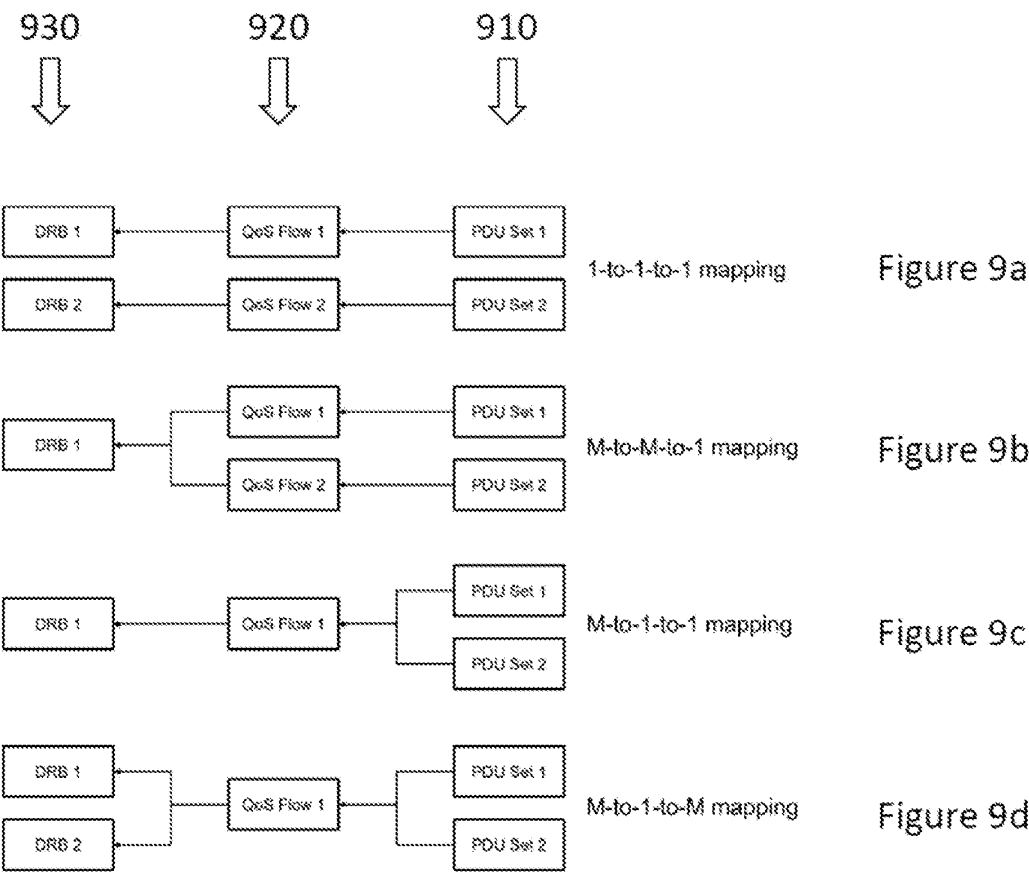


Figure 8



DRB 1

QoS Flow 1

QoS Flow 2

PDU Set 1

PDU Set 2

M-to-M-to-1 mapping

Figure 9b

DRB 1

QoS Flow 1

PDU Set 1

PDU Set 2

M-to-1-to-1 mapping

Figure 9c

DRB 1

DRB 2

QoS Flow 1

PDU Set 1

PDU Set 2

M-to-1-to-M mapping

Figure 9d

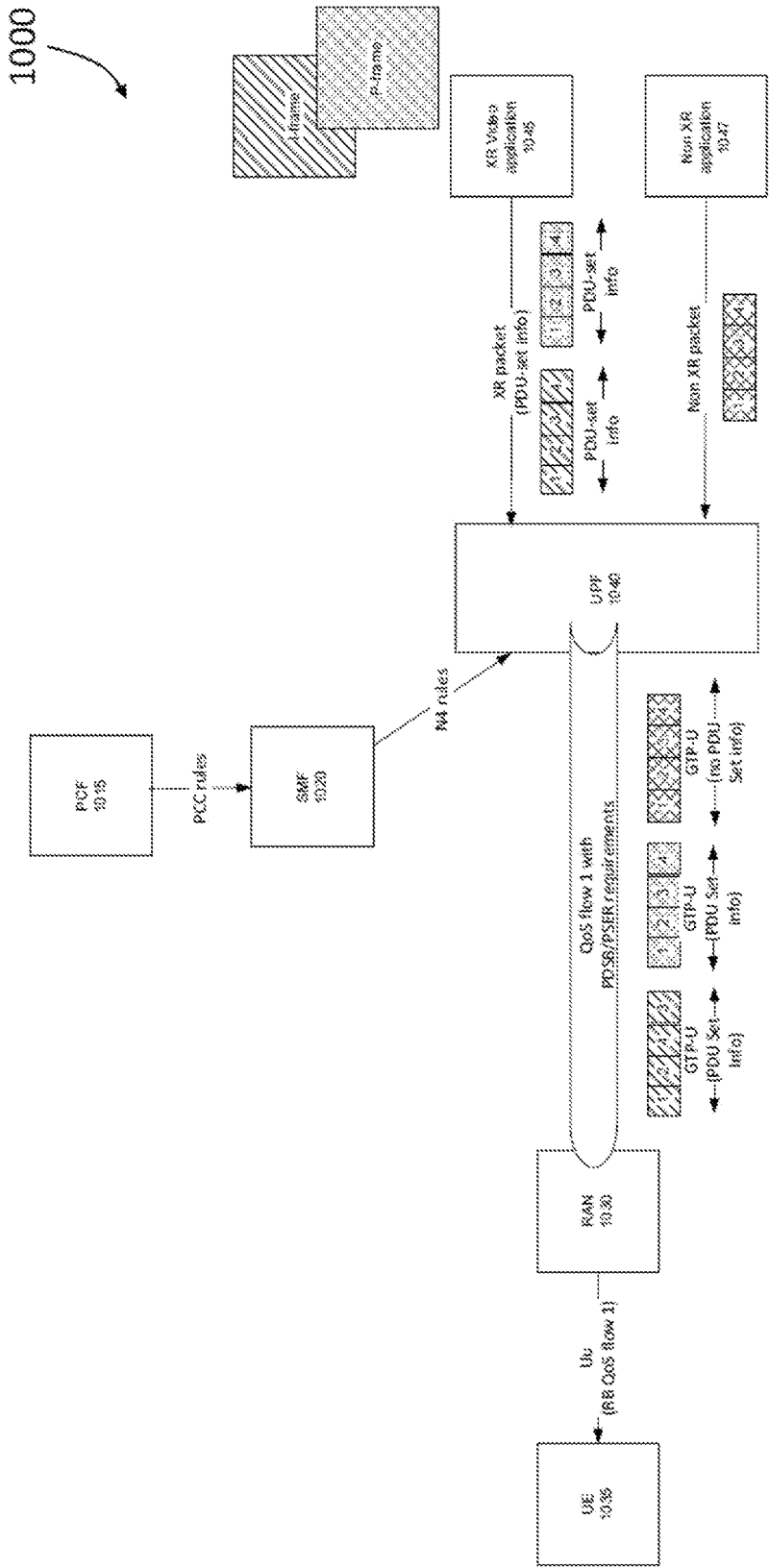


Figure 10

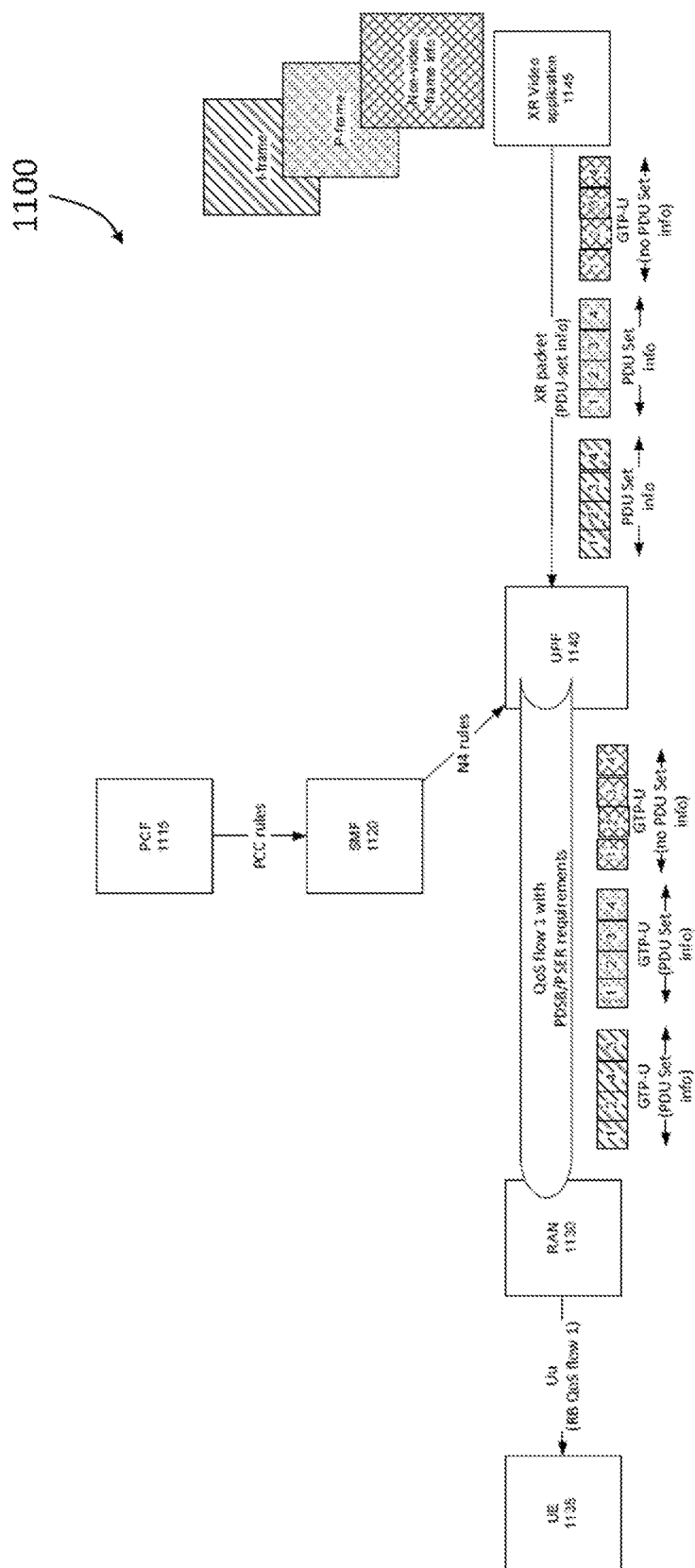


Figure 11

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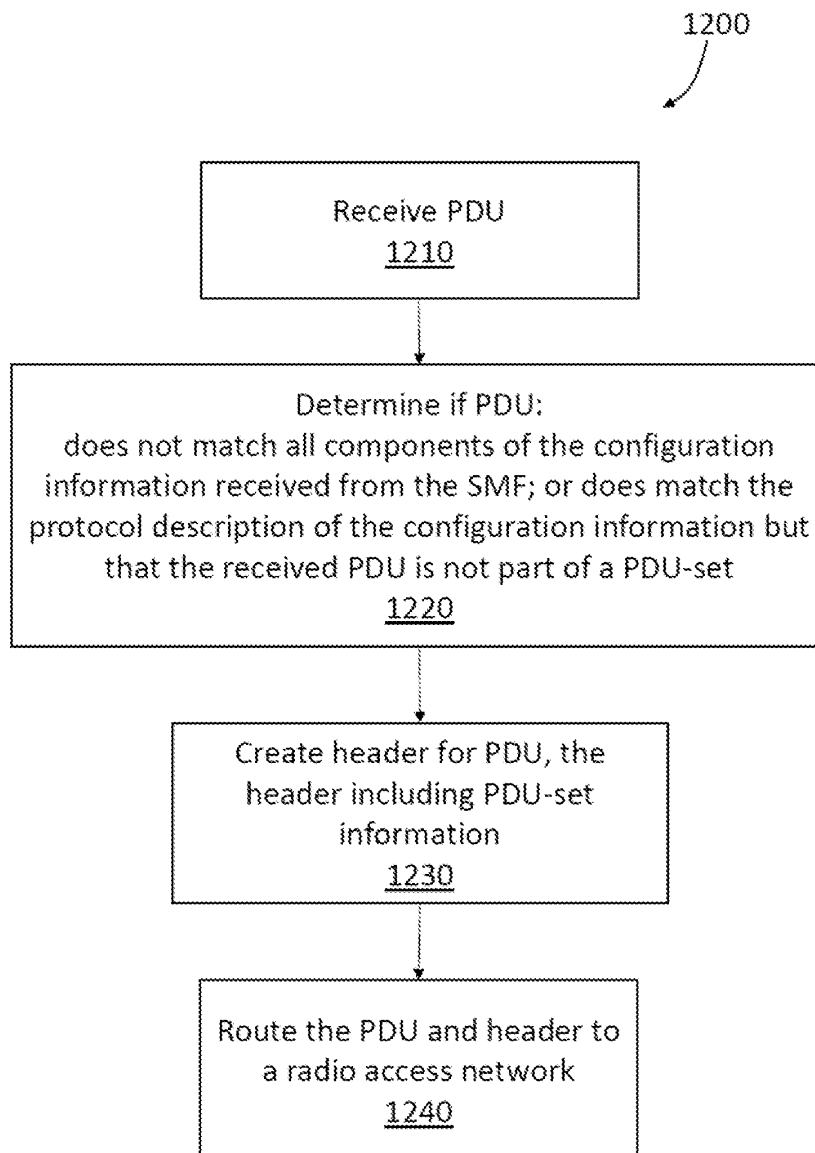


Figure 12