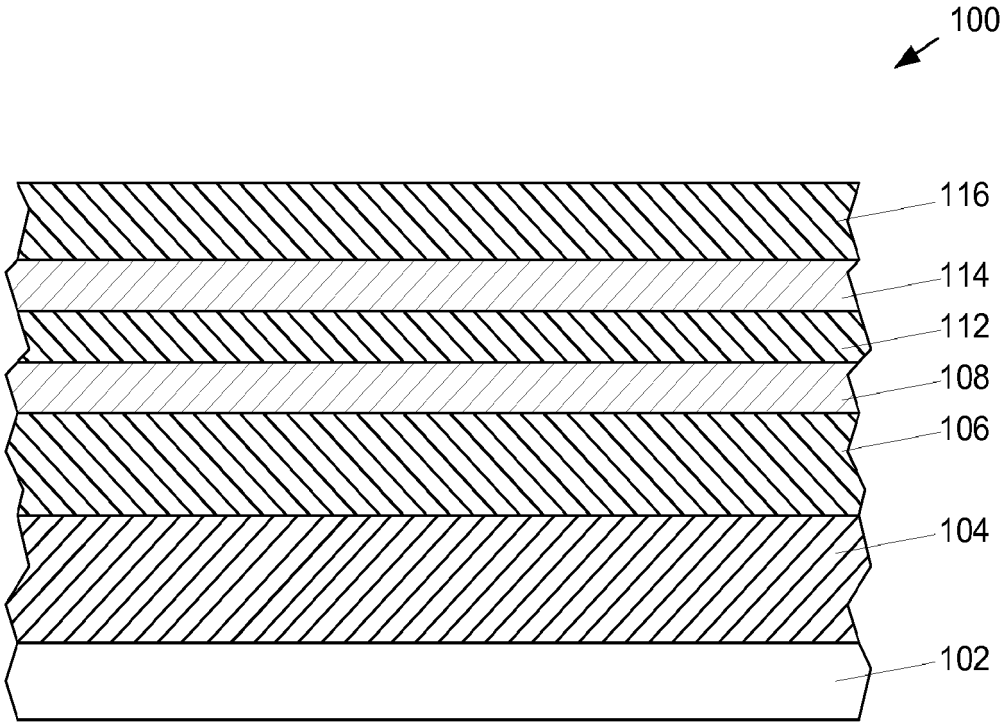

100
116
114
112
108
106
104
102

FIG. 1

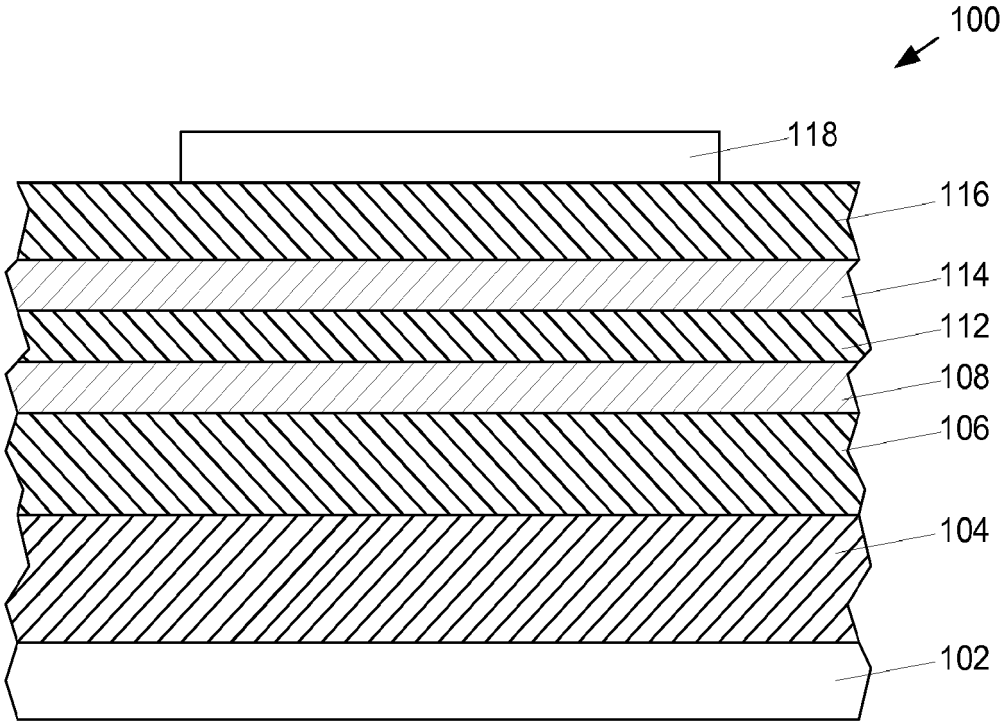

100
118
116
114
112
108
106
104
102

FIG. 2

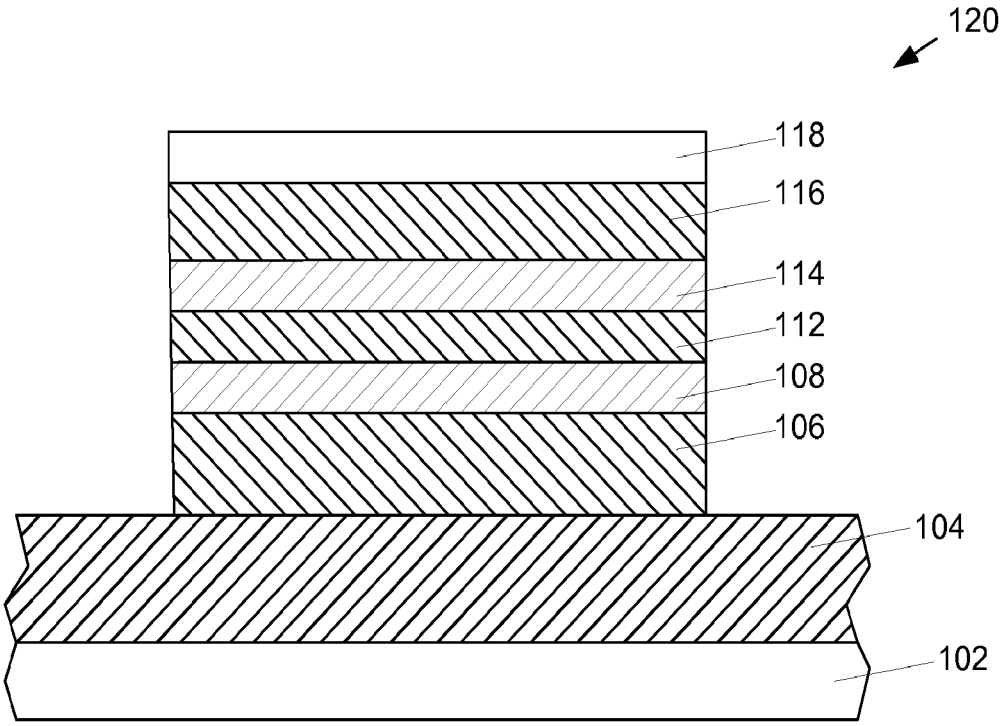

120
118
116
114
112
108
106
104
102

FIG. 3

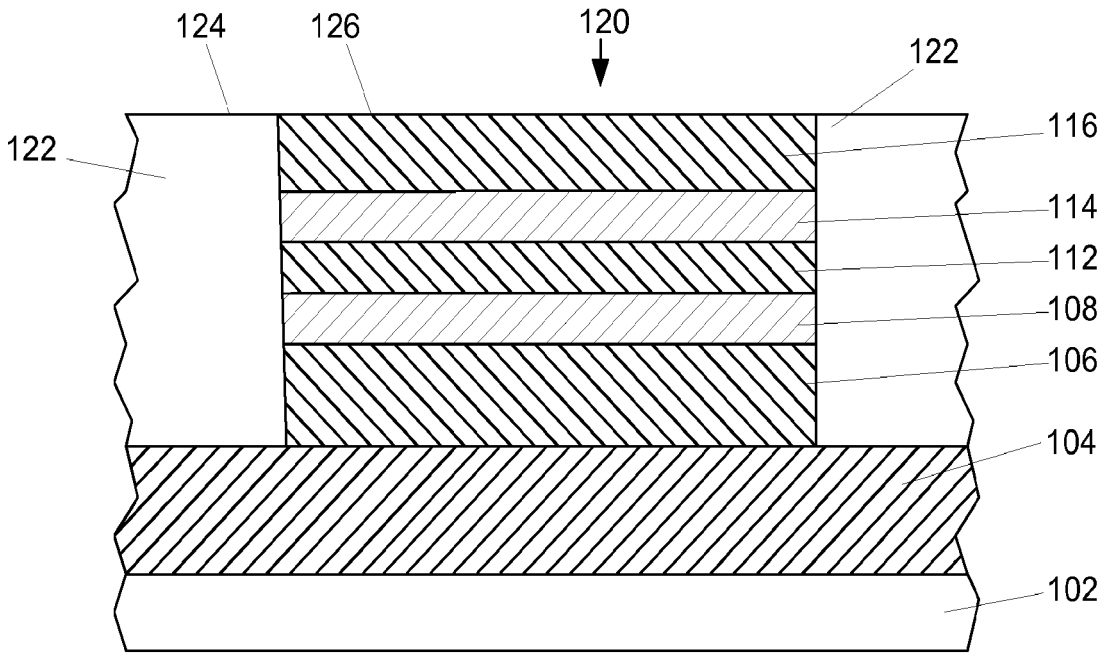

124
126
120
122
122
116
114
112
108
106
104
102

FIG. 4

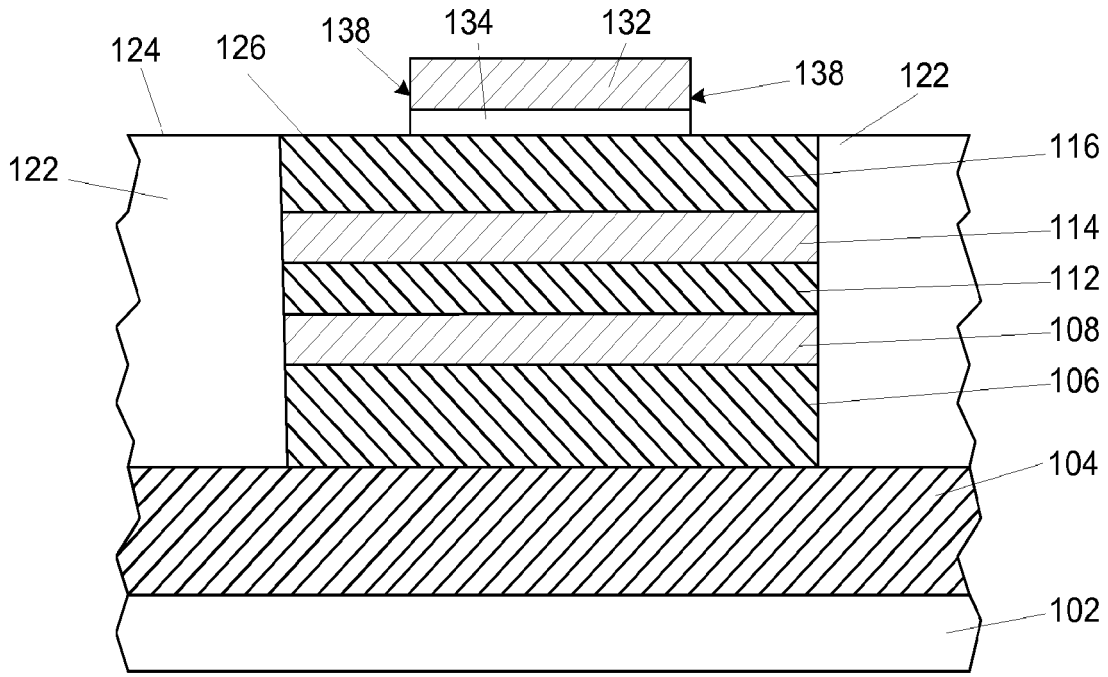

124
126
138
134
132
138
122
122
116
114
112
108
106
104
102

FIG. 5

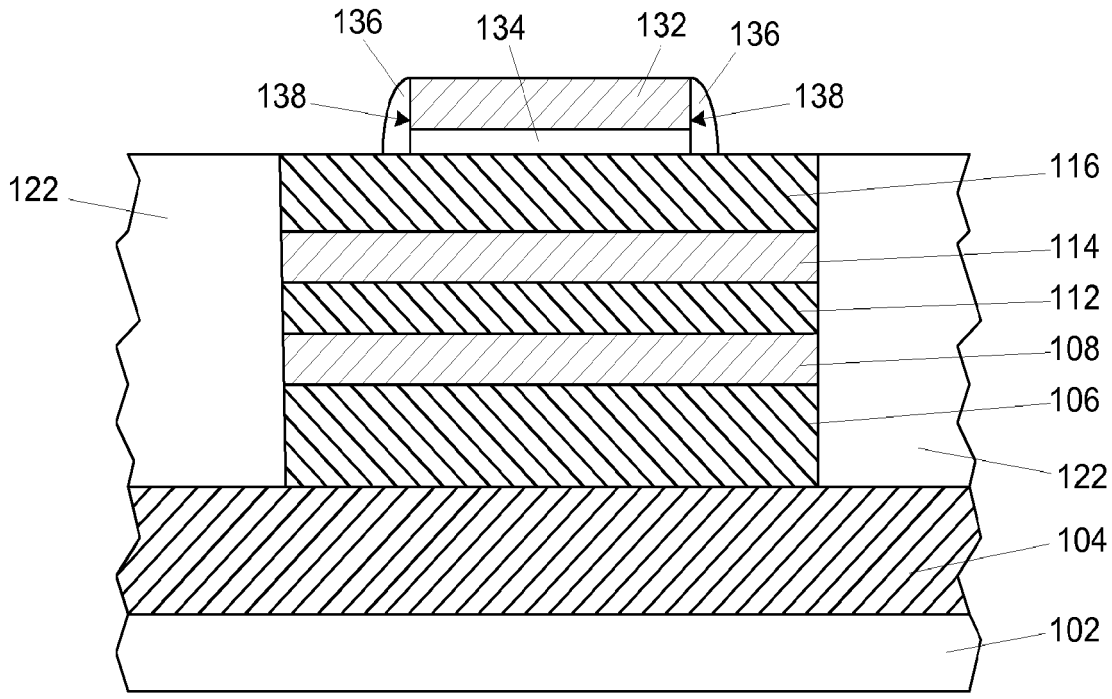

136
134
132
136
138
138
122
116
114
112
108
106
122
104
102

FIG. 6

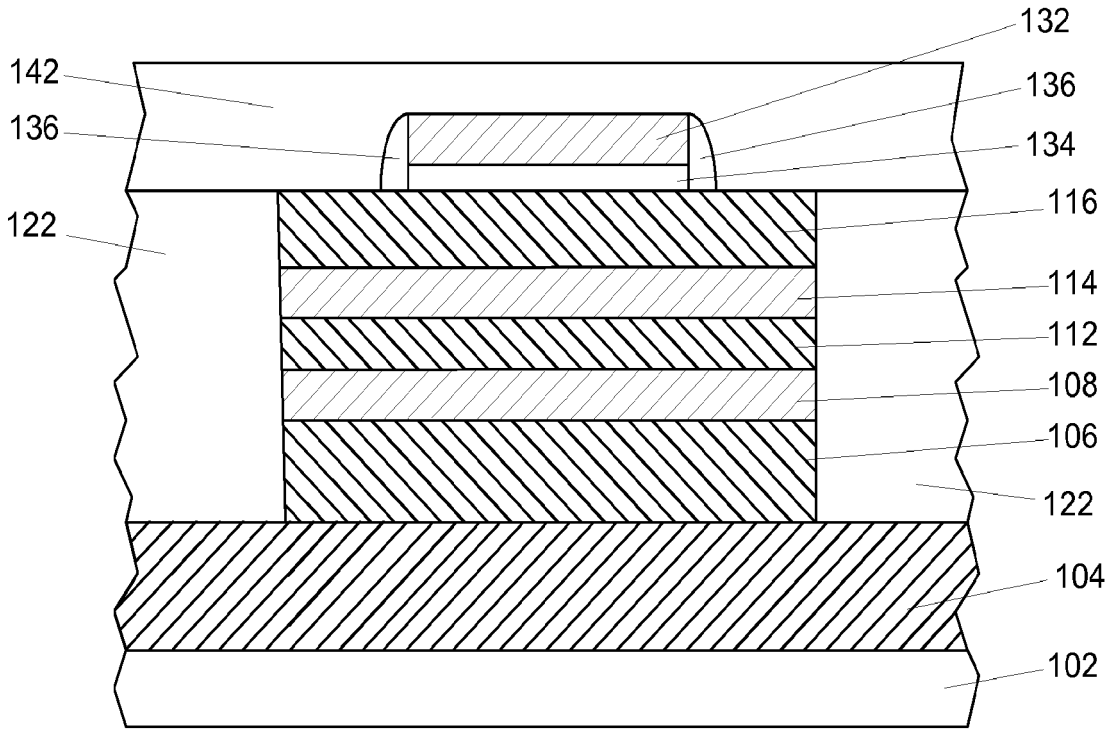

132
142
136
136
134
122
116
114
112
108
106
122
104
102

FIG. 7

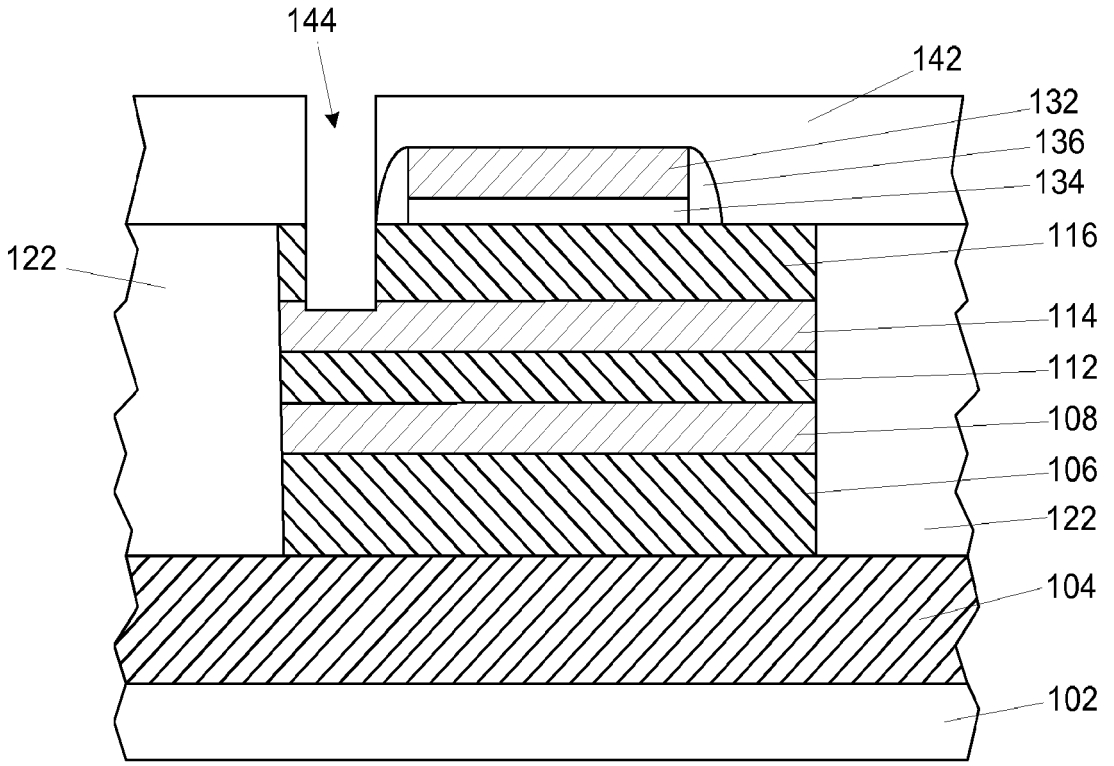

144
142
132
136
134
122
116
114
112
108
106
122
104
102

FIG. 8

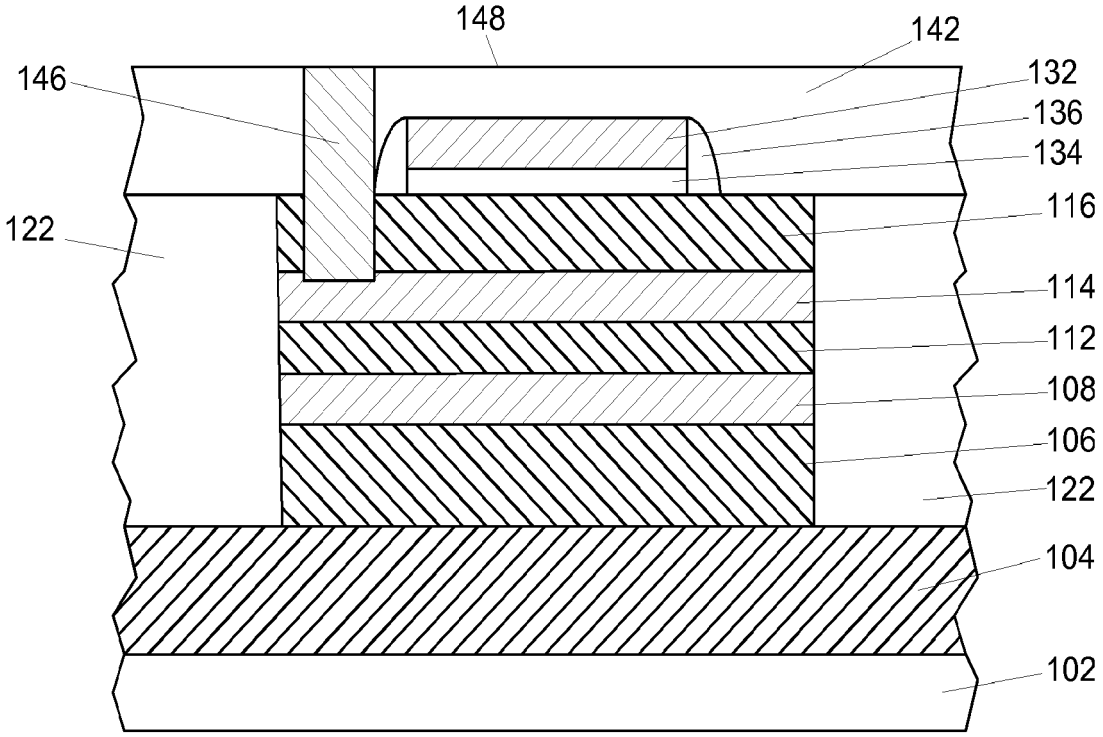

148
142
146
132
136
134
122
116
114
112
108
106
122
104
102

FIG. 9

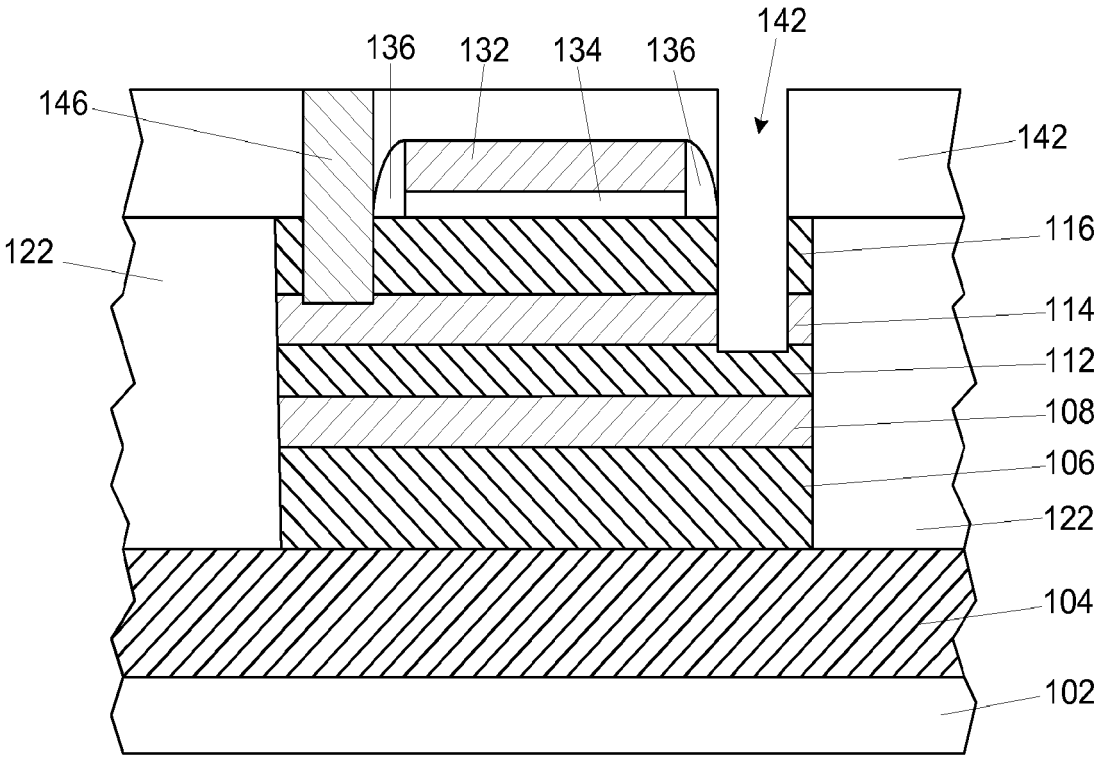

142
136
132
134
136
146
142
122
116
114
112
108
106
122
104
102

FIG. 10

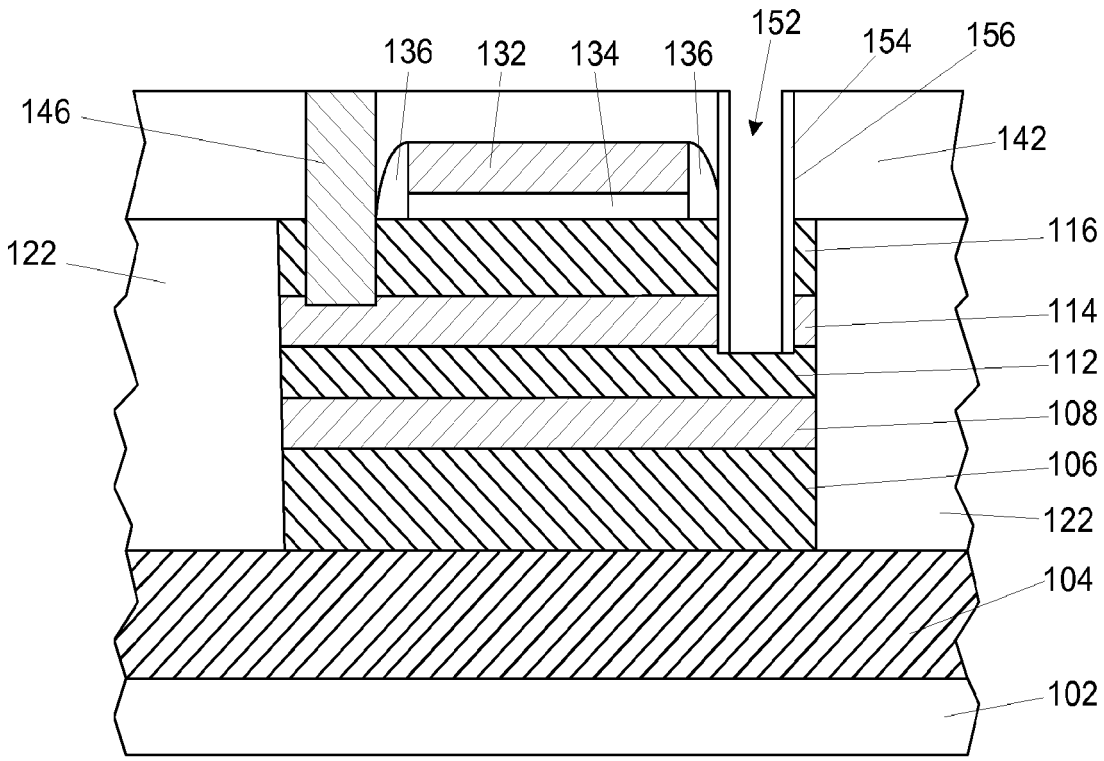

152
136
132
134
136
154
156
146
142
122
116
114
112
108
106
122
104
102

FIG. 11

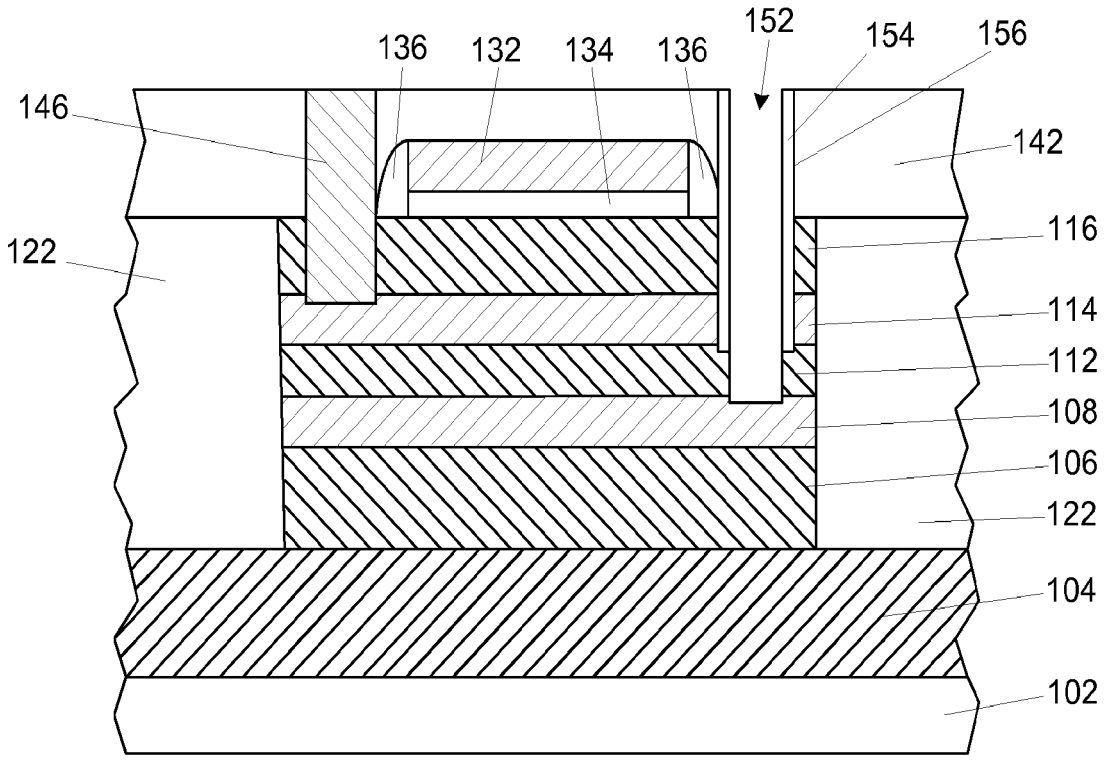

152
136
132
134
136
154
156
146
142
122
116
114
112
108
106
122
104
102

FIG. 12

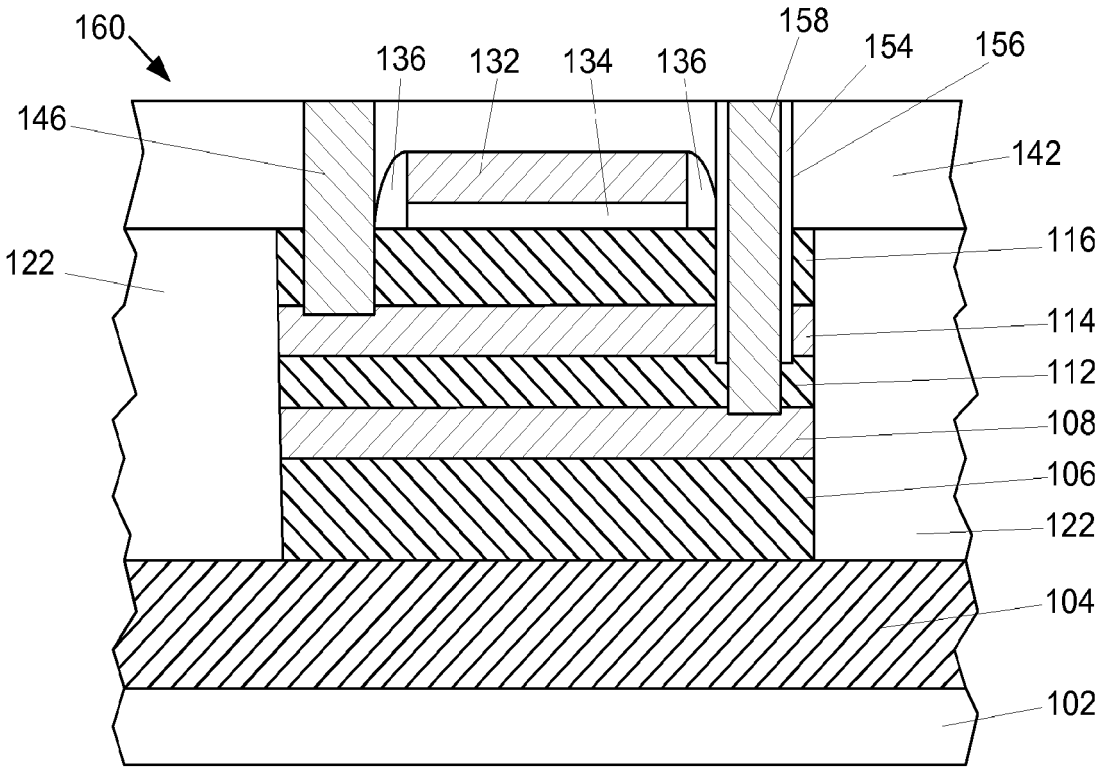

160
158
136
132
134
136
154
156
146
142
122
116
114
112
108
106
122
104
102

FIG. 13

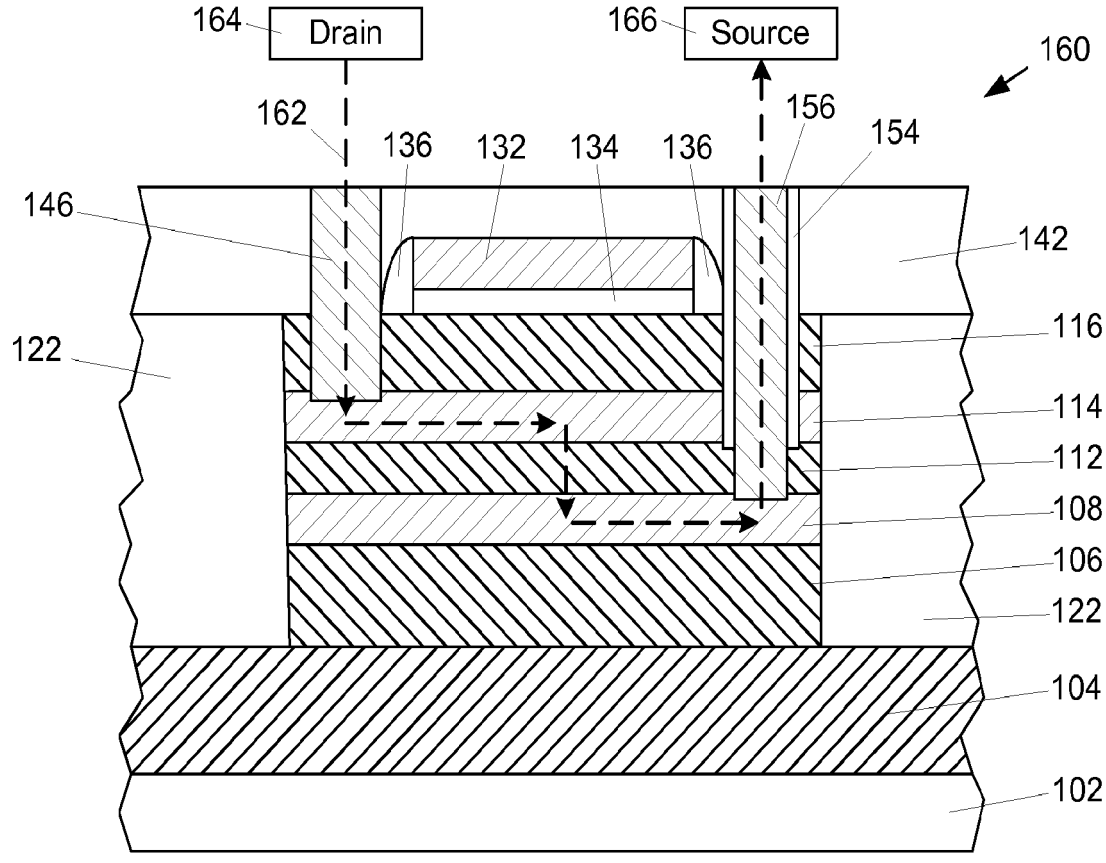

164
Drain
166
Source
160
162
156
154
136
132
134
136
146
142
122
116
114
112
108
106
122
104
102

FIG. 14

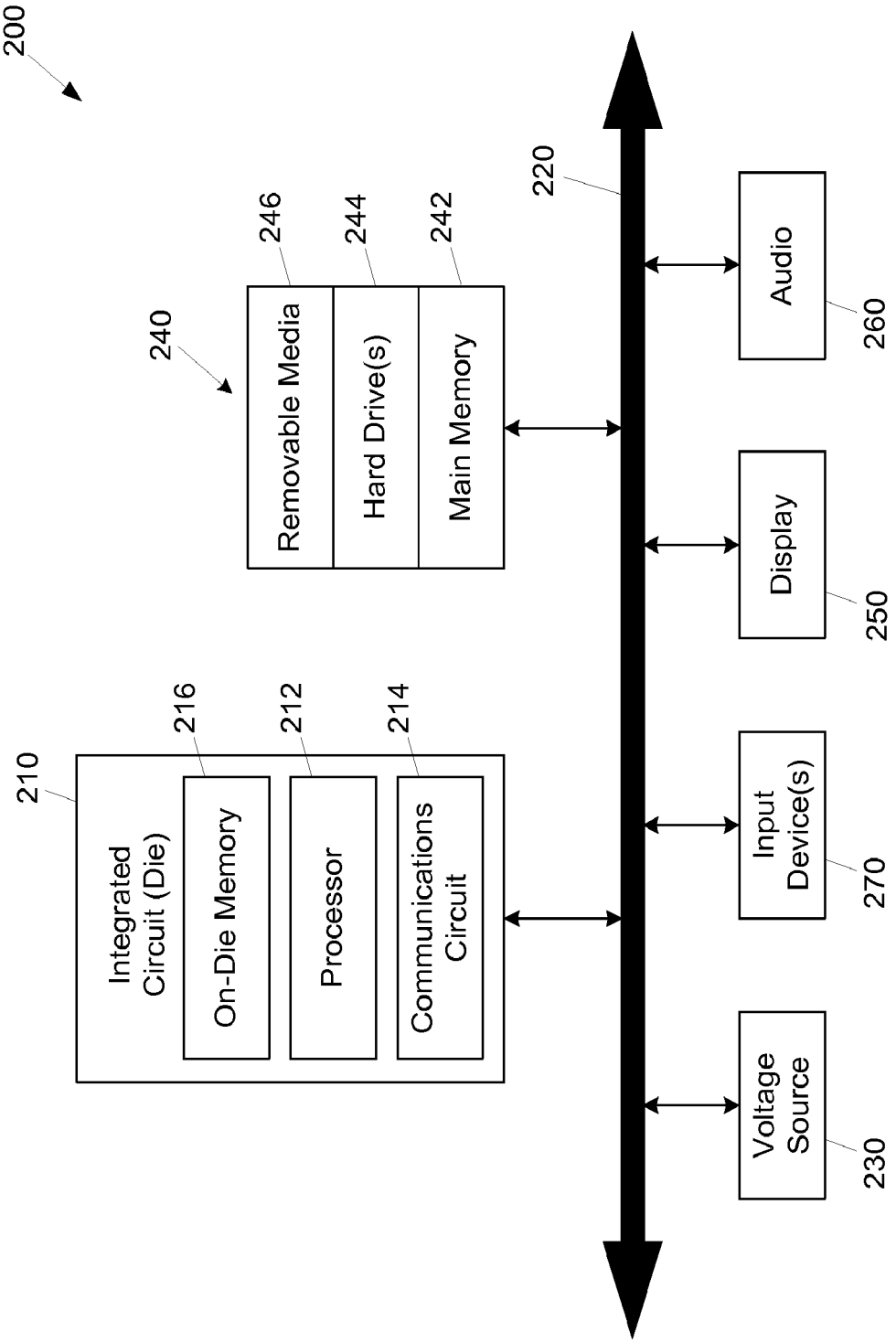

200
220
240
246
244
242
Removable Media
Hard Drive(s)
Main Memory
Audio
260
Display
250
Input Device(s)
270
Voltage Source
230
210
216
212
214
Integrated Circuit (Die)
On-Die Memory
Processor
Communications Circuit

FIG. 15