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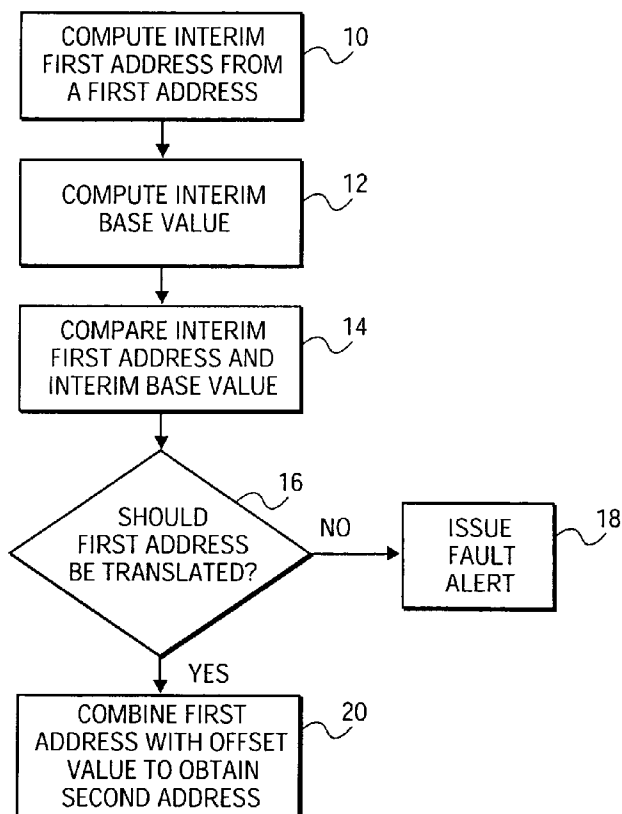
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(54) Title: METHOD AND APPARATUS FOR TRANSLATING GUEST PHYSICAL ADDRESSES IN A VIRTUAL MACHINE ENVIRONMENT



(57) Abstract: A method and an apparatus are used to efficiently translate memory addresses. The translation scheme yields a translated address, a memory type for the translated address, and a fault bit for the translation.

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