

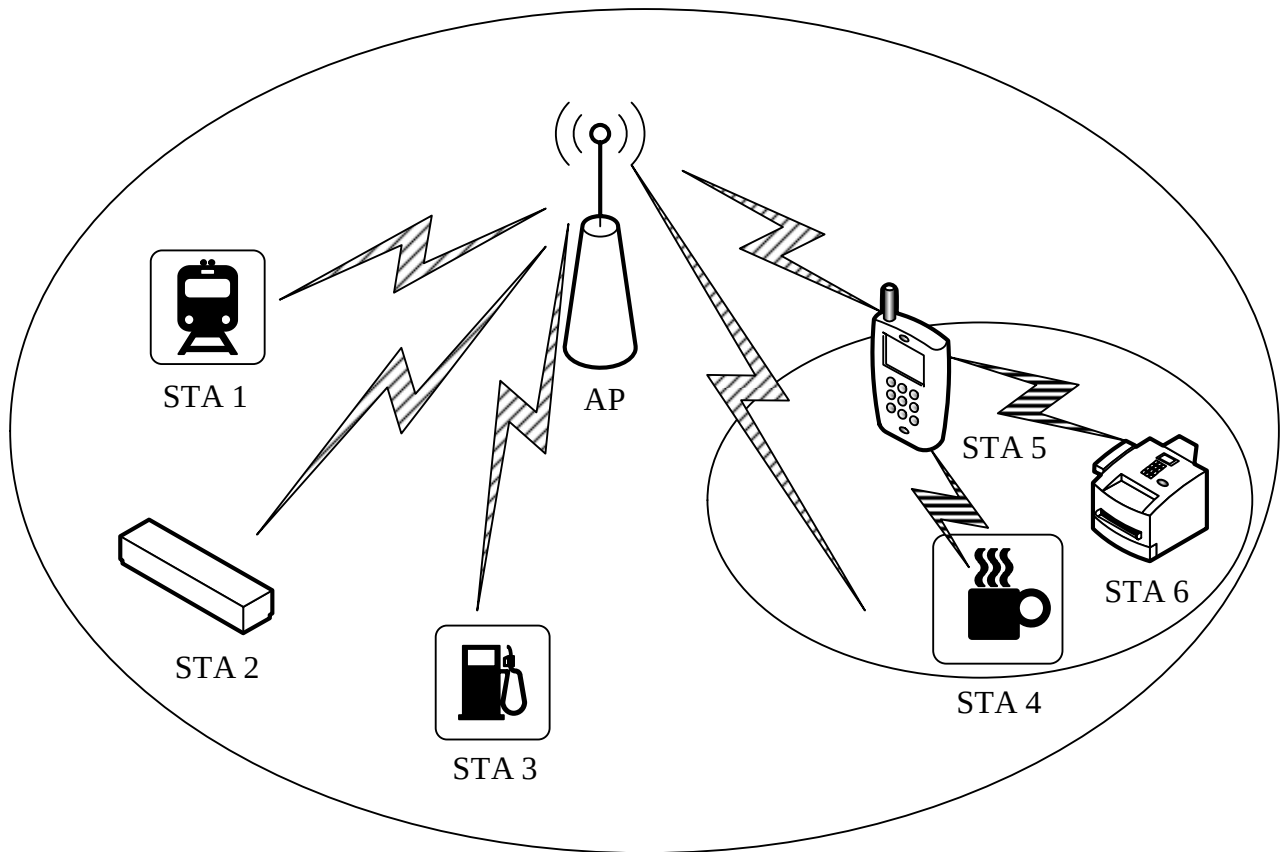


FIG. 1

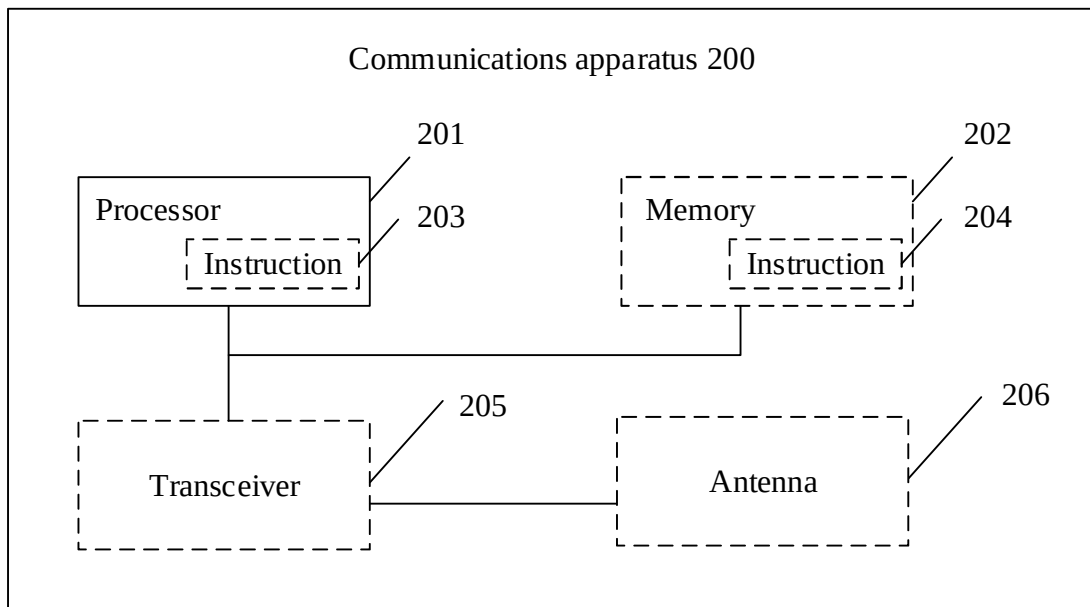


FIG. 2

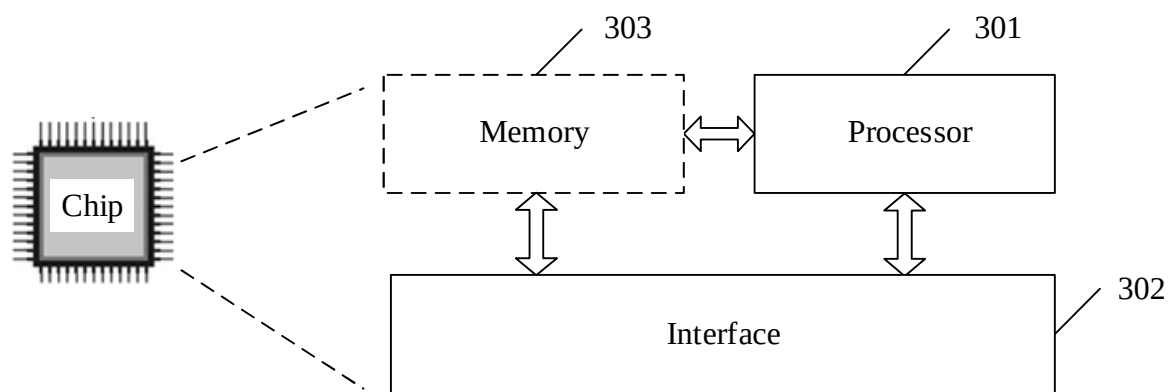


FIG. 3

Octets	2	2	6	0 or 6	0 or 6	0 or 2	0 or 6	0 or 2	0 or 4	Variable	4
Octets:	Frame control	Duration/ ID	A1 Address 1	A2 Address 2	A3 Address 3	Sequence control	A4 Address 4	QoS control	HT control	Frame body	FCS
	Frame control	Duration/ ID				Sequence control		QoS control	High throughput control	Frame body	Frame check sequence

FIG. 4

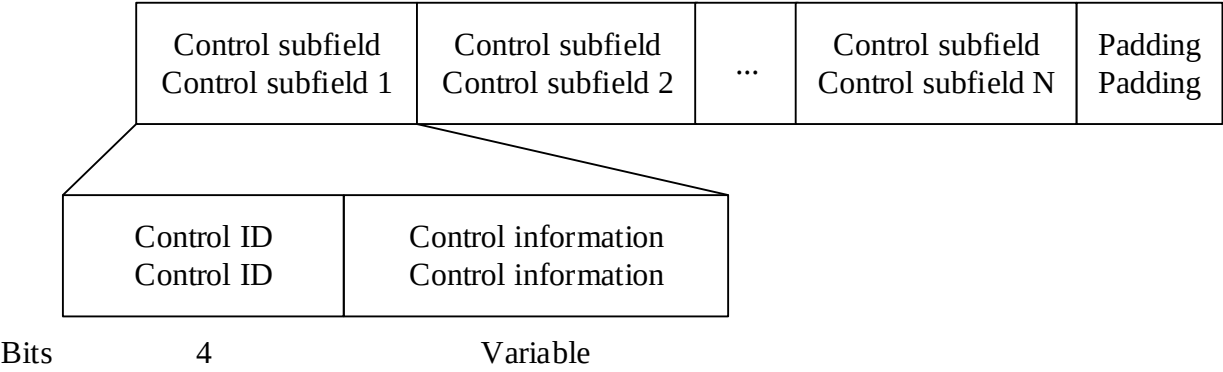


FIG. 5

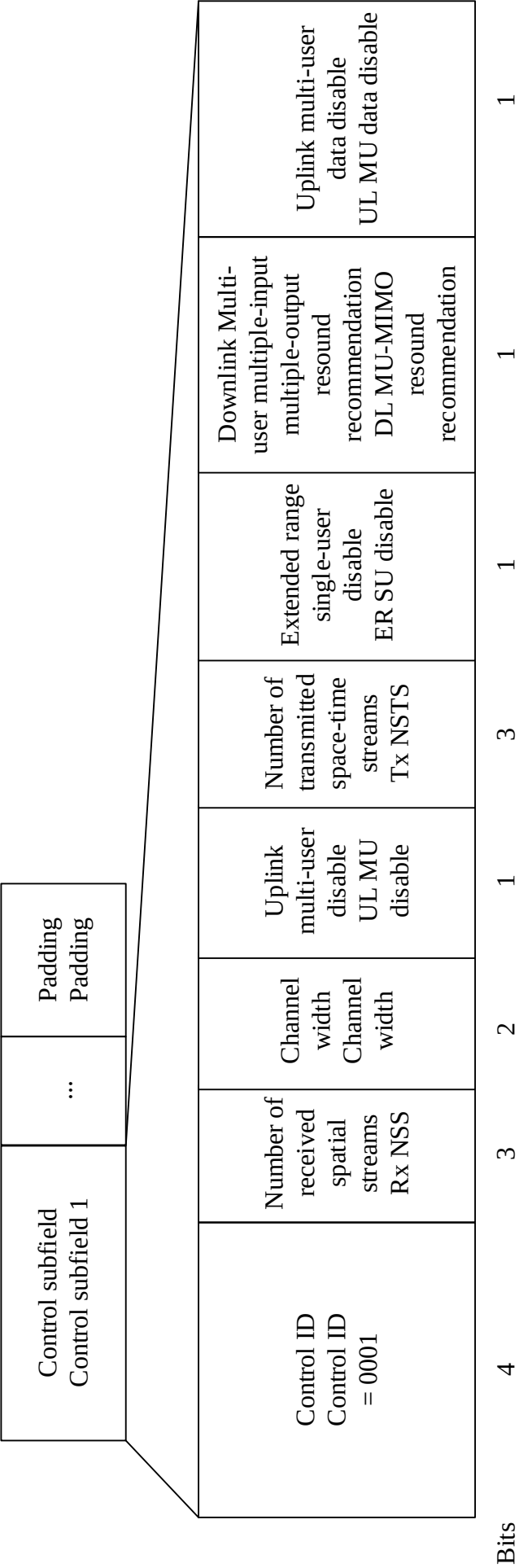


FIG. 6

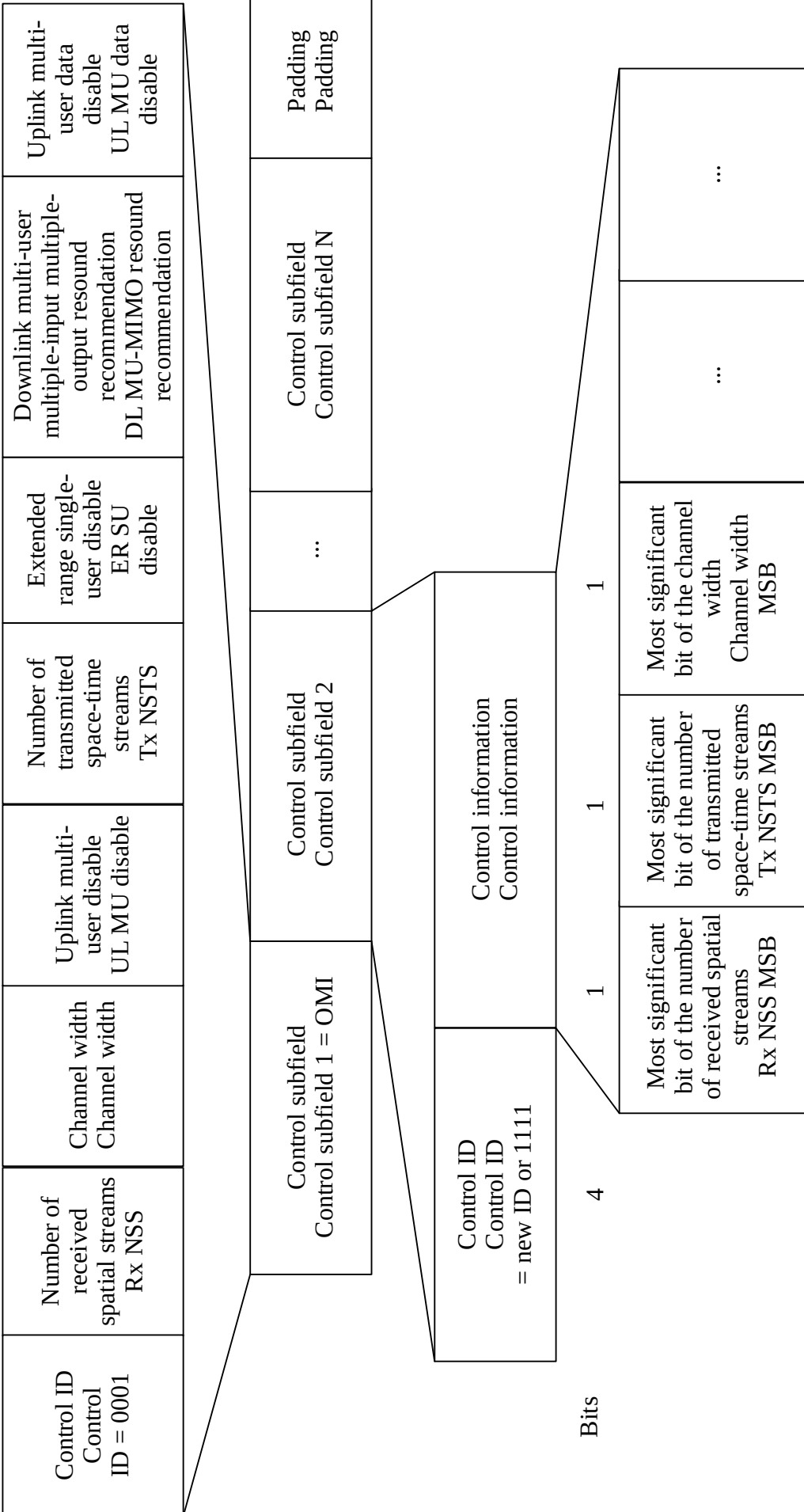


FIG. 7

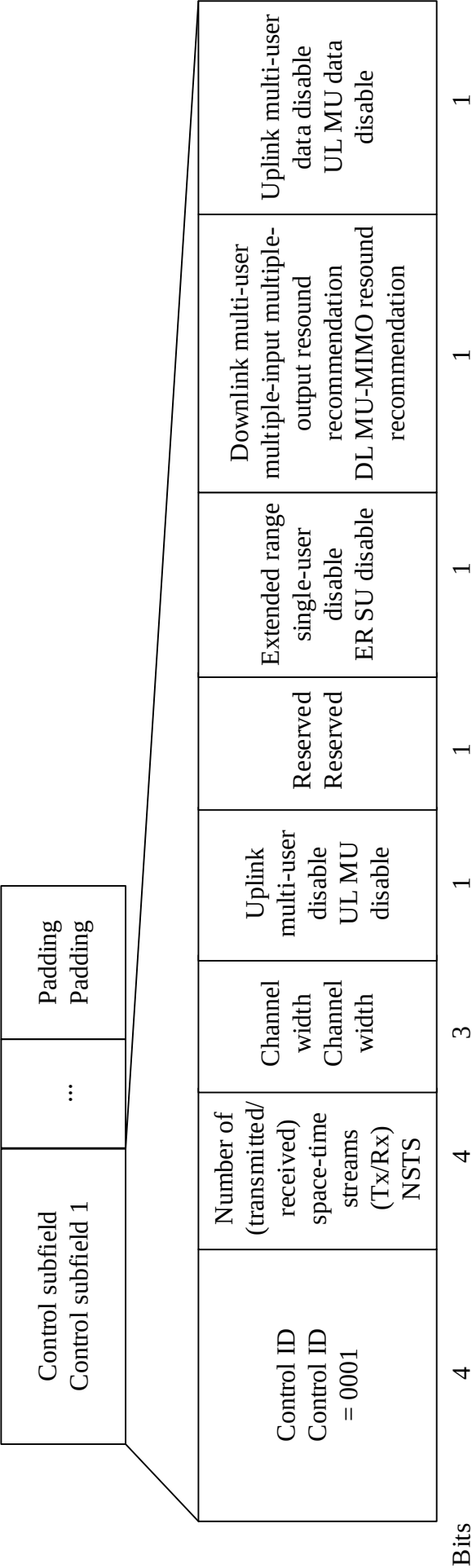


FIG. 8

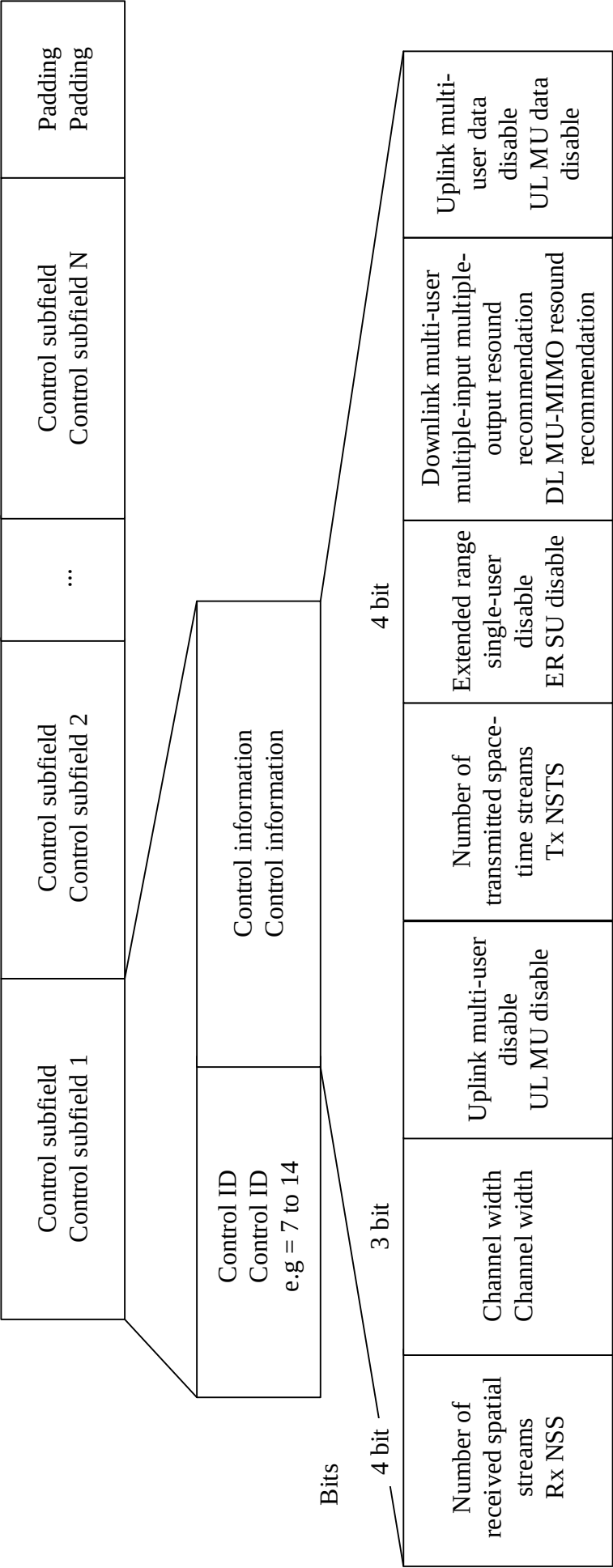


FIG. 9

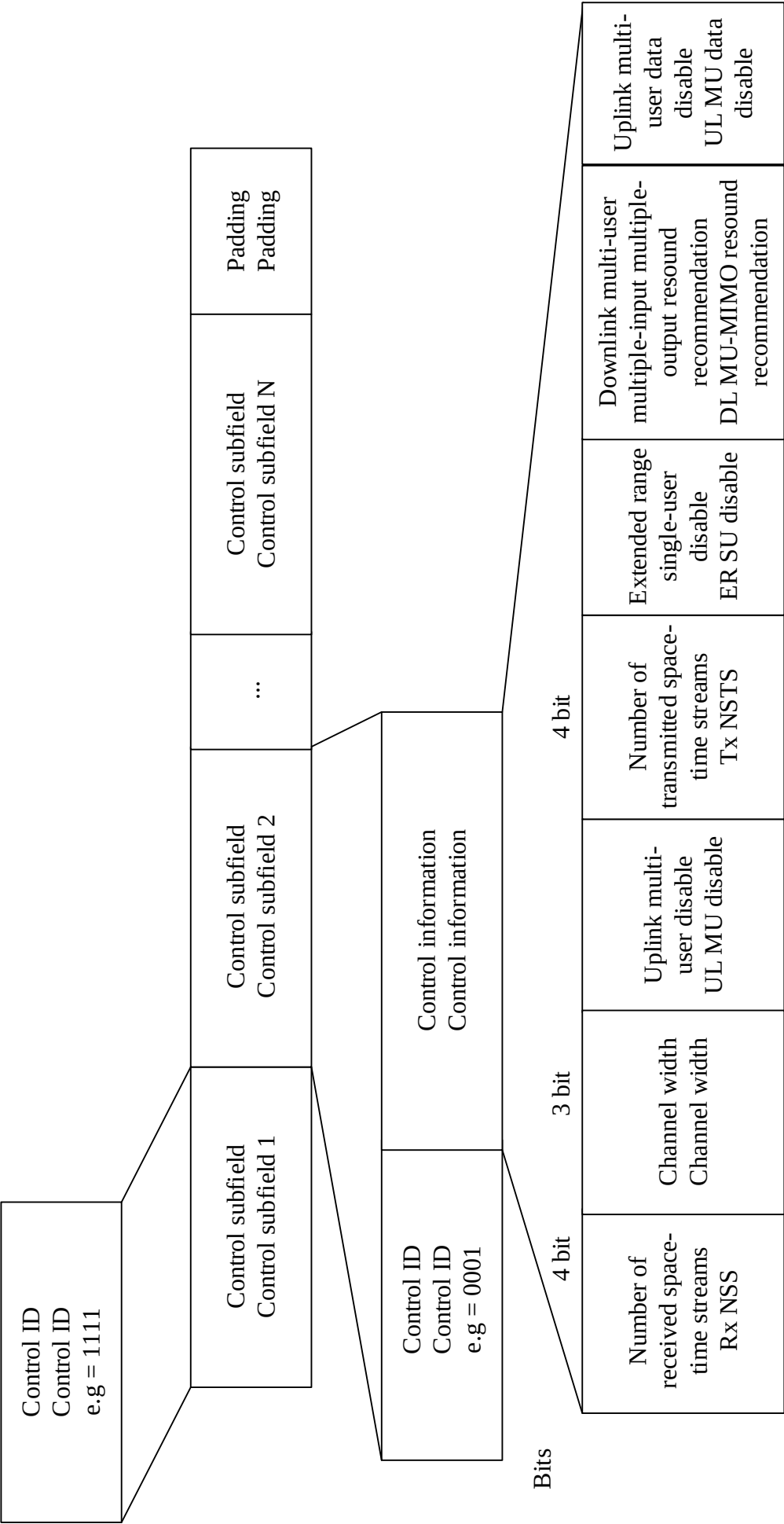


FIG. 10

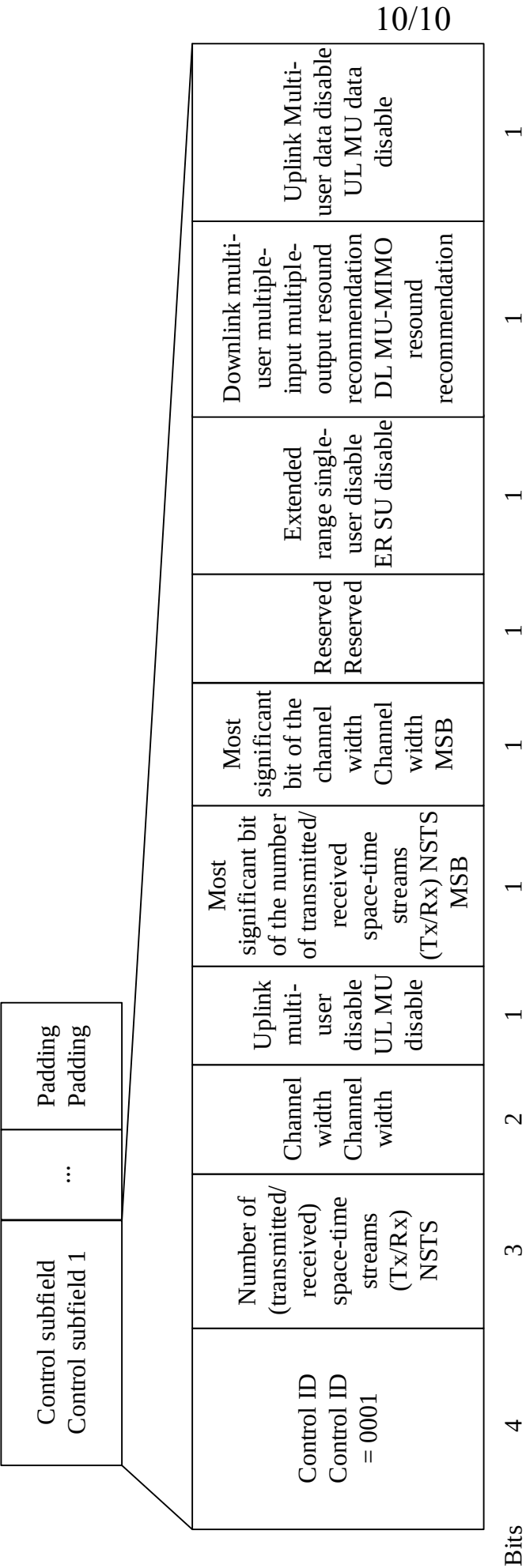


FIG. 11