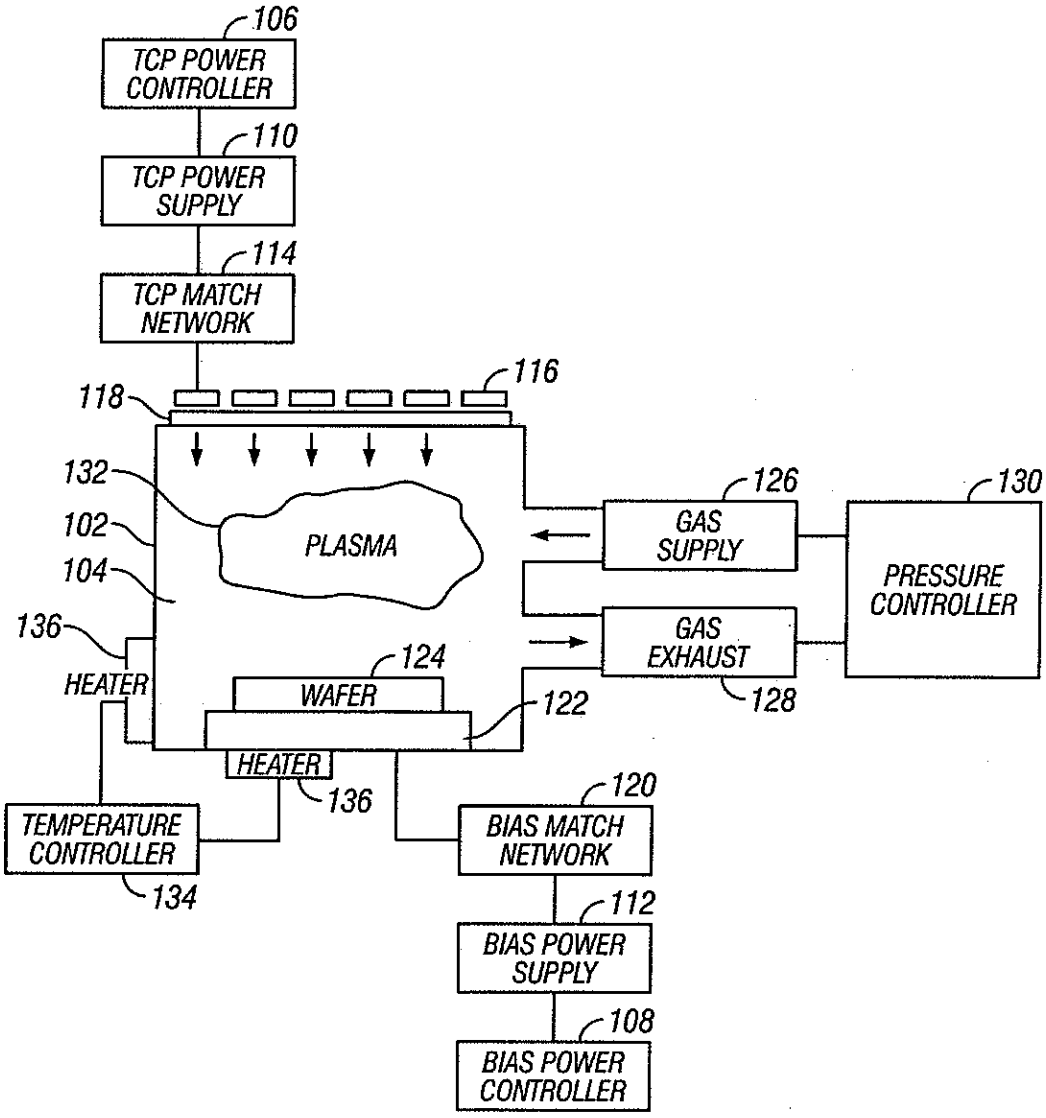


FIG. 1
(Prior Art)

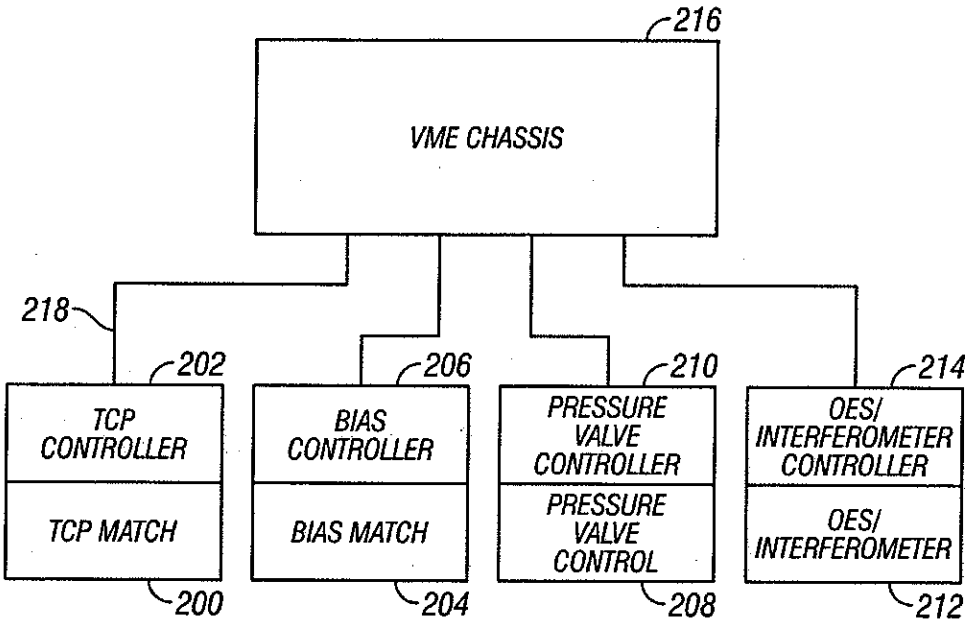


FIG. 2
(Prior Art)

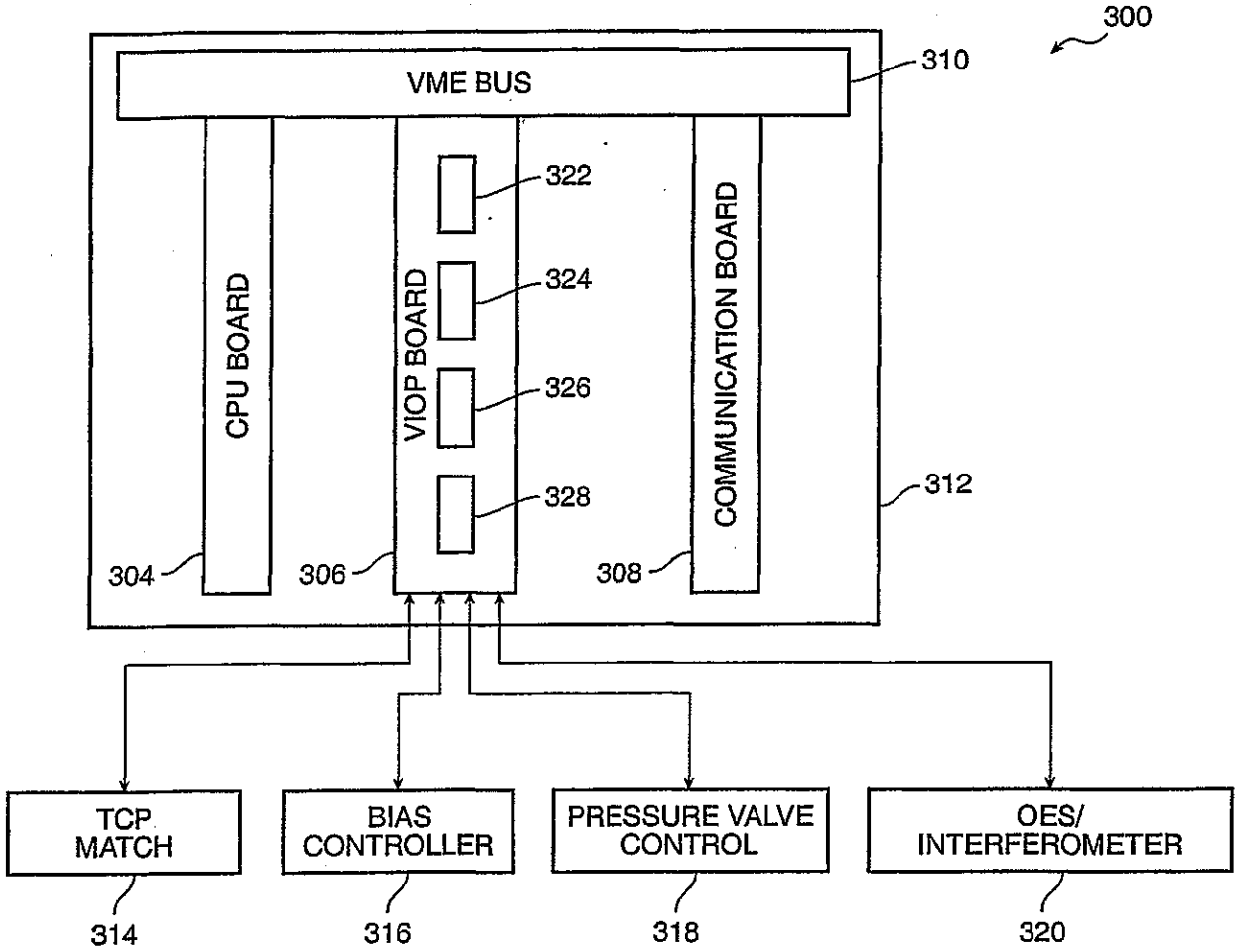

300
VME BUS
310
CPU BOARD
VIOP BOARD
COMMUNICATION BOARD
322
324
326
328
312
304
306
308
TCP MATCH
BIAS CONTROLLER
PRESSURE VALVE CONTROL
OES/ INTERFEROMETER
314
316
318
320

FIG. 3

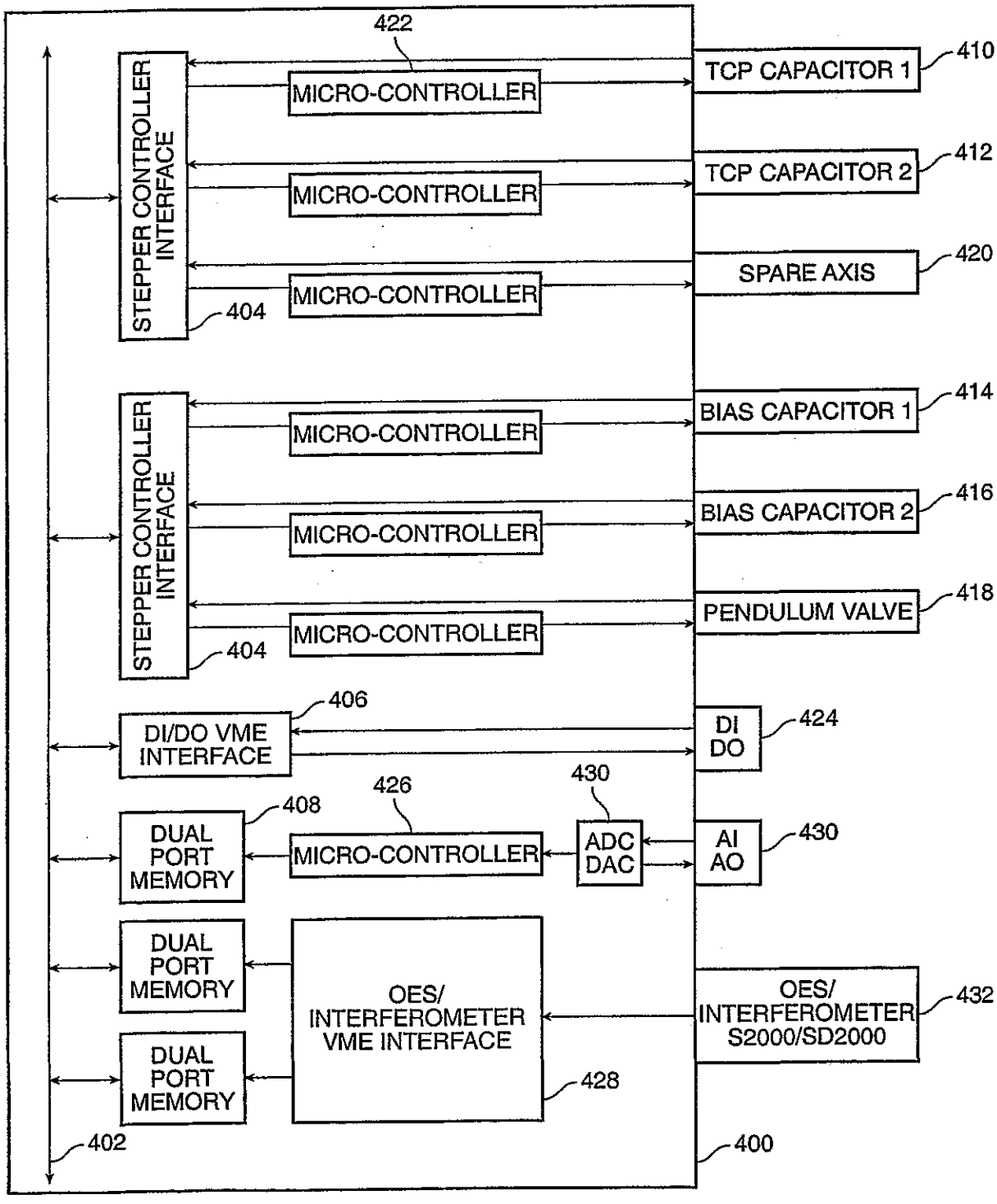

422
STEPPER CONTROLLER INTERFACE
MICRO-CONTROLLER
MICRO-CONTROLLER
MICRO-CONTROLLER
404
TCP CAPACITOR 1
410
TCP CAPACITOR 2
412
SPARE AXIS
420
STEPPER CONTROLLER INTERFACE
MICRO-CONTROLLER
MICRO-CONTROLLER
MICRO-CONTROLLER
404
BIAS CAPACITOR 1
414
BIAS CAPACITOR 2
416
PENDULUM VALVE
418
DI/DO VME INTERFACE
406
DI DO
424
DUAL PORT MEMORY
408
426
MICRO-CONTROLLER
430
ADC DAC
AI AO
430
DUAL PORT MEMORY
DUAL PORT MEMORY
OES/ INTERFEROMETER VME INTERFACE
428
OES/ INTERFEROMETER S2000/SD2000
432
402
400

FIG. 4

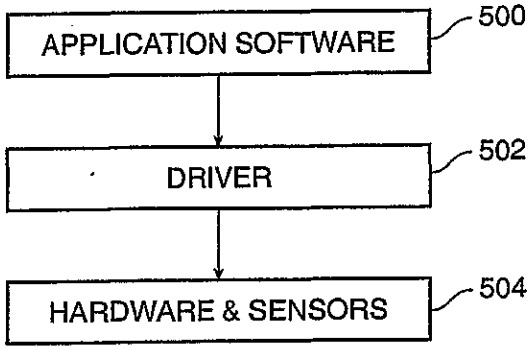

APPLICATION SOFTWARE
500
DRIVER
502
HARDWARE & SENSORS
504

FIG. 5

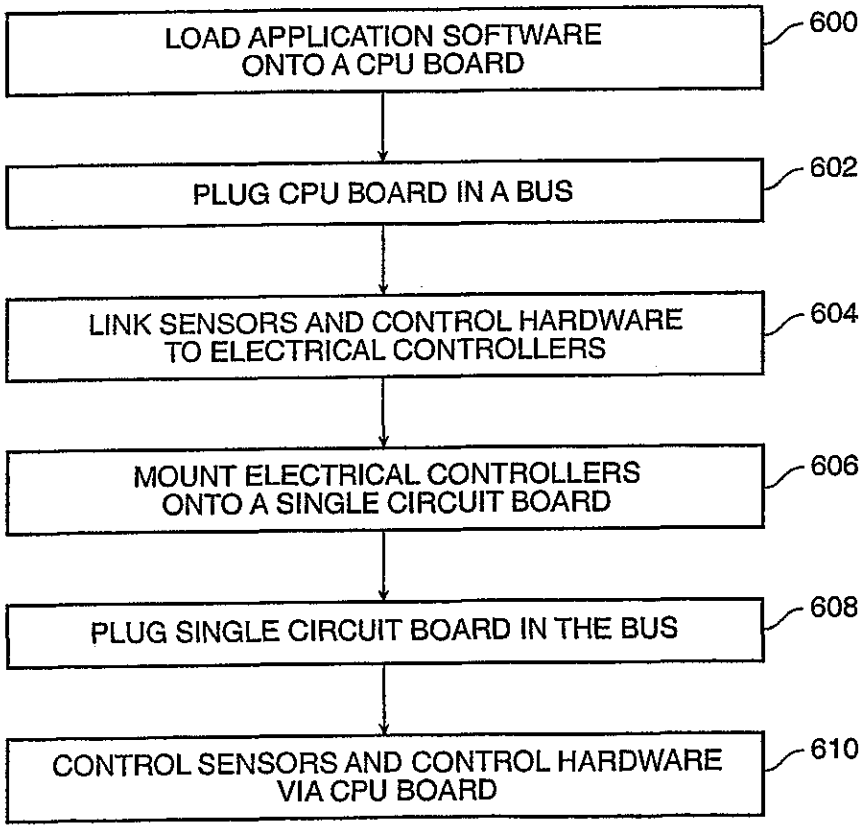

LOAD APPLICATION SOFTWARE ONTO A CPU BOARD
600
PLUG CPU BOARD IN A BUS
602
LINK SENSORS AND CONTROL HARDWARE TO ELECTRICAL CONTROLLERS
604
MOUNT ELECTRICAL CONTROLLERS ONTO A SINGLE CIRCUIT BOARD
606
PLUG SINGLE CIRCUIT BOARD IN THE BUS
608
CONTROL SENSORS AND CONTROL HARDWARE VIA CPU BOARD
610

FIG.6