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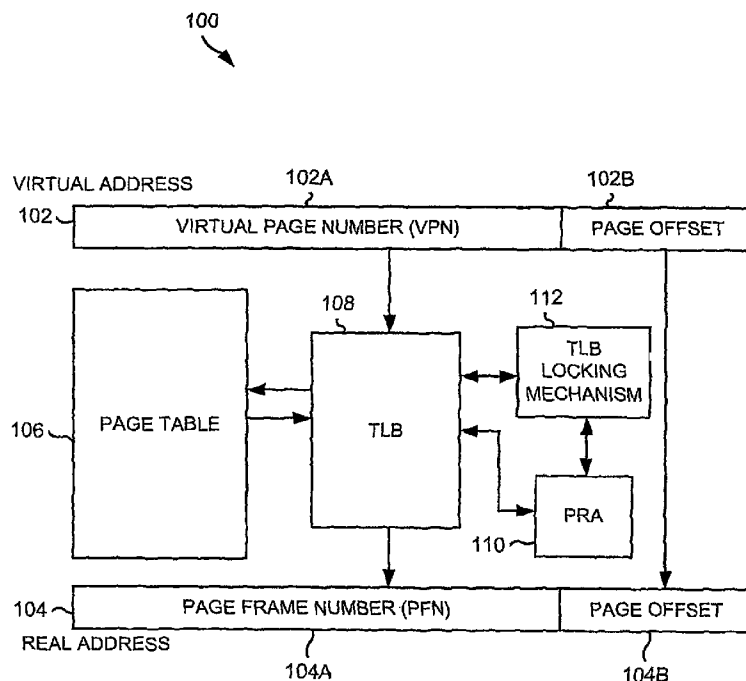
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(54) Title: IMPROVED TLB MANAGEMENT FOR REAL-TIME APPLICATIONS



(57) Abstract: Memory management in a computer system is improved by preventing a subset of address translation information from being replaced with other types of address translation information in a cache memory reserved for storing such address translation information for faster access by a CPU. This way, the CPU can identify the subset of address translation information stored in the cache.

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