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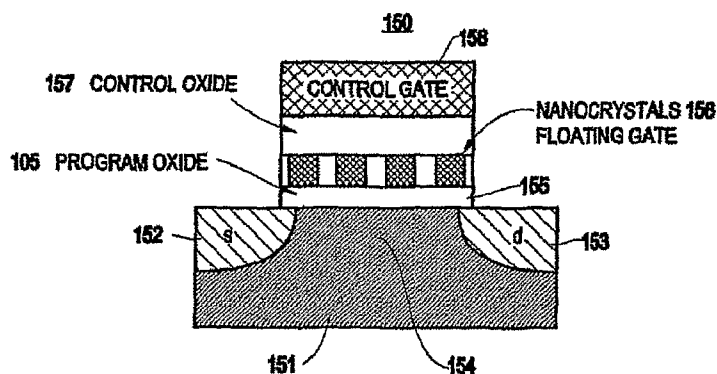
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(57) Abstract: A floating gate (156) for a field effect transistor (150) (and method for forming the same and method of forming a uniform nanoparticle array), includes a plurality of discrete nanoparticles (156) in which at least one of a size, spacing, and density of the nanoparticles is one of templated and defined by a self-assembled material.

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