

Abstract of the Disclosure

A TV receiver including a microprocessor (15) and on
screen display circuitry (20) on a single semiconductor
5 chip. The OSD includes half dot logic and shifting
circuitry (32, 34) to smooth the characters with a
minimum of storage capacity required and the half dot
logic is used in an edge enhancing circuit (75) to
generate a black surround. A simplified window generator
10 (36) is combined therewith to further improve the
characters.

Fig. 2 accompanies this abstract.

This Sheet Has Been Replaced

