FIG. 1

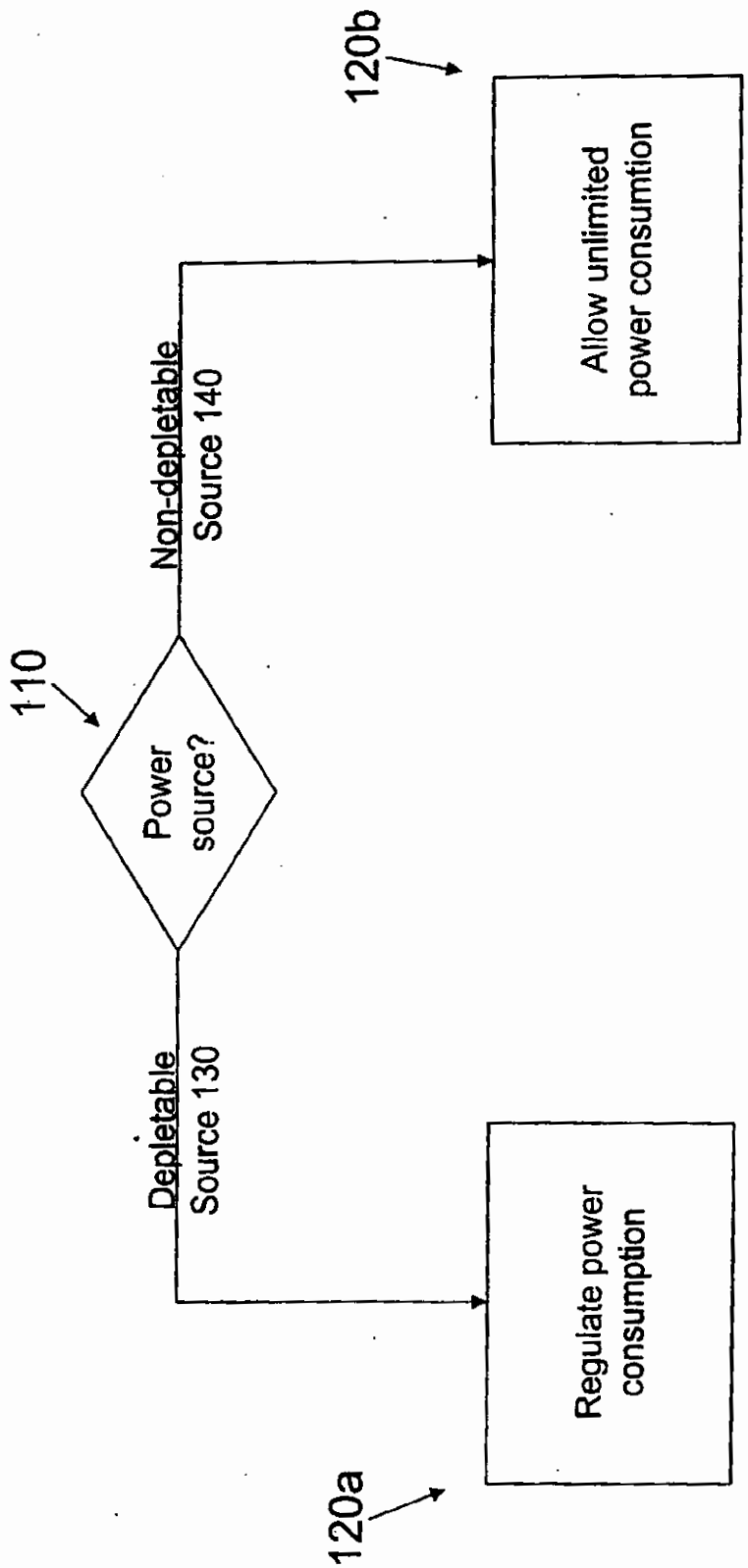

120b
Allow unlimited power consumtion
Non-depletable Source 140
110
Power source?
Depletable Source 130
Regulate power consumption
120a

FIG. 2

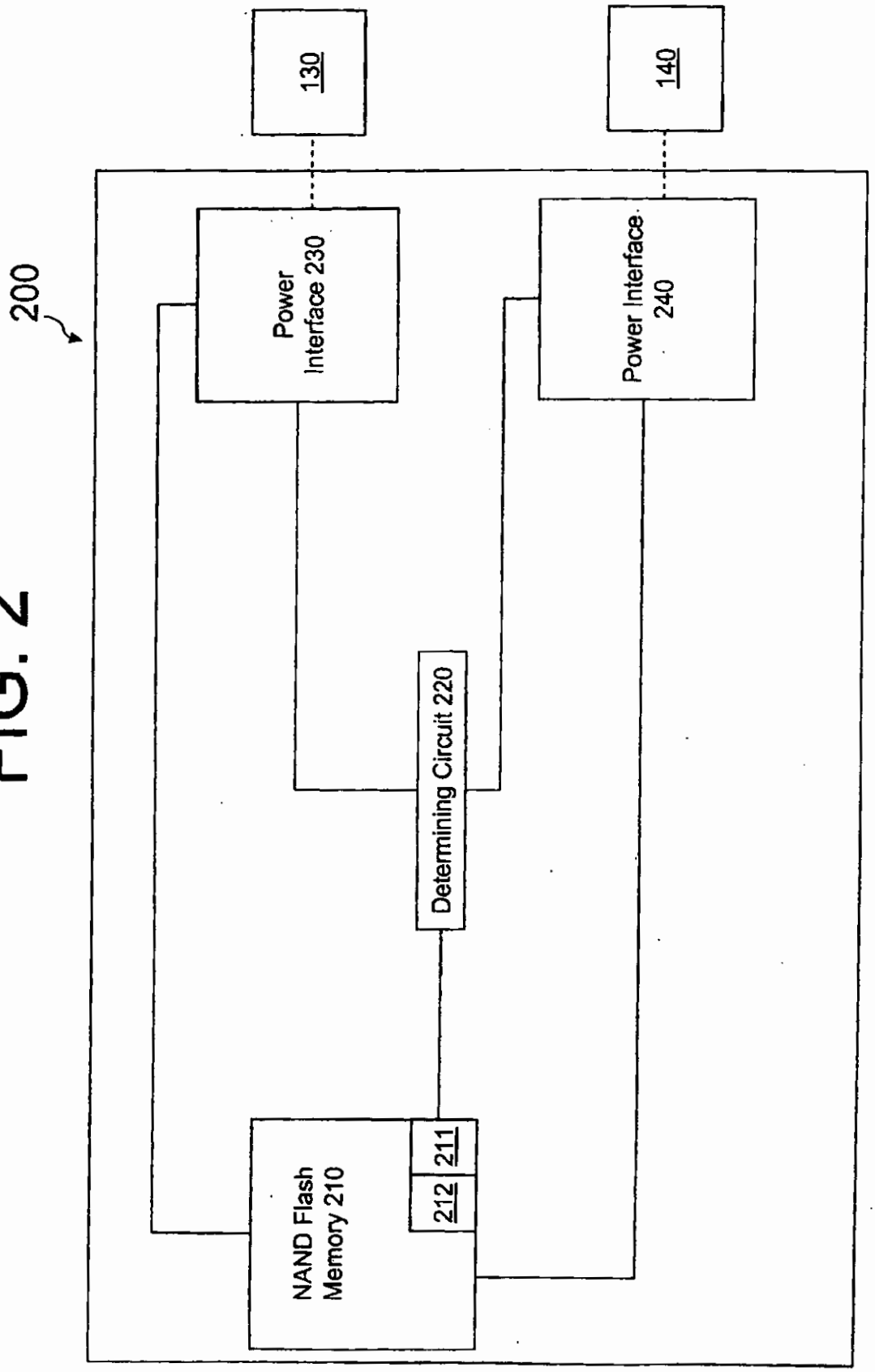

200
130
140
Power Interface 230
Power Interface 240
Determining Circuit 220
NAND Flash Memory 210
211
212

FIG. 3

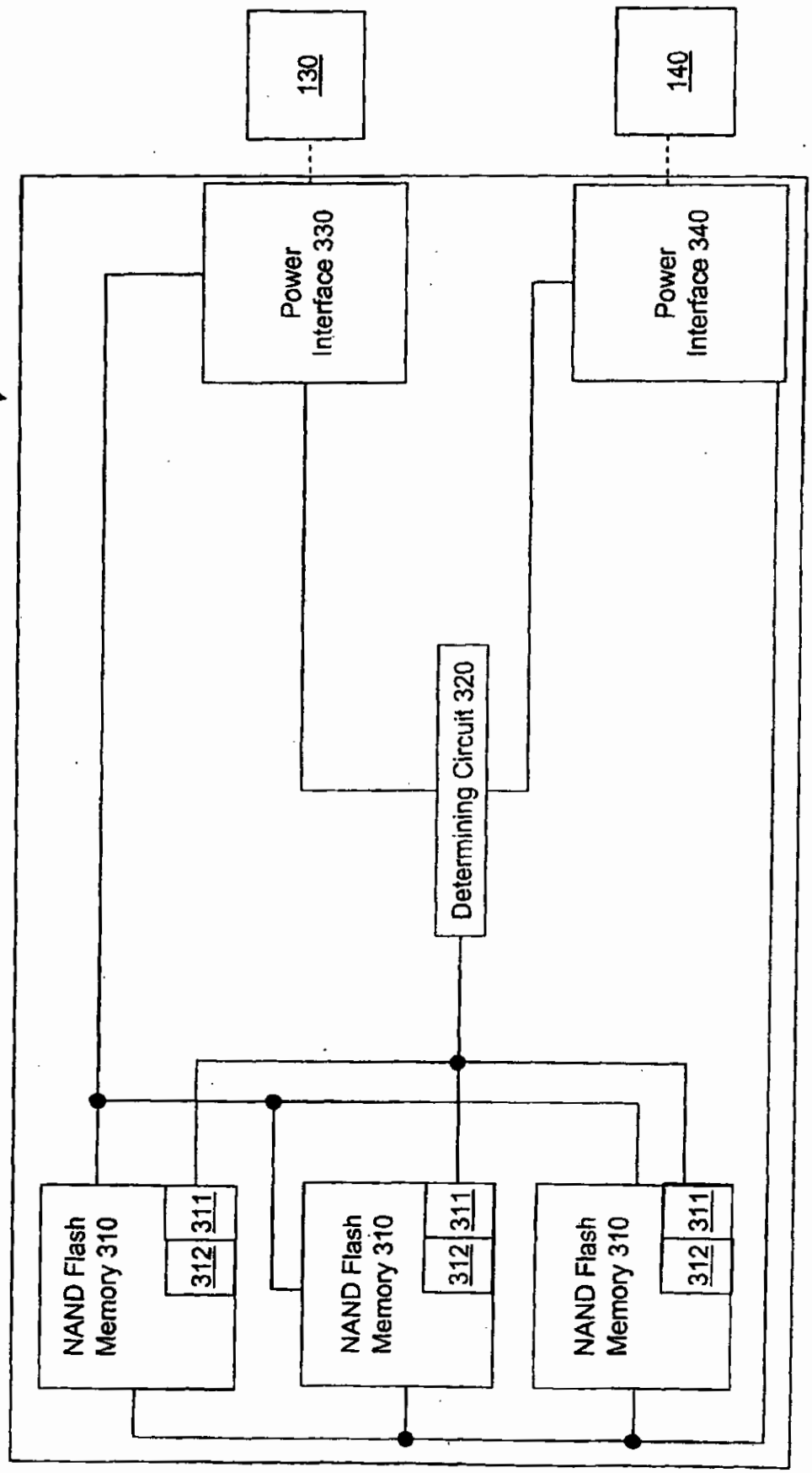

300
130
140
Power Interface 330
Power Interface 340
Determining Circuit 320
NAND Flash Memory 310
311
312
NAND Flash Memory 310
311
312
NAND Flash Memory 310
311
312

FIG. 4A

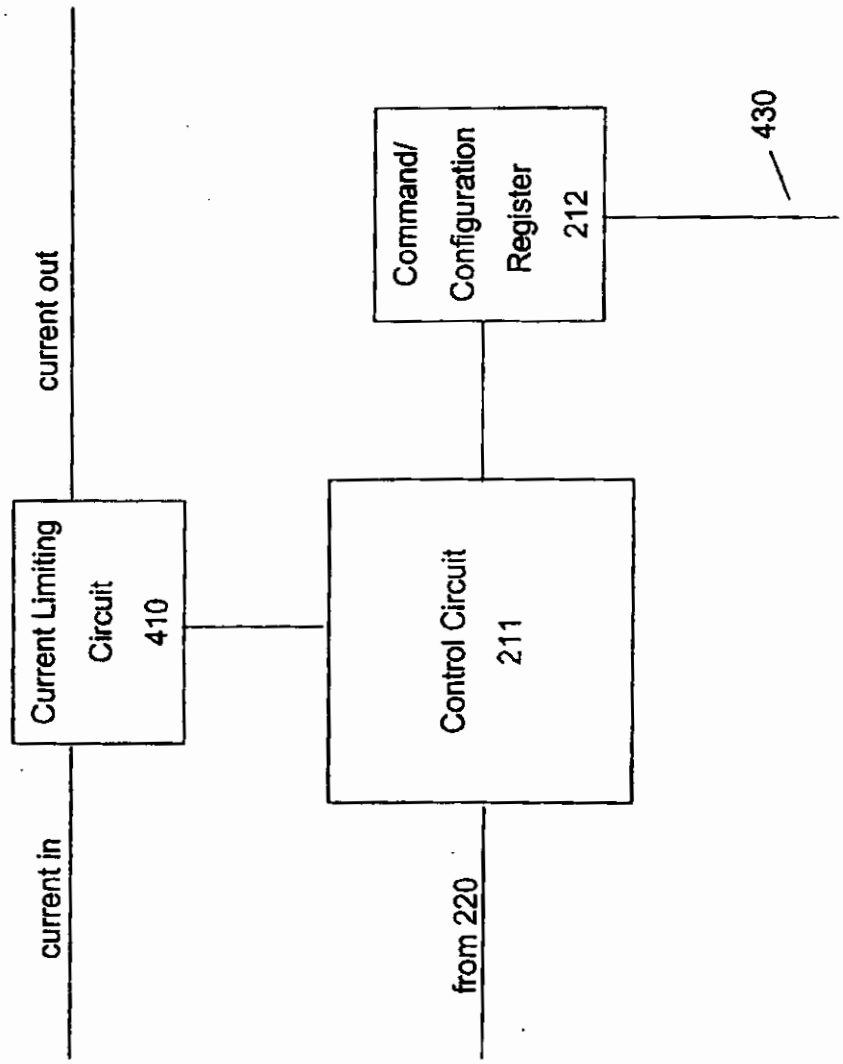

current out
Current Limiting Circuit 410
Command/ Configuration Register 212
430
Control Circuit 211
current in
from 220

FIG. 4B

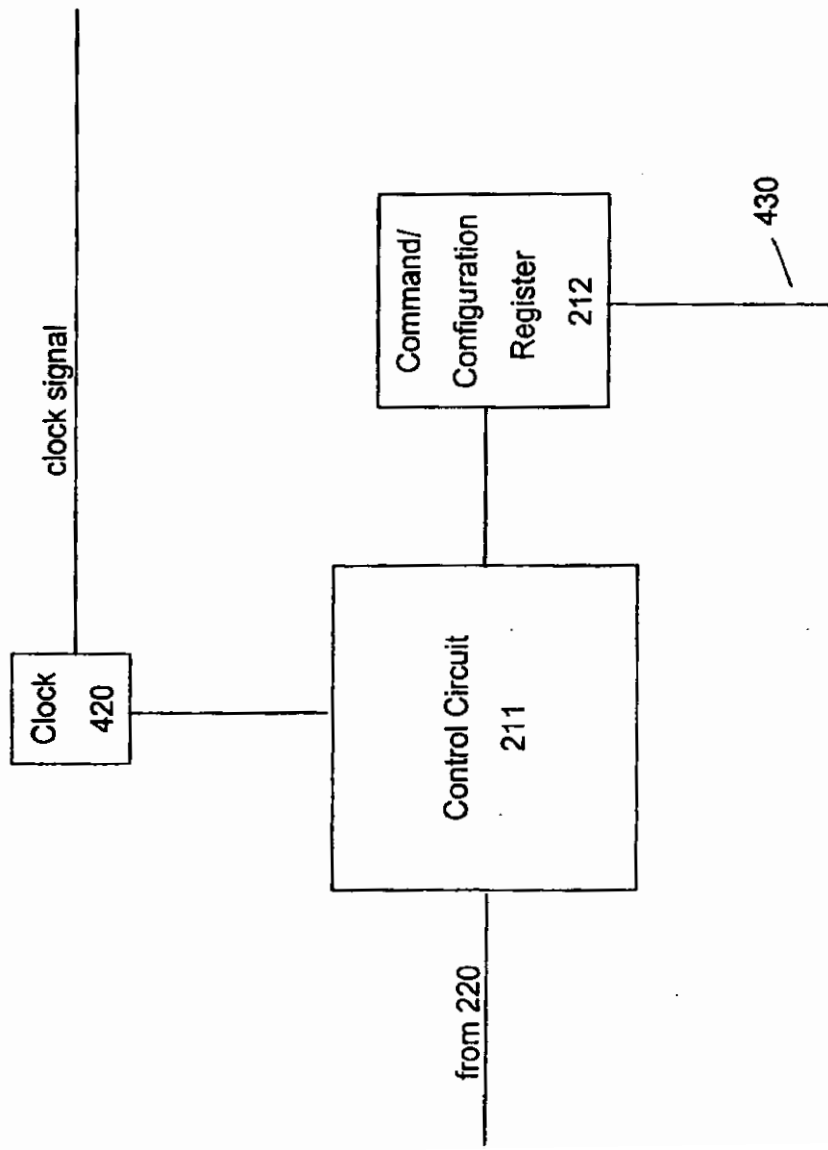

clock signal
Clock
420
Control Circuit
211
Command/
Configuration
Register
212
430
from 220

FIG. 4C

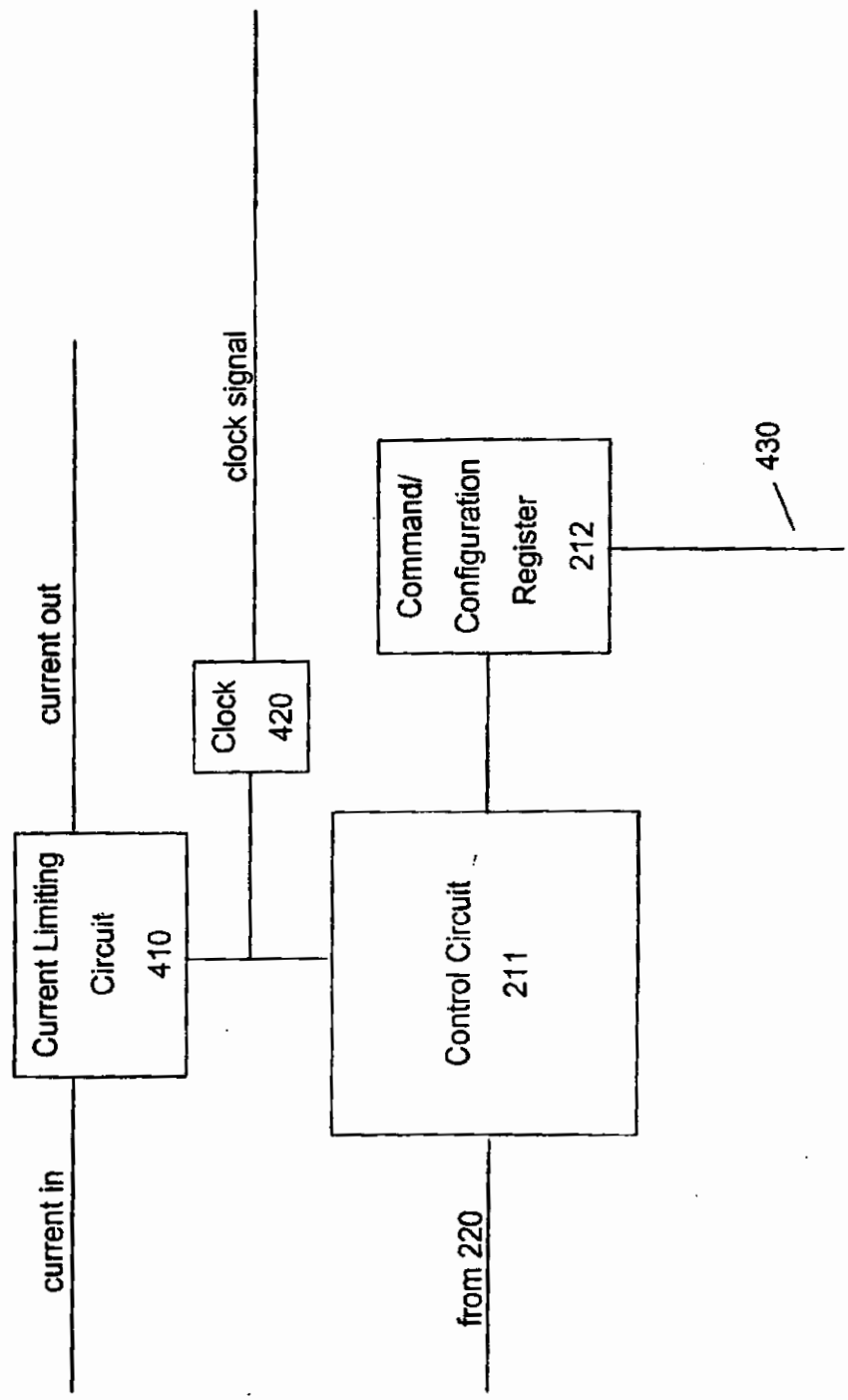

clock signal
current out
Command/
Configuration
Register
212
430
Clock
420
Current Limiting
Circuit
410
Control Circuit
211
current in
from 220

FIG. 5

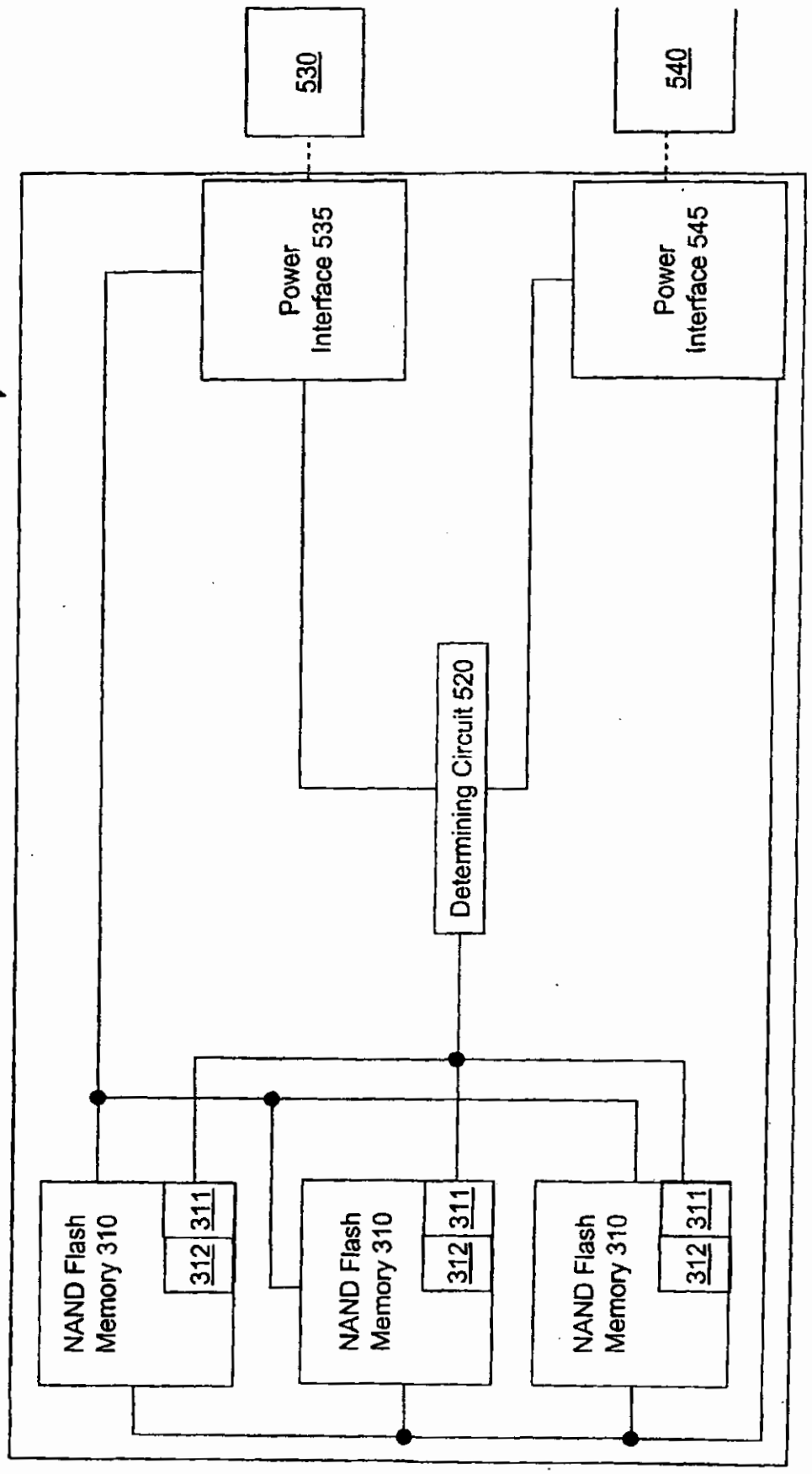
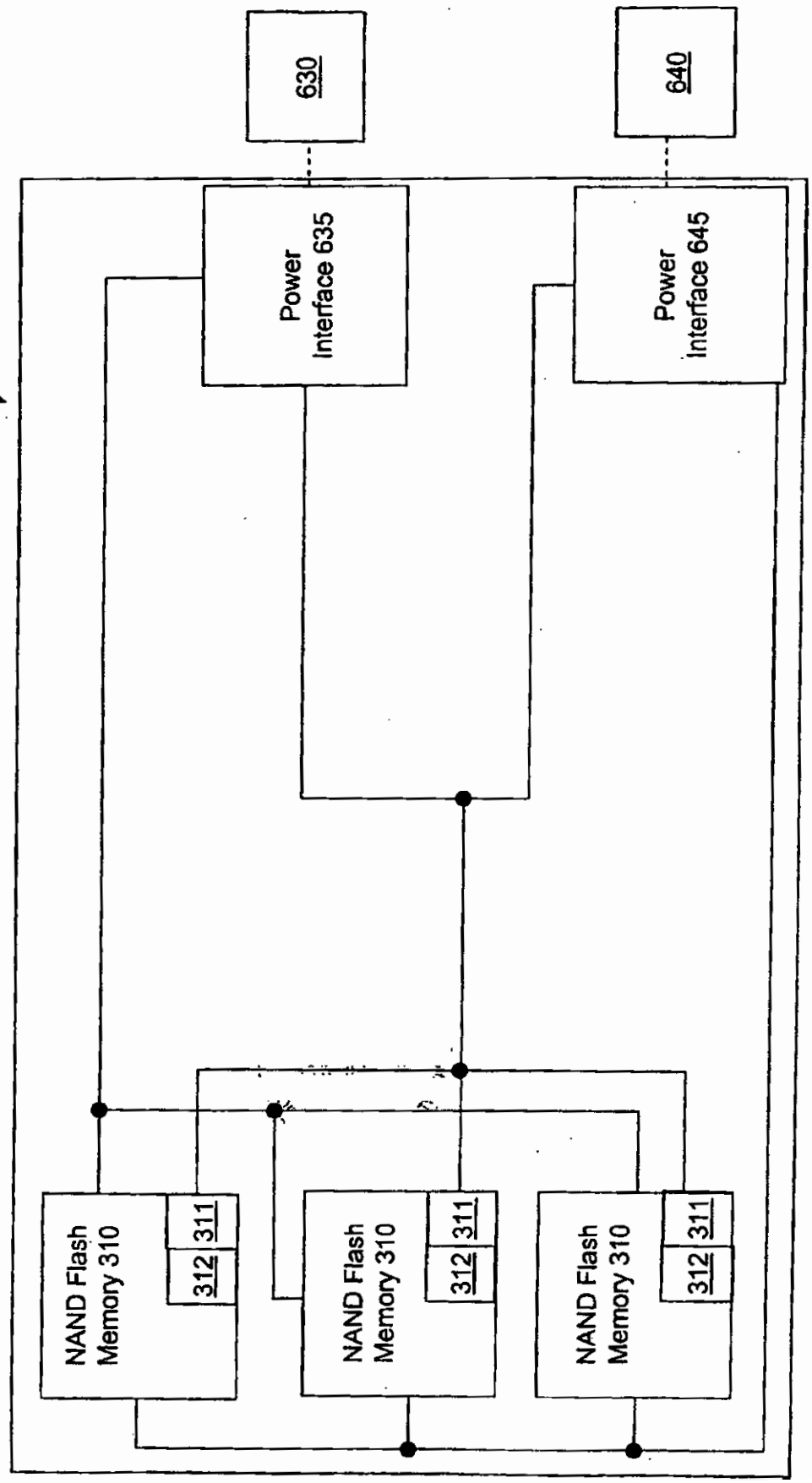

500
530
540
Power Interface 535
Power Interface 545
Determining Circuit 520
NAND Flash Memory 310
312
311
NAND Flash Memory 310
312
311
NAND Flash Memory 310
312
311

FIG. 6


600
630
640
Power Interface 635
Power Interface 645
NAND Flash Memory 310
311
312
NAND Flash Memory 310
311
312
NAND Flash Memory 310
311
312