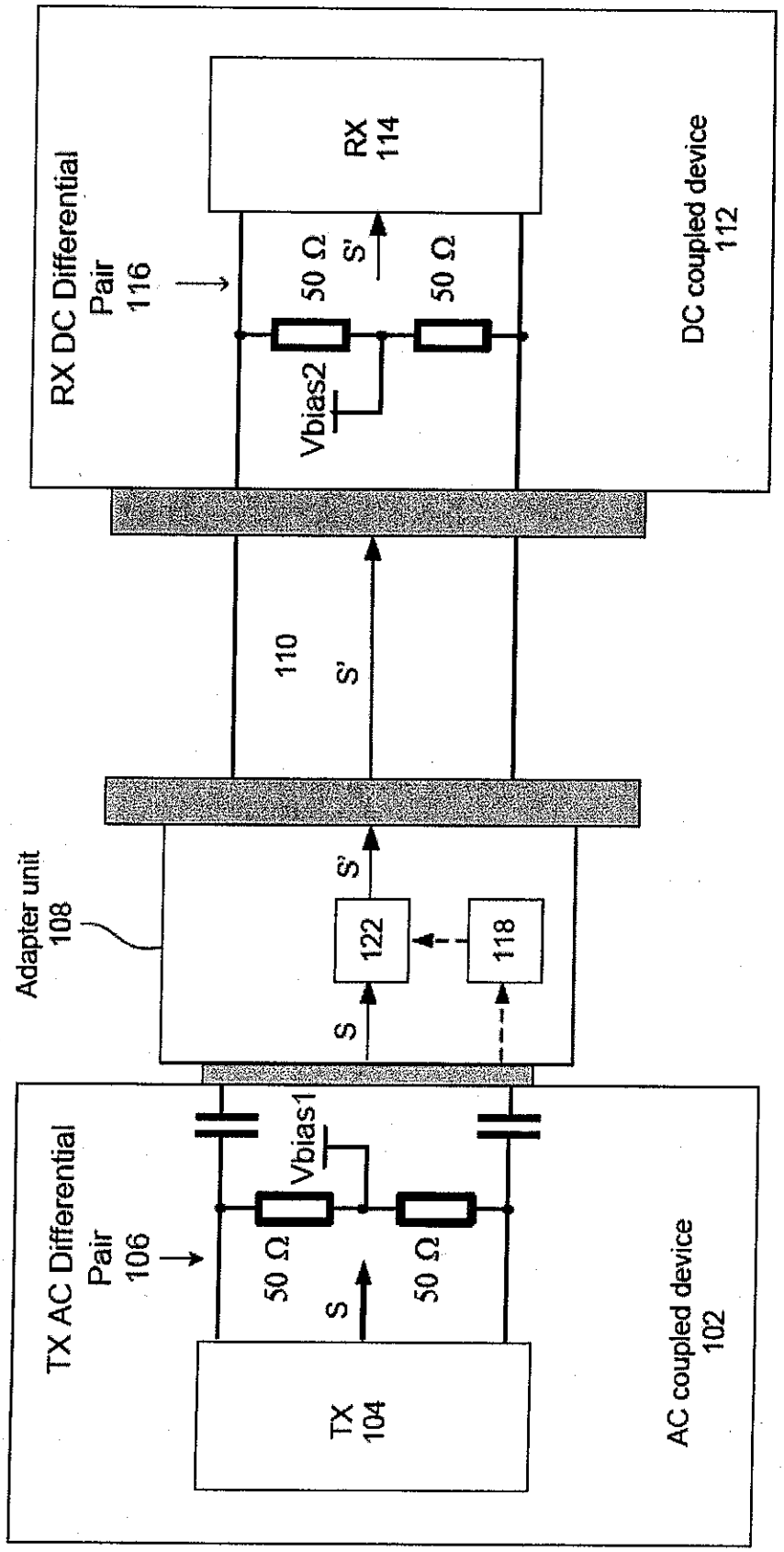

AC coupled device
102
TX
104
S
50 Ω
50 Ω
Vbias1
TX AC Differential
Pair
106
Adapter unit
108
S
122
118
S'
110
S'
RX DC Differential
Pair
116
50 Ω
50 Ω
S'
Vbias2
RX
114
DC coupled device
112

Fig. 1

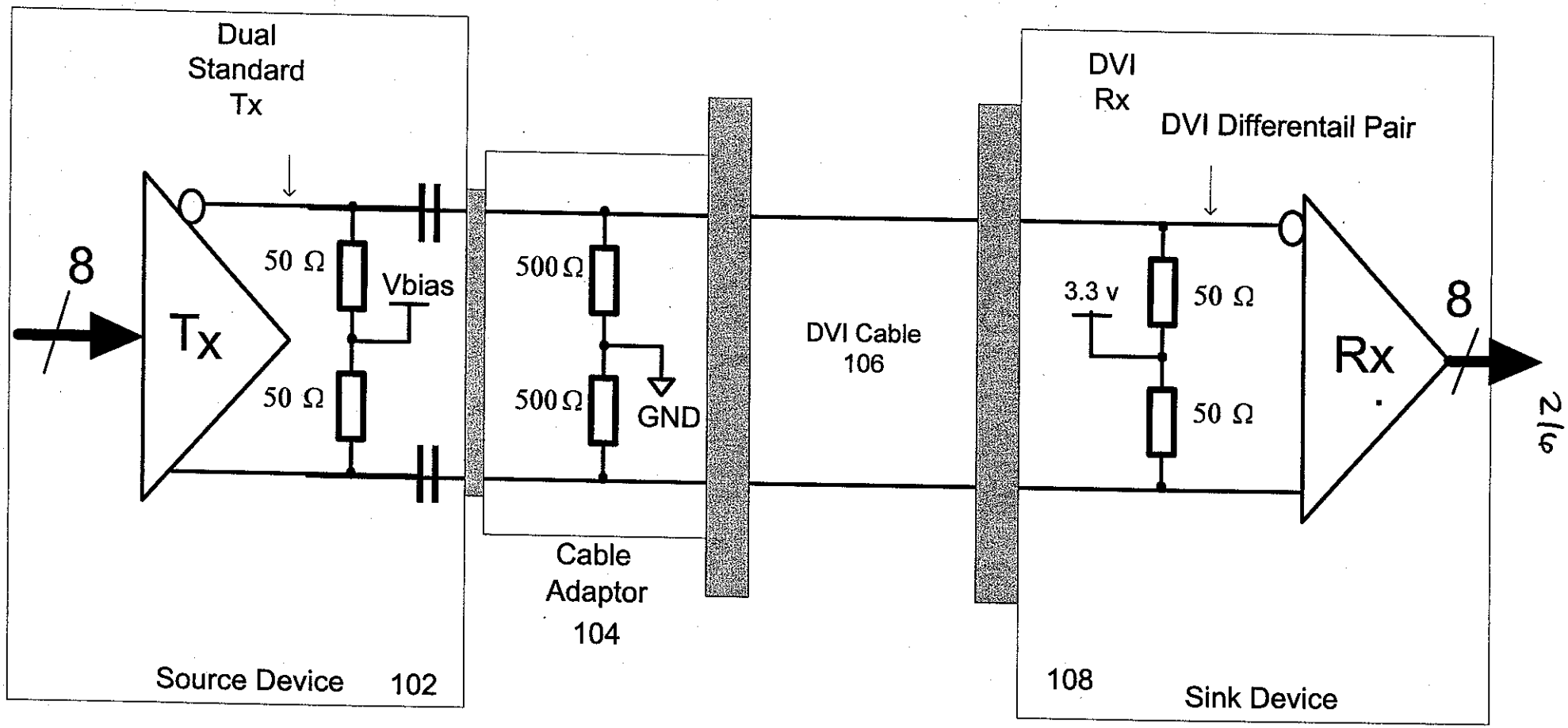

Dual
Standard
Tx
8
Tx
50 Ω
Vbias
50 Ω
500 Ω
500 Ω
GND
Cable
Adaptor
104
DVI Cable
106
DVI
Rx
DVI Differentail Pair
3.3 v
50 Ω
50 Ω
Rx
8
Source Device 102
108
Sink Device

Fig. 2

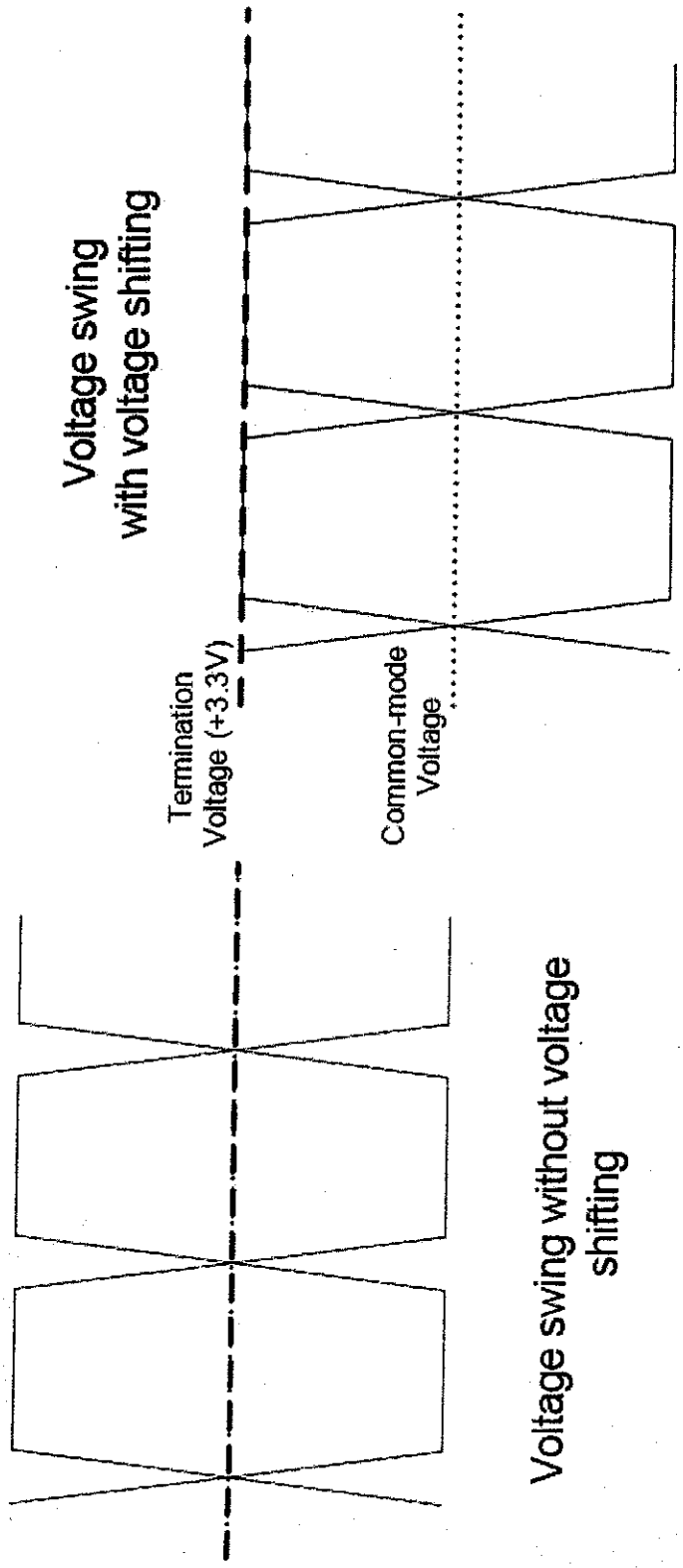

Voltage swing
with voltage shifting
Termination
Voltage (+3.3V)
Common-mode
Voltage
Voltage swing without voltage
shifting

Fig. 3

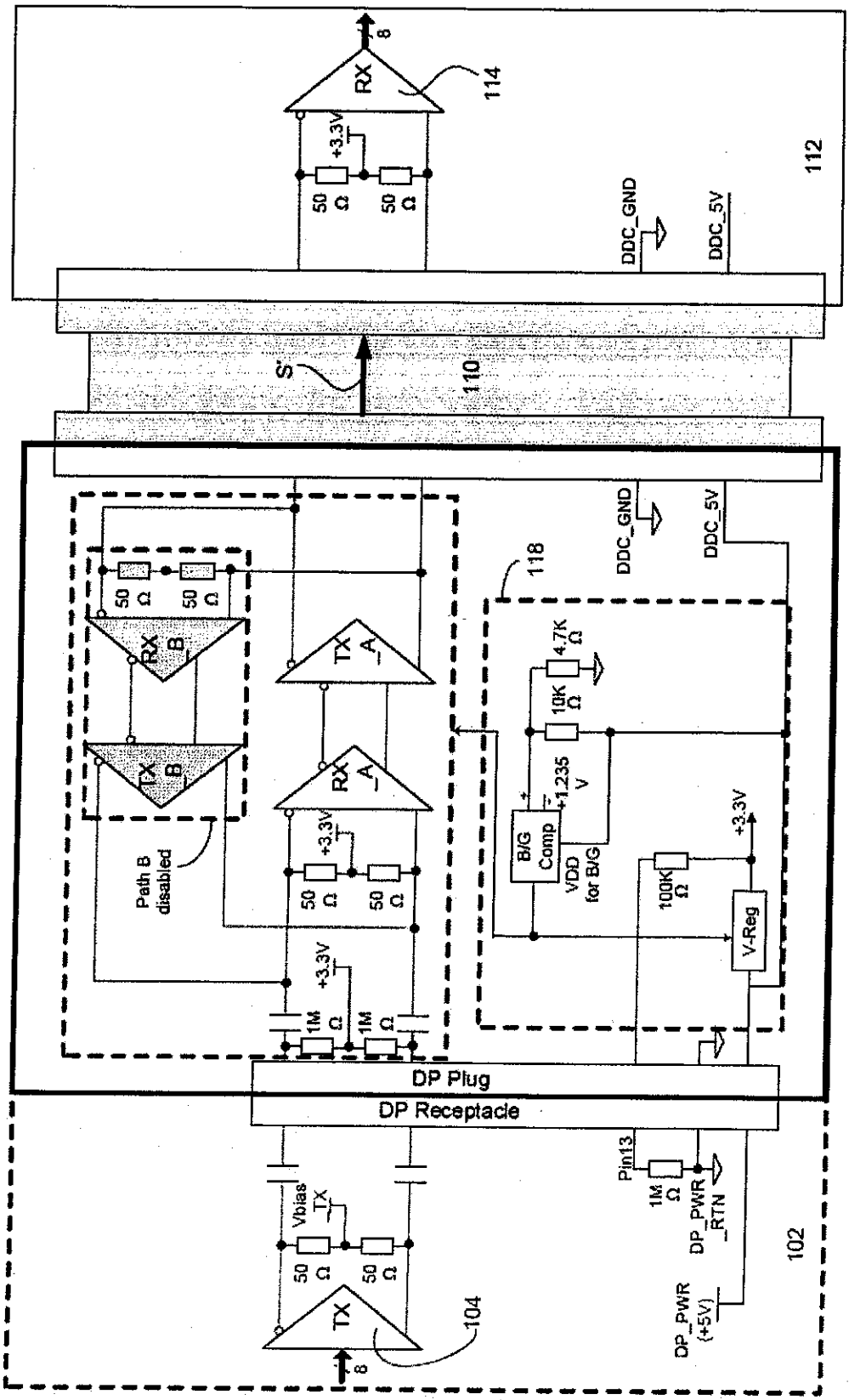

RX
8
114
+3.3V
50 Ω
50 Ω
112
DDC_GND
DDC_5V
S
110
118
TX_A
RX_A
RX_B
TX_B
4.7K Ω
10K Ω
+1.235 V
B/G Comp
VDD for B/G
100K Ω
V-Reg
Path B disabled
1M Ω
DP Plug
DP Receptacle
Vbias
TX
Pin13
DP_PWR_RTN
DP_PWR (+5V)
102
104

Fig 4A

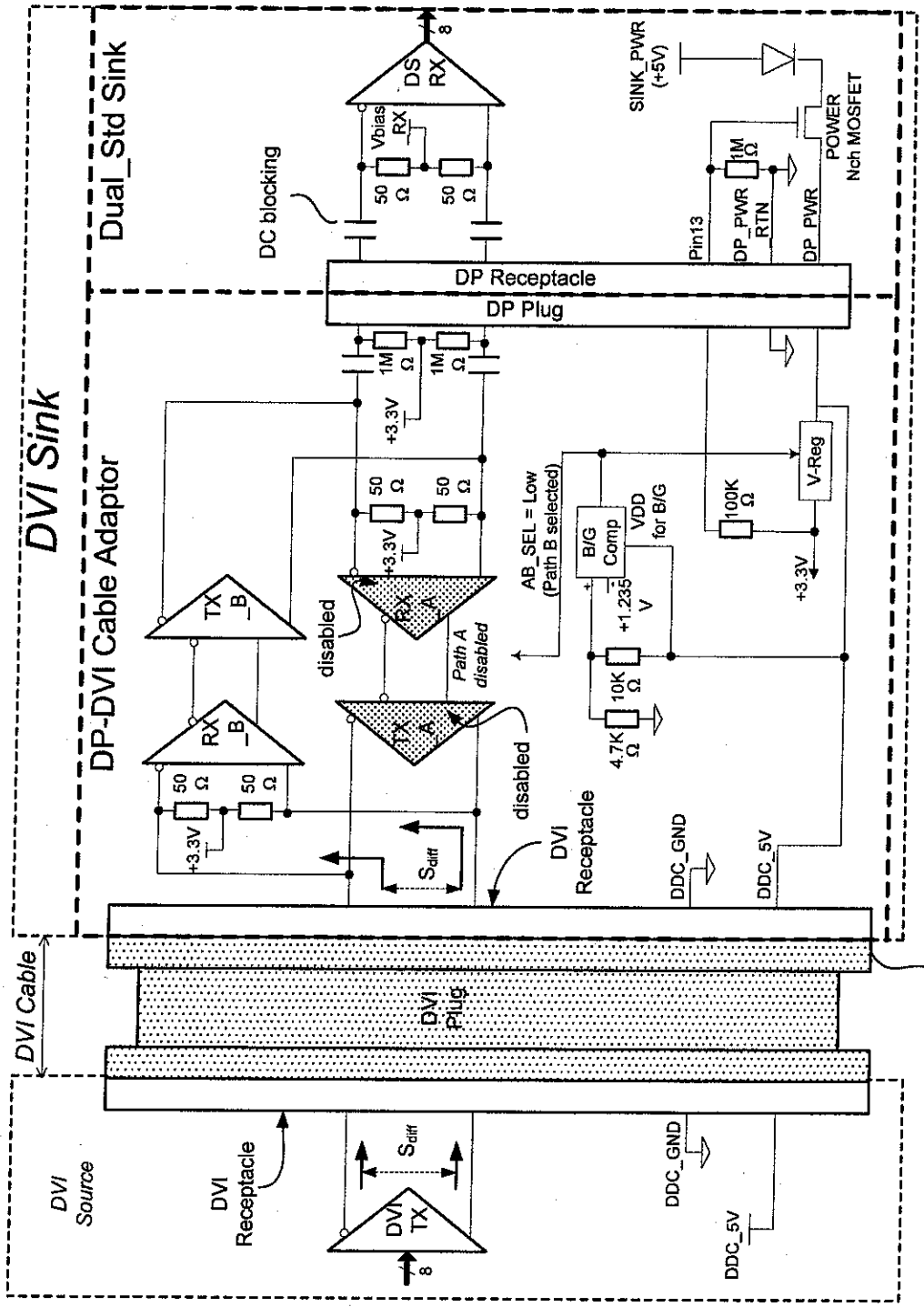

DVI Sink
Dual_Std Sink
DP-DVI Cable Adaptor
DS RX
Vbias RX
50 Ω
DC blocking
SINK_PWR (+5V)
POWER Nch MOSFET
1M Ω
Pin13
DP_PWR RTN
DP_PWR
DP Receptacle
DP Plug
+3.3V
TX_B
RX_B
RX_A
TX_A
disabled
Path A disabled
AB_SEL = Low (Path B selected)
B/G Comp
VDD for B/G
+1.235 V
100K Ω
V-Reg
10K Ω
4.7K Ω
DVI Receptacle
DDC_GND
DDC_5V
Sdiff
DVI Cable
DVI Plug
TP3
DVI Source
DVI TX
8

Fig. 4B

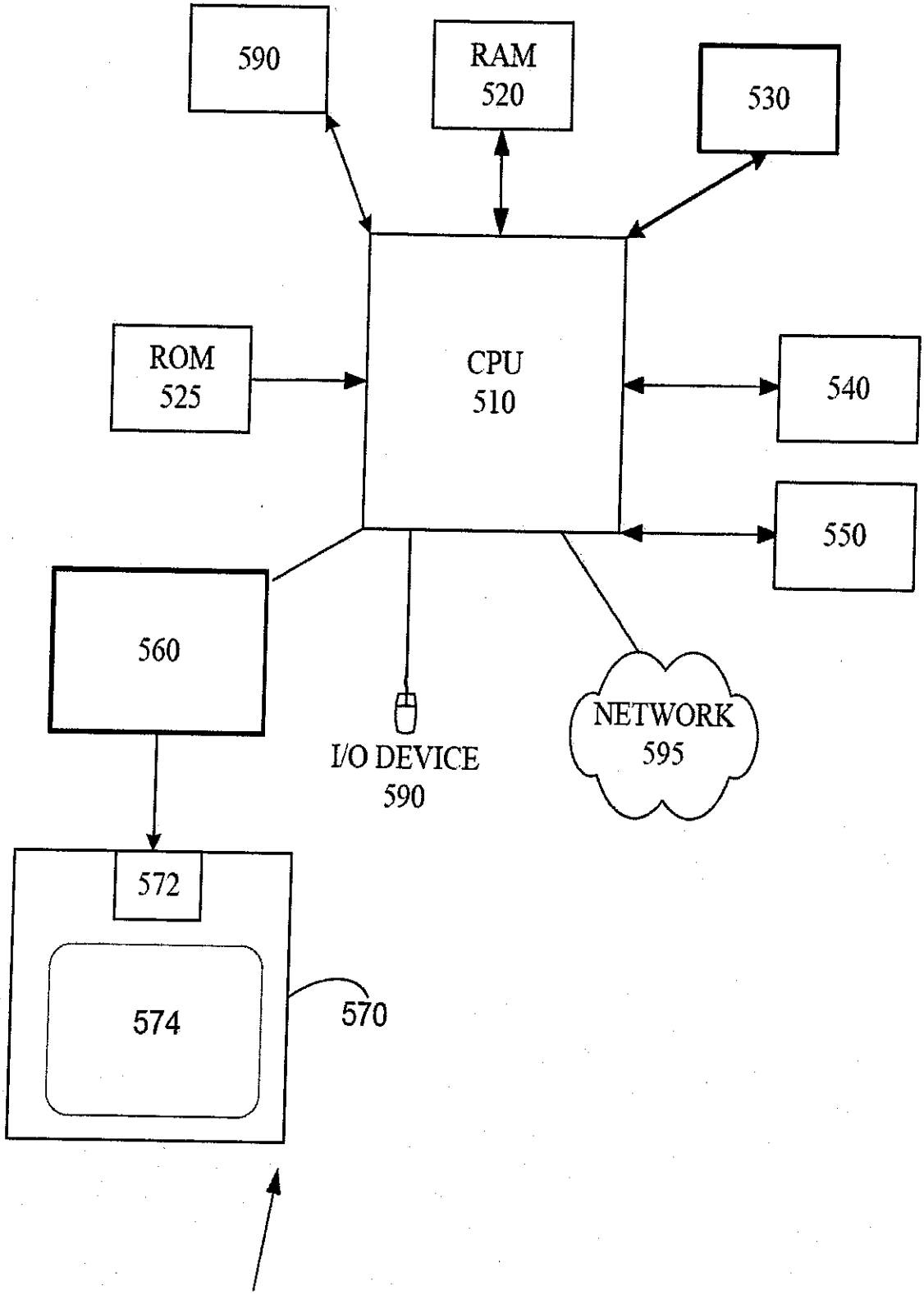

590
RAM
520
530
ROM
525
CPU
510
540
550
560
I/O DEVICE
590
NETWORK
595
572
574
570
500

Fig. 5