Abstract

A video display control system comprises a display (244) for the video signal, a vertical zoom circuit (50) for generating from the video signal a picture having a vertical height greater than the display, and, a vertical panning circuit (1034) for displaying a selected portion of the picture on the display by generating a time delay relative to a vertical synchronizing component of the video signal. A counter (1101) and comparator (1103) measure the respective durations of successive fields of the video signal and the time delay. A circuit (1108, 1110) responsive to the comparing means generates a first signal for initiating delayed reset pulses when a field of the video signal and the time delay match in duration. Another circuit (1102, 1104, 1106) generates a second signal for initiating the delayed reset pulses in accordance with the vertical synchronizing component. A gating circuit (1112, 1116, 1120) selects the first initiating signal as an output, if present, and selects the second initiating signal as an output absent detection of the first initiating signal. A further counter (1114) generates the delayed vertical reset pulses responsive to the output selected by the gating circuit.

FIGURE 15

This Sheet Has Been Replaced

