display area, and a second common lead-out wire electrically connected to the common electrode,

the second common lead-out wire intersects the second multiple gate lead-out wires, and

the common electrode covers an area between the second common lead-out wire intersecting the second multiple gate lead-out wires and a part of the perimeter of the display area along the second common lead-out wire.

Claim 12. The liquid crystal display panel according to any one of claims 1 to 11,

wherein the one of the pair of substrates includes a thin film transistor, and

the thin film transistor includes a semiconductor layer including an oxide semiconductor.

Claim 13. The liquid crystal display panel according to claim 12,

wherein the oxide semiconductor contains indium, gallium, zinc and oxygen.