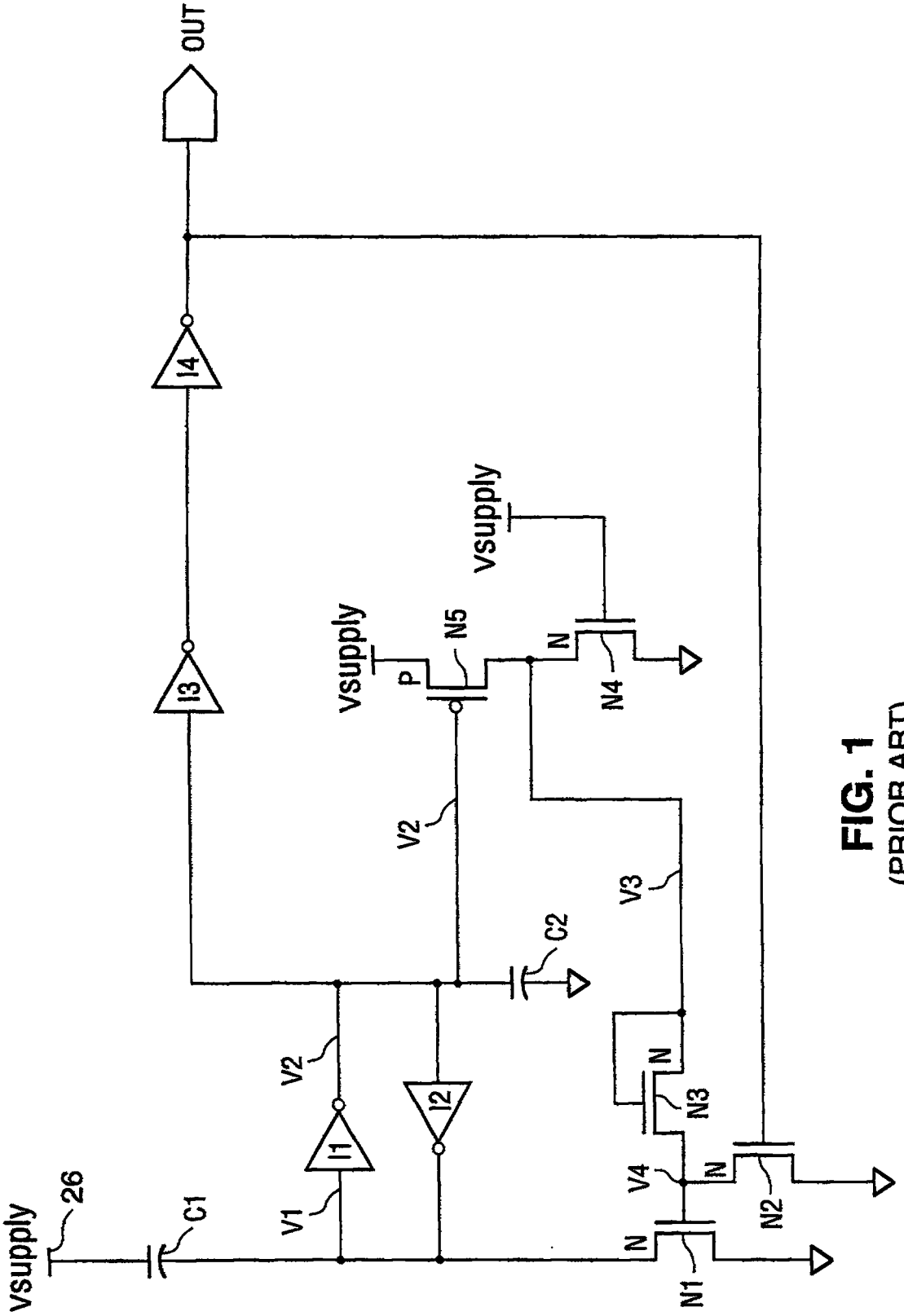


FIG. 1
(PRIOR ART)

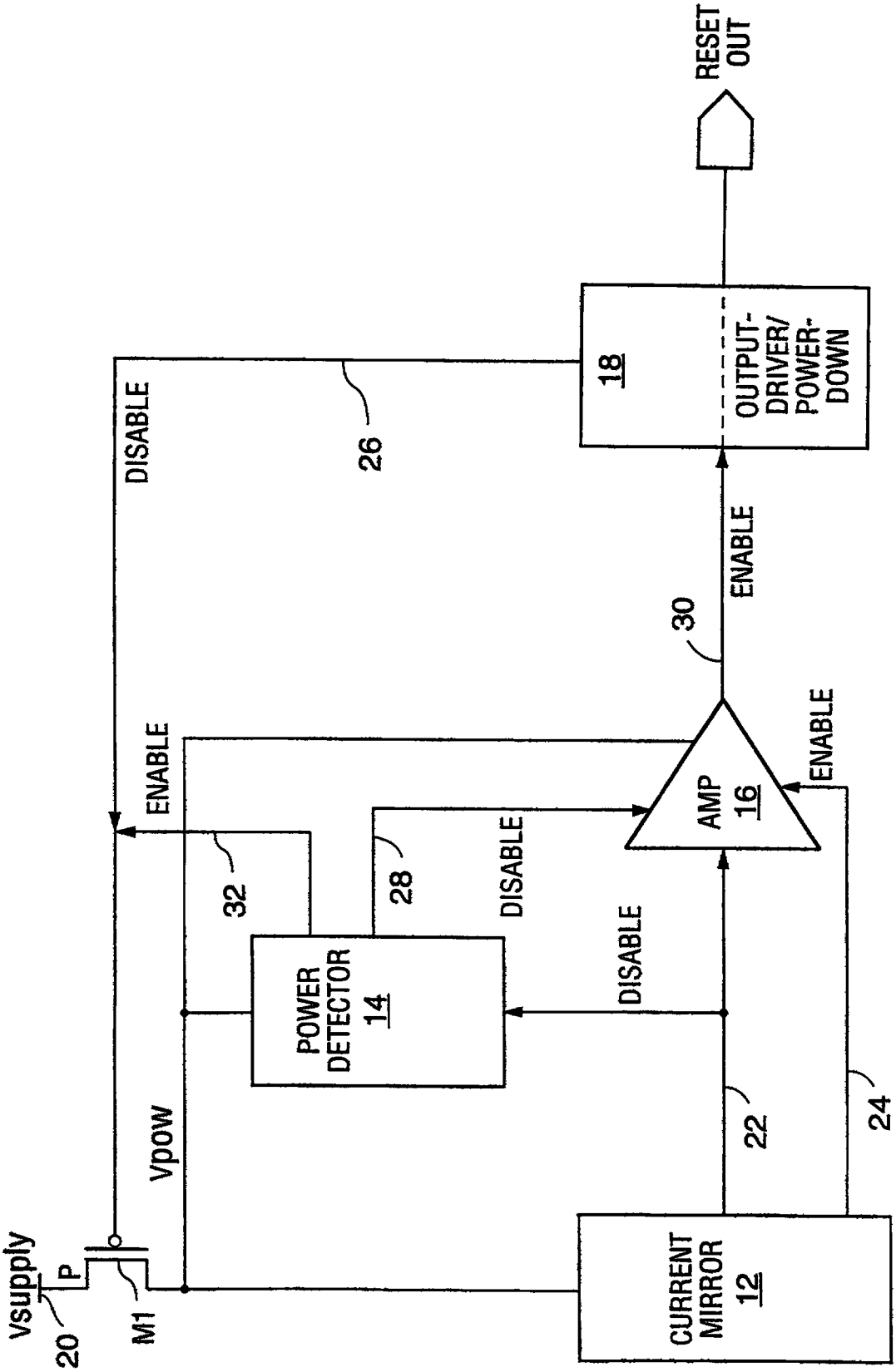

RESET OUT
18
OUTPUT-DRIVER/POWER-DOWN
DISABLE
26
ENABLE
30
ENABLE
AMP 16
ENABLE
32
28
DISABLE
DISABLE
POWER DETECTOR 14
Vpow
22
24
Vsupply
20
P
M1
CURRENT MIRROR 12

FIG. 2

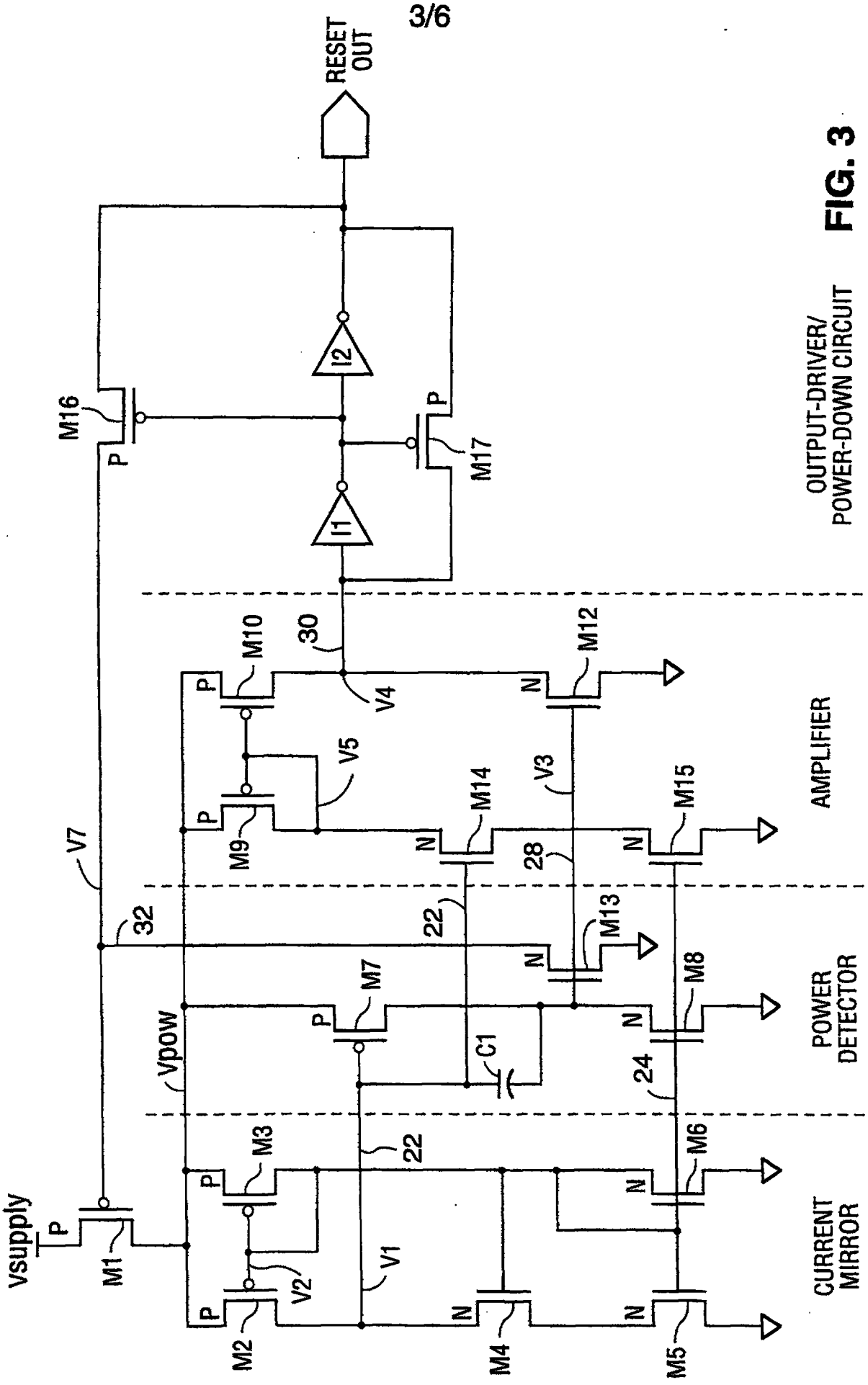

RESET OUT
I2
I1
M16
M17
P
OUTPUT-DRIVER/ POWER-DOWN CIRCUIT
M10
M12
30
V4
V5
V3
M9
M14
M15
28
N
AMPLIFIER
V7
32
22
M13
M7
M8
Vpow
C1
24
POWER DETECTOR
M3
M6
Vsupply
M1
V2
V1
M2
M4
M5
CURRENT MIRROR

FIG. 3

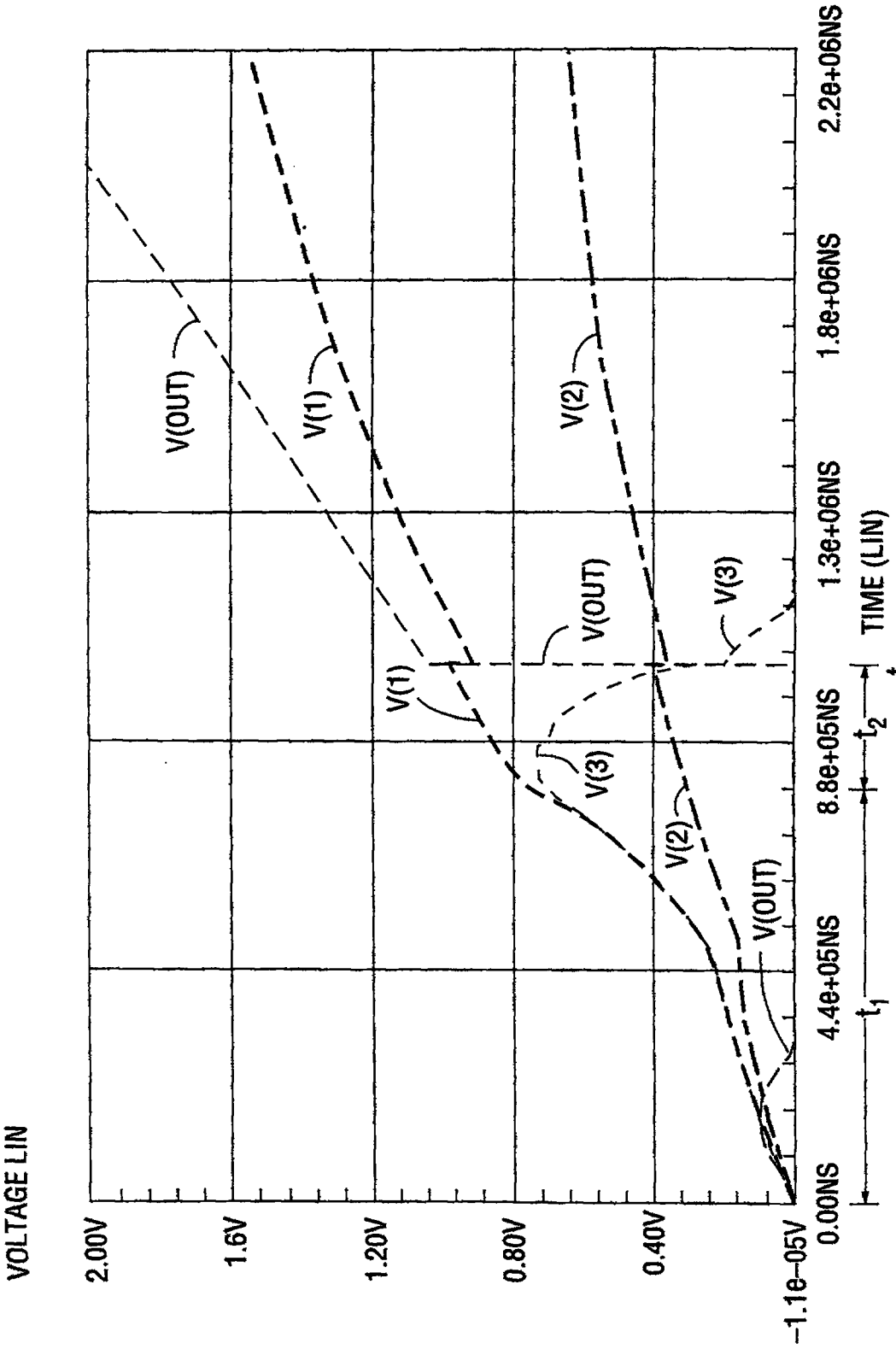

VOLTAGE LIN
2.00V
1.6V
1.20V
0.80V
0.40V
-1.1e-05V
0.00NS
4.4e+05NS
8.8e+05NS
1.3e+06NS
1.8e+06NS
2.2e+06NS
TIME (LIN)
V(OUT)
V(1)
V(2)
V(3)
t1
t2
t3

FIG. 4A

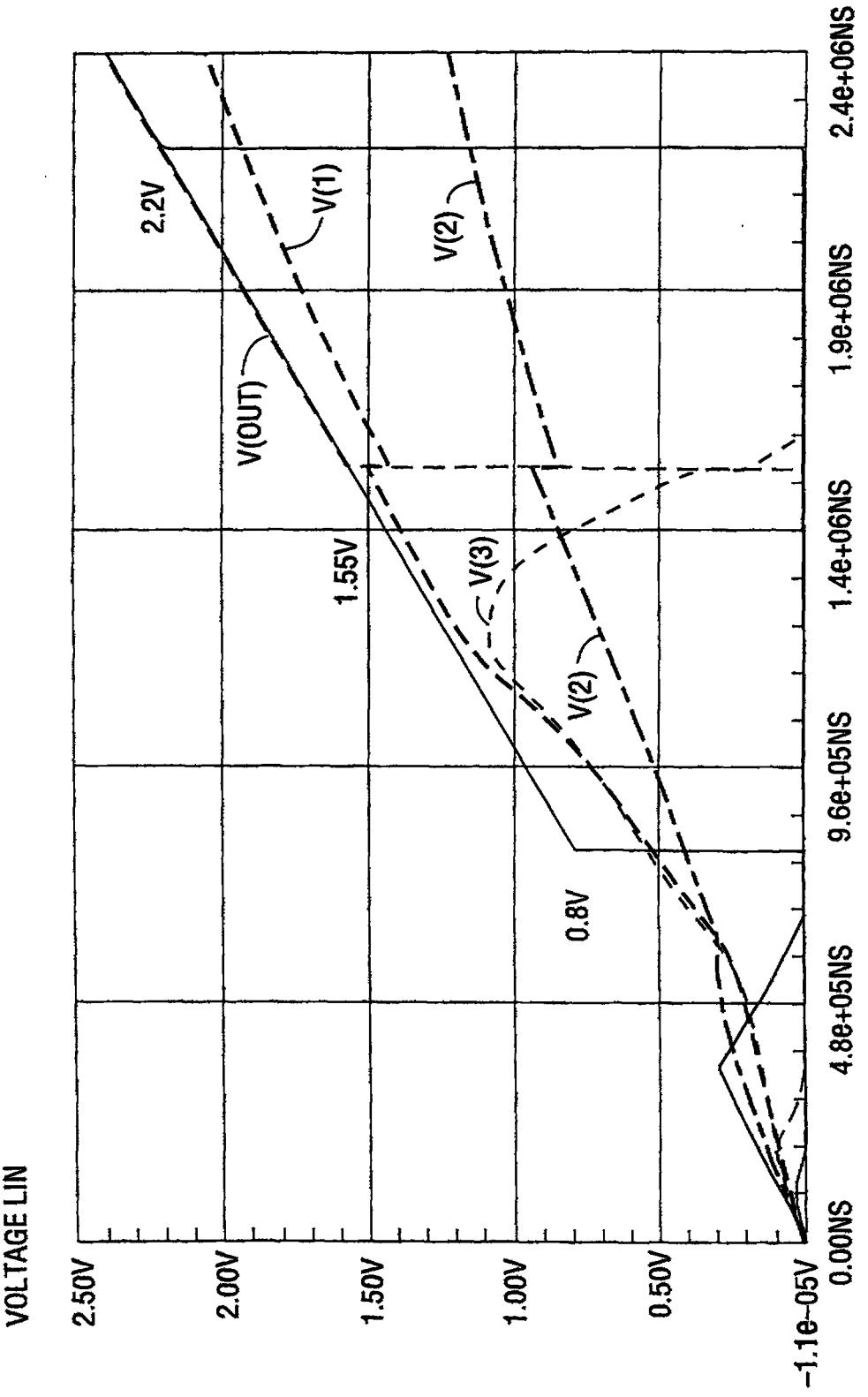

VOLTAGE LIN
2.50V
2.00V
1.50V
1.00V
0.50V
-1.1e-05V
0.00NS
4.8e+05NS
9.6e+05NS
1.4e+06NS
1.9e+06NS
2.4e+06NS
TIME (LIN)
2.2V
V(1)
V(2)
V(OUT)
1.55V
V(3)
V(2)
0.8V

FIG. 4B

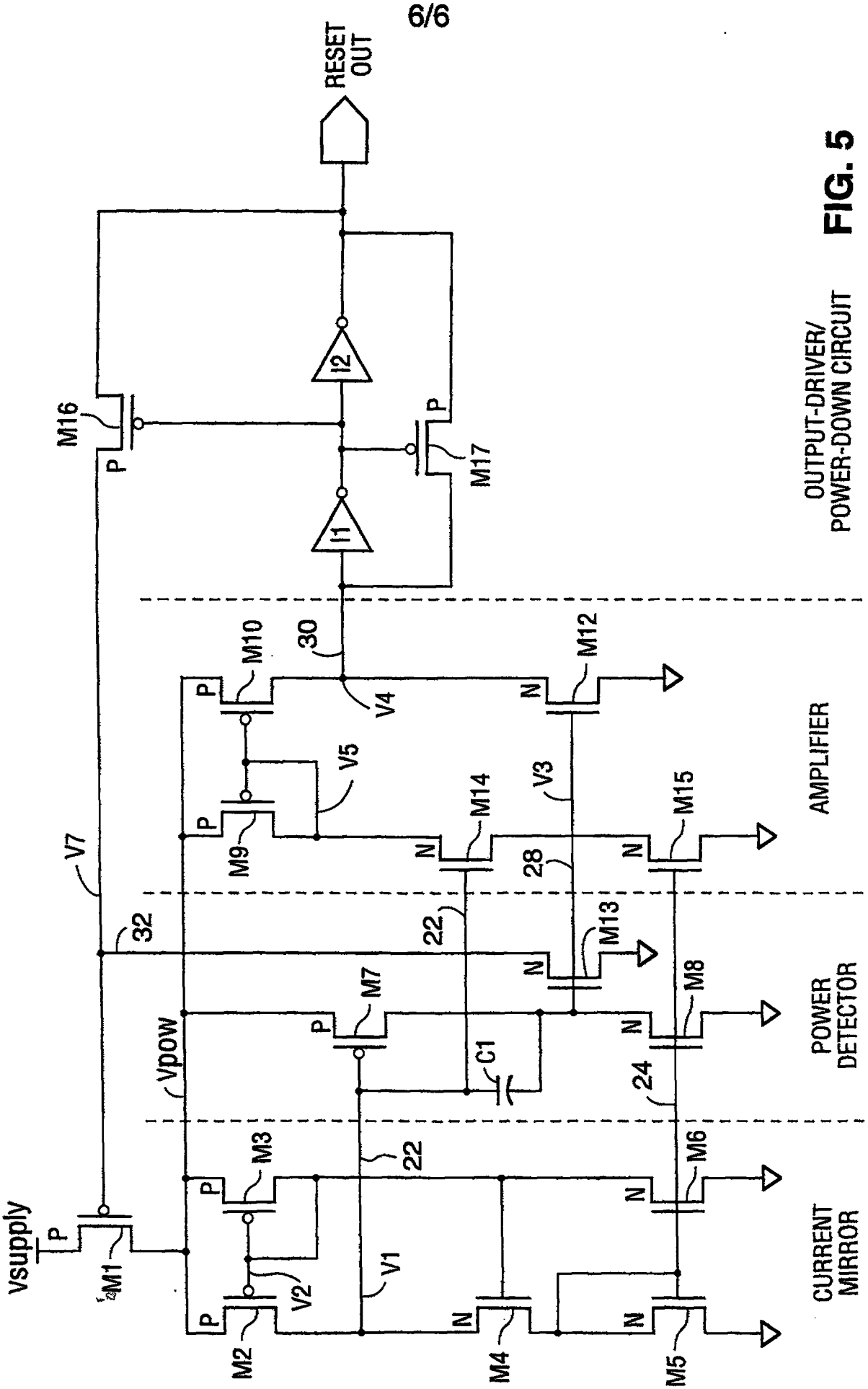

RESET OUT
I2
I1
M16
M17
P
OUTPUT-DRIVER/ POWER-DOWN CIRCUIT
M10
30
M12
V4
V5
V3
M14
M15
M9
28
AMPLIFIER
V7
32
22
M13
M7
M8
Vpow
C1
24
POWER DETECTOR
M3
22
M6
Vsupply
M1
V2
V1
M2
M4
M5
N
CURRENT MIRROR

FIG. 5