【CLAIMS】

1. An electronic device, comprising:

at least one processor, and

a radio frequency (RF) circuit configured to process a data packet associated with a first subscriber identification module (SIM) and a data packet associated with a second SIM,

wherein the at least one processor is configured to:

establish a first packet data unit (PDU) session corresponding to the first SIM,

establish a second PDU session corresponding to the second SIM,

obtain a first information on the first PDU session, wherein the first information on the first PDU session includes a network slice type of the first PDU session and a first IP address corresponds to an IP address of the first PDU session, and

process a first data packet associated with the first IP address using the RF circuit while deferring execution of an operation associated with the second SIM based on the network slice type of the first PDU session corresponding to a specified network slice type, wherein the specified network slice type includes a ultra-reliable and low-latency communications (URLLC).

2. The electronic device of claim 1, wherein the at least one processor is further configured to:

identify the first IP address associated with processing of the first data packet, and

identify that the first IP address corresponds to at least one PDU address of the stored first information.

3. The electronic device of claim 2, wherein the at least one processor is further configured to: store the at least one PDU address, based on a slice/service type (SST) value included in a PDU session establishment accept message corresponding to the first PDU session being at least one specified value.

4. The electronic device of claim 2, wherein the at least one processor is further configured to:

receive a processing request for the first data packet through a first port from a first

application associated with the first SIM, based on a TCP/IP stack, the first port used for data packet transmission/reception between an application layer, where the first application is executed, and the TCP/IP stack, and

identify an IP address associated with the processing of the first data packet based on the first port, based on the TCP/IP stack.

5. The electronic device of claim 4, wherein the at least one processor is further configured to:

request a 3GPP protocol stack to preferentially process the first data packet based on the TCP/IP stack, and

defer execution of an operation associated with the second SIM while processing the first data packet using the RF circuit based on the 3GPP protocol stack.

6. The electronic device of claim 1, wherein the at least one processor is further configured to identify that a PDU session identifier associated with the processing of the first data packet corresponds to at least one PDU session identifier of a stored first information in the electronic device.

7. The electronic device of claim 6, wherein the at least one processor is further configured to: store the at least one PDU session identifier based on an SST value included in a PDU session establishment accept message corresponding to the first PDU session being at least one specified value.

8. The electronic device of claim 6, wherein the at least one processor is further configured to:

receive a processing request for the first data packet through a first network interface from a TCP/IP stack, based on a 3GPP protocol stack, the first network interface used for data packet transmission/reception between the 3GPP protocol stack and the TCP/IP stack, and

identify a PDU session identifier associated with the processing of the first data packet based on the first network interface, based on the 3GPP protocol stack.

9. The electronic device of claim 1, wherein the at least one processor is further configured

to defer a processing request for a second data packet associated with the second SIM and/or a signaling operation associated with the second SIM.

10. The electronic device of claim 1, wherein the at least one processor is further configured to: reject a request for a resource of the RF circuit from a protocol stack associated with the second SIM and/or request the protocol stack associated with the second SIM to defer an operation associated with the second SIM.

11. The electronic device of claim 1, wherein the at least one processor is further configured to: stop deferring execution of an operation associated with the second SIM based on expiration of a designated time and/or completion of processing of the first data packet.

12. The electronic device of claim 1, wherein the at least one processor is further configured to: based on a first application providing the first data packet corresponding to the first information, process the first data packet associated with the first SIM corresponding to a stored information in the electronic device and then process the second data packet.

13. A method of operating an electronic device, the electronic device including at least one processor and a radio frequency (RF) circuit configured to process a data packet associated with a first subscriber identification module (SIM) connected to the at least one processor and a data packet associated with a second SIM connected to the at least one processor, the method comprising:

establishing a first packet data unit (PDU) session corresponding to the first SIM;

establishing a second PDU session corresponding to the second SIM;

obtaining a first information on the first PDU session, wherein the first information on the first PDU session includes a network slice type of the first PDU session and a first IP address corresponds to an IP address of the first PDU session; and

processing a first data packet associated with the first IP address using the RF circuit while deferring execution of an operation associated with the second SIM based on the network slice type of the first PDU session corresponding to a specified network slice type, wherein the specified network slice type includes a ultra-reliable and low-latency communications (URLLC).

14. The method of claim 13, wherein the method further comprises:

identifying the first IP address associated with processing of the first data packet, and

identifying that the first IP address corresponds to at least one PDU address of the first information.