SEMICONDUCTOR INTEGRATED CIRCUIT, COMPUTER SYSTEM, DATA PROCESSOR AND DATA PROCESSING METHOD

ABSTRACT OF THE DISCLOSURE

A memory having a data processing function is connected with a memory network such as a memory bus. A memory controller writes data to be processed in a predetermined area in the memory having the data processing function. The memory having the data processing function processes the written data and stores resultant processed data. Thereafter, the memory controller reads the stored resultant processed data. Accordingly, work data (intermediate data obtained during the processing) is not transferred to the memory controller through the memory bus but is processed within the memory having the data processing function. As a result, the data processing ability can be improved. In the memory having the data processing function, a processing specification for the processing to be executed is written immediately before executing the processing. Accordingly, no work data is transferred through the memory bus between a CPU and the memory serving as a work area for the processing. Thus, the data processing ability can be improved.

(Fig. 1)

This Sheet Has Been Replaced



THIS SHEET HAS BEEN REPLACED