

CLOCK CIRCUIT, MEMORY AND METHOD FOR MANUFACTURING SEMICONDUCTOR STRUCTURE

ABSTRACT

The embodiments of the disclosure relate to the field of semiconductors, and provide a clock circuit, a memory and a method for manufacturing a semiconductor structure. The clock circuit includes at least two first driving circuits and a plurality of discrete first wires located between adjacent first driving circuits, the adjacent first driving circuits are connected through at least one first wire and at least two second wires, the first driving circuits are connected with the second wires, all of the first wires connected between two second wires are connected in series with each other, the first wires are located on a first metal layer, the second wires are located on a second metal layer, and the second metal layer is above the first metal layer.

[Fig. 1]