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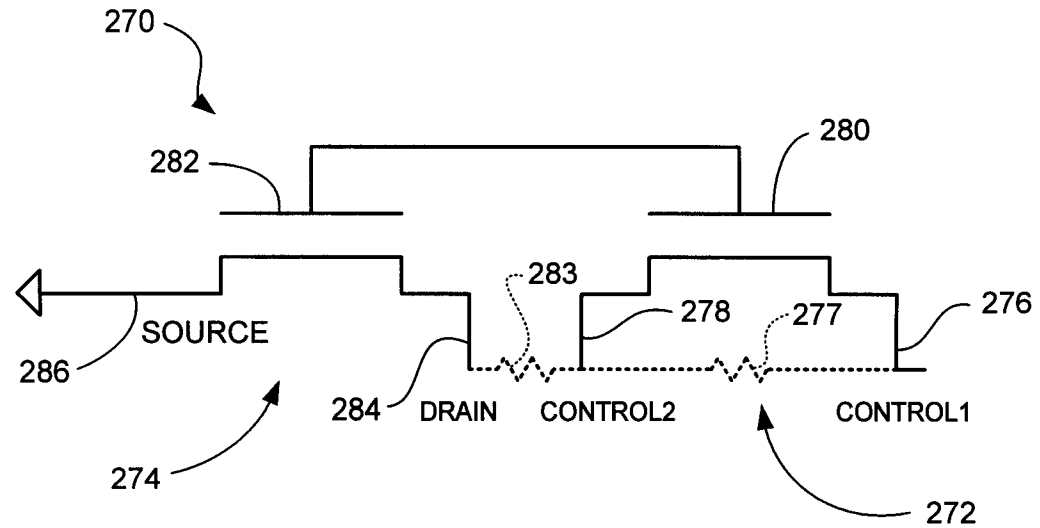
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(57) Abstract: An EPROM cell (270) in a printhead control circuit for an inkjet printer, having exactly one polysilicon layer (256) and a conductive layer (260) disposed above the polysilicon layer, includes a control transistor (272) and an EPROM transistor (274). The control and EPROM transistors each have floating gates (280, 282) comprising a portion of the polysilicon layer (256), and an electrical interconnection, comprising a portion of the conductive layer (260), interconnects the floating gate (280) of the control transistor (272) and the floating gate (282) of the EPROM transistor (274).

WO 2007/120988 A3