VARIABLE CONTROL CHANNEL FOR A WIRELESS COMMUNICATION SYSTEM

[0001] The present application claims priority to provisional U.S. Application Serial No. 60/832,487, entitled "METHOD AND APPARATUS FOR VARIABLE CONTROL CHANNEL STRUCTURE FOR ASYMMETRIC DOWNLINK AND UPLINK ALLOCATIONS," filed July 24, 2006, and U.S. Application Serial No. 60/633,054, entitled "A METHOD AND APPARATUS FOR VARIABLE CONTROL CHANNEL STRUCTURE FOR ASYMMETRIC DOWNLINK AND UPLINK LOCATIONS," filed July 24, 2006, both assigned to the assignee hereof and incorporated herein by reference.

BACKGROUND

I. Field

[0002] The present disclosure relates generally to communication, and more specifically to techniques for sending control information in a wireless communication system.

II. Background

[0003] Wireless communication systems are widely deployed to provide various communication services such as voice, video, packet data, messaging, broadcast, etc. These wireless systems may be multiple-access systems capable of supporting multiple users by sharing the available system resources. Examples of such multiple-access systems include Code Division Multiple Access (CDMA) systems, Time Division Multiple Access (TDMA) systems, Frequency Division Multiple Access (FDMA) systems, Orthogonal FDMA (OFDMA) systems, and Single-Carrier FDMA (SC-FDMA) systems.

[0004] In a wireless communication system, a Node B (or base station) may transmit data to a user equipment (UE) on the downlink and/or receive data from the UE on the uplink. The downlink (or forward link) refers to the communication link from the Node B to the UE, and the uplink (or reverse link) refers to the communication link

from the UE to the Node B. The Node B may also send control information (e.g., assignments of system resources) to the UE. Similarly, the UE may send control information to the Node B to support data transmission on the downlink and/or for other purposes. It is desirable to send data and control information as efficiently as possible in order to improve system performance.

SUMMARY

[0005] Techniques for sending control information on a variable control channel are described herein. The variable control channel may support transmission of one or more types of control information with a variable amount of resources. Different structures for mapping control information to resources may be used depending on various factors such as operating configuration, the available resources for the control channel, the type(s) of control information being sent, the amount of control information being sent for each type, whether or not data is being sent, etc. The structure of the control channel may thus be varied depending on these various factors.

[0006] In one design, at least one type of control information being sent may be determined and may comprise only channel quality indicator (CQI) information, only acknowledgement (ACK) information, both CQI and ACK information, and/or other types of control information. A structure of the control channel may be determined based on operating configuration and/or other factors. The operating configuration may be determined based on system configuration, UE configuration, etc. The system configuration may indicate the number of subframes allocated for the downlink and the number of subframes allocated for the uplink. The UE configuration may indicate downlink and uplink subframes applicable for the UE among the allocated subframes. The control channel structure may be determined based on asymmetry of the downlink and uplink allocations. In one design, the control channel may comprise (i) a fixed amount of resources from a control segment if data is not being sent or (ii) a variable amount of resources from a data segment if data is being sent. The at least one type of control information may be mapped to the resources for the control channel based on the structure. Each type of control information may be mapped to a respective portion of the control channel resources based on the structure.

[0007] Various aspects and features of the disclosure are described in further detail below.

BRIEF DESCRIPTION OF THE DRAWINGS

[0008] FIG. 1 shows a wireless communication system.

[0009] FIG. 2 shows example transmissions on the downlink and uplink.

[0010] FIG. 3 shows a structure for sending data and control information.

[0011] FIG. 4A shows transmission of only control information.

[0012] FIG. 4B shows transmission of data and control information.

[0013] FIG. 5 shows a time structure for a time division duplex (TDD) mode.

[0014] FIG. 6 shows transmission with asymmetric downlink and uplink allocations.

[0015] FIGS. 7A and 7B show control channel structures for sending CQI and/or ACK information on a control segment.

[0016] FIGS. 7C and 7D show control channel structures for sending CQI and/or ACK information on a data segment.

[0017] FIG. 8 shows a process for sending control information.

[0018] FIG. 9 shows an apparatus for sending control information.

[0019] FIG. 10 shows a process for receiving control information.

[0020] FIG. 11 shows an apparatus for receiving control information.

[0021] FIG. 12 shows a block diagram of a Node B and a UE.

[0022] FIG. 13 shows a block diagram of a modulator for control information.

[0023] FIG. 14 shows a block diagram of a modulator for data and control information.

[0024] FIG. 15 shows a block diagram of a demodulator.

DETAILED DESCRIPTION

[0025] **FIG. 1** shows a wireless communication system 100 with multiple Node Bs 110 and multiple UEs 120. A Node B is generally a fixed station that communicates with the UEs and may also be referred to as an evolved Node B (eNode B), a base station, an access point, etc. Each Node B 110 provides communication coverage for a particular geographic area and supports communication for the UEs located within the coverage area. The term "cell" can refer to a Node B and/or its coverage area depending on the context in which the term is used. A system controller 130 may

couple to the Node Bs and provide coordination and control for these Node Bs. System controller 130 may be a single network entity or a collection of network entities, e.g., a Mobility Management Entity (MME)/System Architecture Evolution (SAE) Gateway, a Radio Network Controller (RNC), etc.

[0026] UEs 120 may be dispersed throughout the system, and each UE may be stationary or mobile. A UE may also be referred to as a mobile station, a mobile equipment, a terminal, an access terminal, a subscriber unit, a station, etc. A UE may be a cellular phone, a personal digital assistant (PDA), a wireless communication device, a handheld device, a wireless modem, a laptop computer, etc.

[0027] A Node B may transmit data to one or more UEs on the downlink and/or receive data from one or more UEs on the uplink at any given moment. The Node B may also send control information to the UEs and/or receive control information from the UEs. In FIG. 1, a solid line with double arrows (e.g., between Node B 110a and UE 120b) represents data transmission on the downlink and uplink, and transmission of control information on the uplink. A solid line with a single arrow pointing to a UE (e.g., UE 120e) represents data transmission on the downlink, and transmission of control information on the uplink. A solid line with a single arrow pointing from a UE (e.g., UE 120c) represents transmission of data and control information on the uplink. A dashed line with a single arrow pointing from a UE (e.g., UE 120a) represents transmission of control information (but no data) on the uplink. Transmission of control information on the downlink is not shown in FIG. 1 for simplicity. A given UE may receive data on the downlink, transmit data on the uplink, and/or transmit control information on the uplink at any given moment.

[0028] **FIG. 2** shows example downlink transmission by a Node B and uplink transmission by a UE. The UE may periodically estimate the downlink channel quality for the Node B and may send CQI information to the Node B. The Node B may use the CQI information to select a suitable rate (e.g., a code rate and a modulation scheme) for downlink (DL) data transmission to the UE. The Node B may process and transmit data to the UE when there is data to send and system resources are available. The UE may process a downlink data transmission from the Node B and may send an acknowledgement (ACK) if the data is decoded correctly or a negative acknowledgement (NAK) if the data is decoded in error. The Node B may retransmit

the data if a NAK is received and may transmit new data if an ACK is received. The UE may also transmit data on the uplink (UL) to the Node B when there is data to send and the UE is assigned uplink resources.

[0029] As shown in FIG. 2, the UE may transmit data and/or control information, or neither, in any given time interval. The control information may also be referred to as control, overhead, signaling, etc. The control information may comprise ACK/NAK, CQI, other information, or any combination thereof. The type and amount of control information may be dependent on various factors such as the number of data streams being sent, whether multiple-input multiple-output (MIMO) is used for transmission, etc. For simplicity, much of the following description assumes that control information comprises CQI and ACK information.

[0030] The system may support hybrid automatic retransmission (HARQ), which may also be referred to as incremental redundancy, chase combining, etc. For HARQ on the downlink, the Node B may send a transmission for a packet and may send one or more retransmissions until the packet is decoded correctly by the UE, or the maximum number of retransmissions has been sent, or some other termination condition is encountered. HARQ may improve reliability of data transmission.

[0031] Z HARQ interlaces may be defined, where Z may be any integer value. Each HARQ interlace may include time intervals that are spaced apart by Z time intervals. For example, six HARQ interlaces may be defined, and HARQ interlace z may include time intervals $n+z$, $n+z+6$, $n+z+12$, etc., for $z \in \{1,...,6\}$.

[0032] An HARQ process may refer to all transmission and retransmissions, if any, for a packet. An HARQ process may be started when resources are available and may terminate after the first transmission or after one or more subsequent retransmissions. An HARQ process may have a variable duration that may depend on the decoding results at the receiver. Each HARQ process may be sent on one HARQ interlace. In one design, up to Z HARQ processes may be sent on the Z HARQ interlaces. In another design, multiple HARQ processes may be sent on different resources (e.g., on different sets of subcarriers or from different antennas) in the same HARQ interlace.

[0033] The transmission techniques described herein may be used for uplink transmission as well as downlink transmission. The techniques may also be used for various wireless communication systems such as CDMA, TDMA, FDMA, OFDMA,

and SC-FDMA systems. The terms "system" and "network" are often used interchangeably. A CDMA system may implement a radio technology such as Universal Terrestrial Radio Access (UTRA), cdma2000, etc. UTRA includes Wideband CDMA (W-CDMA) and Low Chip Rate (LCR). cdma2000 covers IS-2000, IS-95 and IS-856 standards. A TDMA system may implement a radio technology such as Global System for Mobile Communications (GSM). An OFDMA system may implement a radio technology such as Evolved UTRA (E-UTRA), IEEE 802.11, IEEE 802.16, IEEE 802.20, Flash-OFDM®, etc. These various radio technologies and standards are known in the art. UTRA, E-UTRA, and GSM are part of Universal Mobile Telecommunication System (UMTS). Long Term Evolution (LTE) is an upcoming release of UMTS that uses E-UTRA. UTRA, E-UTRA, GSM, UMTS and LTE are described in documents from an organization named "3rd Generation Partnership Project" (3GPP). cdma2000 is described in documents from an organization named "3rd Generation Partnership Project 2" (3GPP2). For clarity, certain aspects of the techniques are described below for uplink transmission in LTE, and 3GPP terminology is used in much of the description below.

[0034] LTE utilizes orthogonal frequency division multiplexing (OFDM) on the downlink and single-carrier frequency division multiplexing (SC-FDM) on the uplink. OFDM and SC-FDM partition the system bandwidth into multiple (N) orthogonal subcarriers, which are also commonly referred to as tones, bins, etc. Each subcarrier may be modulated with data. In general, modulation symbols are sent in the frequency domain with OFDM and in the time domain with SC-FDM. For LTE, the spacing between adjacent subcarriers may be fixed, and the total number of subcarriers (N) may be dependent on the system bandwidth. In one design, $N = 512$ for a system bandwidth of 5 MHz, $N = 1024$ for a system bandwidth of 10 MHz, and $N = 2048$ for a system bandwidth of 20 MHz. In general, N may be any integer value.

[0035] **FIG. 3** shows a design of a structure 300 that may be used for sending data and control information on the uplink. The transmission timeline may be partitioned into subframes. A subframe may have a fixed duration, e.g., one millisecond (ms), or a configurable duration. A subframe may be partitioned into two slots, and each slot may include L symbol periods, where L may be any integer value, e.g., $L = 6$ or 7. Each

symbol period may be used for data, control information, pilot, or any combination thereof.

[0036] In the design shown in FIG. 3, the N total subcarriers may be divided into a data section and a control section. The control section may be formed at an edge of the system bandwidth, as shown in FIG. 3. The control section may have a configurable size, which may be selected based on the amount of control information being sent on the uplink by the UEs. The data section may include all subcarriers not included in the control section. The design in FIG. 3 results in the data section including contiguous subcarriers, which allows a single UE to be assigned all of the contiguous subcarriers in the data section.

[0037] A UE may be assigned a control segment of M contiguous subcarriers, where M may be a fixed or configurable value. A control segment may also be referred to as a physical uplink control channel (PUCCH). In one design, a control segment may include an integer multiple of 12 subcarriers. The UE may also be assigned a data segment of Q contiguous subcarriers, where Q may be a fixed or configurable value. A data segment may also be referred to as a physical uplink shared channel (PUSCH). In one design, a data segment may include an integer multiple of 12 subcarriers. The UE may also be assigned no data segment or no control segment in a given subframe.

[0038] It may be desirable for a UE to transmit on contiguous subcarriers using SC-FDM, which is referred to as localized frequency division multiplexing (LFDM). Transmitting on contiguous subcarriers may result in a lower peak-to-average ratio (PAR). PAR is the ratio of the peak power of a waveform to the average power of the waveform. A low PAR is desirable since it may allow a power amplifier (PA) to be operated at an average output power closer to the peak output power. This, in turn, may improve throughput and/or link margin for the UE.

[0039] The UE may be assigned a control segment located near an edge of the system bandwidth. The UE may also be assigned a data segment within the data section when there is data to send. The subcarriers for the control segment may not be adjacent to the subcarriers for the data segment. The UE may send control information in the control segment if there is no data to send on the uplink. The UE may send data and control information in the data segment if there is data to send on the uplink. This dynamic transmission of control information may allow the UE to transmit on

contiguous subcarriers regardless of whether or not data is being sent, which may improve PAR.

[0040] **FIG. 4A** shows transmission of control information in a subframe when there is no data to send on the uplink. The UE may be assigned a control segment, which may be mapped to different sets of subcarriers in the two slots of the subframe. The UE may send control information on the assigned subcarriers for the control segment in each symbol period. The remaining subcarriers may be used by other UEs for uplink transmission.

[0041] **FIG. 4B** shows transmission of data and control information when there is data to send on the uplink. The UE may be assigned a data segment, which may be mapped to different sets of subcarriers in the two slots of a subframe. The UE may send data and control information on the assigned subcarriers for the data segment in each symbol period. The remaining subcarriers may be used by other UEs for uplink transmission.

[0042] FIGS. 4A and 4B show frequency hopping from slot to slot. Frequency hopping may also be performed over other time intervals, e.g., from symbol period to symbol period, from subframe to subframe, etc. Frequency hopping may provide frequency diversity against deleterious path effects and randomization of interference.

[0043] The system may support a frequency division duplex (FDD) mode and/or a time division duplex (TDD) mode. In the FDD mode, separate frequency channels may be used for the downlink and uplink, and downlink transmissions and uplink transmissions may be sent concurrently on their separate frequency channels. In the TDD mode, a common frequency channel may be used for both the downlink and uplink, downlink transmissions may be sent in some time periods, and uplink transmissions may be sent in other time periods.

[0044] **FIG. 5** shows a time structure 500 that may be used for the TDD mode. The transmission timeline may be partitioned into units of frames. Each frame may span a predetermined time duration, e.g., 10 ms, and may be partitioned into a predetermined number of subframes. In each frame, N_{DL} subframes may be allocated for the downlink, and N_{UL} subframes may be allocated for the uplink. N_{DL} and N_{UL} may be any suitable values and may be configurable based on traffic loads for the downlink and uplink and/or other considerations.

[0045] The downlink and uplink may have symmetric or asymmetric allocations depending on the system configuration. For symmetric downlink and uplink allocations, the number of downlink subframes is equal to the number of uplink subframes, or $N_{DL} = N_{UL}$. Each downlink subframe may be associated with a corresponding uplink subframe. For example, a data transmission may be sent in downlink subframe *n*, and control information for the data transmission may be sent in the corresponding uplink subframe *n*, where $n \in \{1, ..., N_{DL}\}$. For asymmetric downlink and uplink allocations, the number of downlink subframes does not match the number of uplink subframes, or $N_{DL} \neq N_{UL}$. Hence, there may not be a one-to-one mapping between downlink and uplink subframes. Asymmetric allocations may allow for more flexible allocation of system resources to match loading conditions but may complicate system operation.

[0046] **FIG. 6** shows an example data transmission with asymmetric downlink and uplink allocations. In this example, M downlink subframes 1 through M may be associated with a single uplink subframe, where M may be any integer value. A UE may be assigned resources in downlink subframes 1 through M as well as the associated uplink subframe. M packets may be sent on M HARQ processes in the M downlink subframes to the UE. The UE may decode each packet and determine ACK information for the packet. The ACK information may also be referred to as ACK feedback and may comprise ACK or NAK. The UE may send ACK information for all M packets in the uplink frame. In FIG. 6, ACK1 is the ACK information for the packet sent on HARQ process H1, and ACKM is the ACK information for the packet sent on HARQ process HM, where H1 through HM may be any available HARQ processes. The ACK information may be used to control transmission of new packets or retransmission of packets decoded in error.

[0047] In an aspect, a variable control channel may be used to support both symmetric and asymmetric downlink and uplink allocations. The control channel may be allocated different amounts of resources, e.g., depending on whether or not data is being sent. The control channel may be used to flexibly send different types of control information and/or different amounts of control information.

[0048] For clarity, specific designs of the variable control channel are described below. In these designs, the control channel may be allocated four resource units in a

control segment when data is not being sent and may be allocated a variable number of resource units in a data segment when data is being sent. A resource unit may correspond to physical resources or logical resources. Physical resources may be resources used for transmission and may be defined by subcarriers, symbol periods, etc. Logical resources may be used to simplify resource allocation and may be mapped to physical resources based on a mapping, a transformation, etc. A resource unit may have any dimension and may be used to send one or more bits of control information. In the following designs, the control channel may be used to send only CQI information, or only ACK information for up to three HARQ processes, or both CQI and ACK information, or no control information.

[0049] **FIG. 7A** shows designs of control channel structures for sending ACK information for up to three HARQ processes on the control segment when CQI and data are not sent. In FIG. 7A, the four resource units for the control segment may be represented by a 2×2 matrix. The first and second rows of the matrix may correspond to two virtual frequency resources (VFR) S1 and S2, respectively. A VFR may be a set of subcarriers, may be mapped to a set of subcarriers, or may correspond to some other logical or physical resources. The first and second columns of the matrix may correspond to two slots T1 and T2, respectively, of one subframe. The four blocks of the 2×2 matrix may correspond to four resource units for the control channel. In the following description, H1, H2 and H3 may be any three different HARQ processes.

[0050] In one design, the ACK information for one HARQ process H1 (ACK1) may be sent on all four resource units for the control segment as shown by a structure 712. For example, the ACK information may be repeated four times and sent on all four resource units to improve reliability.

[0051] In one design, the ACK information for two HARQ processes H1 and H2 may be sent on the four resource units for the control segment as shown by a structure 714. In this design, the ACK information for HARQ process H1 (ACK1) may be sent on two resource units occupying VFR S1 in slots T1 and T2. The ACK information for HARQ process H2 (ACK2) may be sent on two resource units occupying VFR S2 in slots T1 and T2.

[0052] In one design, the ACK information for three HARQ processes H1, H2 and H3 may be sent on the four resource units for the control segment as shown by a

structure 716. In this design, the ACK information for HARQ process H1 (ACK1) may be sent on one resource unit occupying VFR S1 in slot T1. The ACK information for HARQ process H2 (ACK2) may be sent on one resource unit occupying VFR S2 in slot T1. The ACK information for HARQ process H3 (ACK3) may be sent on one resource unit occupying VFR S1 in slot T2. The remaining resource unit may be shared by the three HARQ processes in a time division multiplexed (TDM) manner. For example, this resource unit may be used for the ACK information for HARQ process H1 in one subframe, then for the ACK information for HARQ process H2 in the next subframe, then for the ACK information for HARQ process H3 in the next subframe, etc. In another design, the ACK information for all three HARQ processes may be encoded with a (4, 3) block code and sent on all four resource units. The ACK information for the three HARQ processes may also be sent in other manners.

[0053] **FIG. 7B** shows designs of control channel structures for sending CQI and ACK information for up to three HARQ processes on the control segment when data is not sent. In one design, CQI information may be sent on all four resource units for the control segment, as shown by a structure 720, when no ACK information is sent.

[0054] In one design, the CQI and ACK information for one HARQ process H1 may be sent on the four resource units for the control segment as shown by a structure 722. In this design, the CQI information may be sent on two resource units occupying VFR S1 in slots T1 and T2. The ACK information for HARQ process H1 may be sent on two resource units occupying VFR S2 in slots T1 and T2.

[0055] In one design, the CQI and ACK information for two HARQ processes H1 and H2 may be sent on the four resource units for the control segment as shown by a structure 724. In this design, the CQI information may be sent on two resource units occupying VFR S1 in slots T1 and T2. The ACK information for HARQ process H1 may be sent on one resource unit occupying VFR S2 in slot T1. The ACK information for HARQ process H2 may be sent on one resource unit occupying VFR S2 in slot T2.

[0056] In one design, the CQI and ACK information for three HARQ processes H1, H2 and H3 may be sent on the four resource units for the control segment as shown by a structure 726. In this design, the CQI information may be sent on one resource unit occupying VFR S1 in slot T1. The ACK information for HARQ process H1 may be sent on one resource unit occupying VFR S2 in slot T1. The ACK information for

HARQ process H2 may be sent on one resource unit occupying VFR S1 in slot T2. The ACK information for HARQ process H3 may be sent on one resource unit occupying VFR S2 in slot T2.

[0057] **FIG. 7C** shows designs of control channel structures for sending ACK information for up to three HARQ processes on the data segment when data is being sent but not CQI. The data segment may include 2K resource units and may be represented by a $K \times 2$ matrix, where K may be any value. The K rows of the matrix may correspond to K VFRs $S1'$ through SK', where $S1'$ may be the lowest index and SK' may be the highest index of the K VFRs for the data segment. The first and second columns of the matrix may correspond to two slots T1 and T2, respectively, of one subframe. The 2K blocks of the $K \times 2$ matrix may correspond to 2K resource units. A resource unit for the data segment may have the same or different dimension as a resource unit for the control segment. As shown in FIG. 7C, different numbers of resource units may be taken from the data segment and used to send different amounts of control information. The remaining resource units in the data segment may be used to send data.

[0058] In one design, the ACK information for one HARQ process H1 may be sent on two resource units for the data segment as shown by a structure 732. The two resource units may occupy VFR $S1'$ in slots T1 and T2. The remaining $2K-2$ resource units may be used for data.

[0059] In one design, the ACK information for two HARQ processes H1 and H2 may be sent on four resource units for the data segment as shown by a structure 734. In this design, the ACK information for HARQ process H1 may be sent on two resource units occupying VFR $S1'$ in slots T1 and T2. The ACK information for HARQ process H2 may be sent on two resource units occupying VFR $S2'$ in slots T1 and T2. The remaining $2K-4$ resource units may be used for data.

[0060] In one design, the ACK information for three HARQ processes H1, H2 and H3 may be sent on six resource units for the data segment as shown by a structure 736. In this design, the ACK information for HARQ process H1 may be sent on two resource units occupying VFR $S1'$ in slots T1 and T2. The ACK information for HARQ process H2 may be sent on two resource units occupying VFR $S2'$ in slots T1 and T2. The ACK information for HARQ process H3 may be sent on two resource units occupying

VFRs S3′ for the data segment in slots T1 and T2. The remaining $2K-6$ resource units may be used for data.

[0061] **FIG. 7D** shows designs of control channel structures for sending CQI and ACK information for up to three HARQ processes on the data segment when data is being sent. In one design, the CQI information may be sent on two resource units for the data segment as shown by a structure 740. These two resource units may occupy VFR S1′ in slots T1 and T2. The remaining $2K-2$ resource units may be used for data.

[0062] In one design, the CQI and ACK information for one HARQ process H1 may be sent on four resource units for the data segment as shown by a structure 742. In this design, the CQI information may be sent on two resource units occupying VFR S1′ in slots T1 and T2. The ACK information for HARQ process H1 may be sent on two resource units occupying VFR S2′ in slots T1 and T2. The remaining $2K-4$ resource units may be used for data.

[0063] In one design, the CQI and ACK information for two HARQ processes H1 and H2 may be sent on six resource units for the data segment as shown by a structure 744. In this design, the CQI information may be sent on two resource units occupying VFR S1′ in slots T1 and T2. The ACK information for HARQ process H1 may be sent on two resource units occupying VFR S2′ in slots T1 and T2. The ACK information for HARQ process H2 may be sent on two resource units occupying VFR S3′ in slots T1 and T2. The remaining $2K-6$ resource units may be used for data.

[0064] In one design, the CQI and ACK information for three HARQ processes H1, H2 and H3 may be sent on eight resource units for the data segment as shown by a structure 746. In this design, the CQI information may be sent on two resource units occupying VFR S1′ in slots T1 and T2. The ACK information for HARQ process H1 may be sent on two resource units occupying VFR S2′ in slots T1 and T2. The ACK information for HARQ process H2 may be sent on two resource units occupying VFR S3′ in slots T1 and T2. The ACK information for HARQ process H3 may be sent on two resource units occupying VFR S4′ in slots T1 and T2. The remaining $2K-8$ resource units may be used for data.

[0065] FIGS. 7A through 7D show specific designs of control channel structures for sending CQI and ACK information in the control segment and the data segment. These

designs show specific mapping of CQI and/or ACK information to resource units available to send control information. The CQI and ACK information may also be mapped to the available resource units in various other manners. As an example, instead of using structure 714 in FIG. 7A, the ACK information for HARQ process H1 may be sent on (i) the upper left and lower right resource units in the matrix, (ii) the lower left and upper right resource units in the matrix, (iii) the upper left and lower left resource units in the matrix, etc. As another example, a block code may be used for all of the control information being sent, and the resultant codeword may be sent on all of the available resource units.

[0066] The CQI and ACK information may be multiplexed in various manners, e.g., using time division multiplexing (TDM), frequency division multiplexing (FDM), code division multiplexing (CDM), etc., or a combination thereof. In the designs shown in FIGS. 7A through 7D, a combination of TDM and FDM may be used for the control channel. In these designs, each VFR may correspond to a set of subcarriers. For example, 12 subcarriers may be allocated for the control segment, each VFR may correspond to six subcarriers, and one resource unit may correspond to six subcarriers in L symbol periods for one slot. CQI or ACK information for each HARQ process may be sent in the assigned resource unit(s), e.g., as shown in FIGS. 7A through 7D.

[0067] TDM may also be used for the control information. In this case, all control information mapped to a given slot may be processed (e.g., jointly encoded) and sent on all subcarriers for the control channel in that slot. As an example, for structure 726 in FIG. 7B, the CQI and ACK information for HARQ process H1 may be processed and sent on all subcarriers in slot T1, and the ACK information for HARQ processes H2 and H3 may be processed and sent on all subcarriers in slot T2.

[0068] FDM may also be used for the control information. In this case, all control information mapped to a given VFR may be processed (e.g., jointly encoded) and sent on all subcarriers in that VFR over two slots. As an example, for structure 726 in FIG. 7B, the CQI and ACK information for HARQ process H2 may be processed and sent on the subcarriers in VFR S1 over both slots T1 and T2, and the ACK information for HARQ processes H1 and H3 may be processed and sent on all subcarriers in VFR S2 over both slots T1 and T2.

[0069] CDM may also be used for the control information. In this case, the CQI and ACK information may be spread with orthogonal codes, combined, and then mapped to all resources available to send control information.

[0070] The control information may also be sent by varying the modulation order. For example, BPSK may be used to send one bit of control information, QPSK may be used to send two information bits, 8-PSK may be used to send three information bits, 16-QAM may be used to send four information bits, etc.

[0071] The designs in FIGS. 7A through 7D assume two types of control information being sent - CQI and ACK information. In general, any number and any type of control information may be sent on the control channel. For example, the control information may comprise information identifying one or more desired subbands among all subbands, information for one or more precoding/beamforming matrices or one or more antennas for MIMO transmission, a resource request, etc. In general, a fixed or variable amount of control information may be sent for each type. The amount of ACK information may be dependent on the number of HARQ processes being acknowledged. The amount of CQI information may be fixed (as shown in FIGS. 7A through 7D) or variable (e.g., dependent on whether or not MIMO is used, the number of streams being sent using MIMO, etc.).

[0072] The designs in FIGS. 7A through 7D assume that the control channel includes (i) a fixed number of resource units when data is not being sent and (ii) a variable number of resource units when data is being sent. In general, the control channel may include (i) a fixed or variable number of resource units when data is not being sent and (ii) a fixed or variable number of resource units when data is being sent. The number of resource units available for the control channel may be different than shown in FIG. 7A through 7D.

[0073] In general, the variable control channel may have different structures depending on one or more of the following:

- System configuration, e.g., allocations for the downlink and uplink such as the number of downlink subframes and the number of uplink subframes,
- UE configuration, e.g., the downlink and uplink subframes applicable for the UE,
- The amount of resources available for the control channel,

- The type(s) of control information being sent on the control channel, e.g., CQI and/or ACK information,
- The amount of control information being sent for each type, e.g., the number of HARQ processes being acknowledged,
- Whether or not data is being sent, which may determine the size and location of the control channel, and
- The desired reliability for each type of control information.

[0074] The variable control channel may support transmission of one or more types of control information with a variable amount of resources. Different structures for mapping control information to control channel resources may be used depending on various factors such as those given above. The structure of the control channel may thus be varied depending on the various factors.

[0075] **FIG. 8** shows a design of a process 800 for sending control information. Process 800 may be performed by a UE for the uplink (e.g., as described above) or by a Node B for the downlink. At least one type of control information being sent may be determined (block 812). The control information being sent may comprise only CQI information, only ACK information, both CQI and ACK information, and/or other types of control information. A structure of a control channel may be determined based on operating configuration and/or the factors noted above (block 814). The operating configuration may be determined based on system configuration (e.g., asymmetry of downlink and uplink allocations), UE configuration (e.g., applicable downlink and uplink subframes), etc. A plurality of structures may be supported for the control channel, some examples of which are given in FIGS. 7A through 7D. One of the supported structures may be selected based on the operating configuration and/or other factors. The control channel may comprise (i) a fixed amount of resources from a control segment if data is not being sent or (ii) a variable amount of resources from a data segment if data is being sent. The control and data segments may occupy different frequency locations.

[0076] The at least one type of control information may be mapped to resources for the control channel based on the structure (block 816). The control channel resources may comprise time resources, frequency resources, code resources, etc., or any combination thereof. Each type of control information may be mapped to a respective

portion of the control channel resources based on the structure. Only CQI information may be sent and may be mapped to all of the control channel resources, e.g., as shown by structure 720 in FIG. 7B and structure 740 in FIG. 7D. Only ACK information may be sent and may be mapped to all of the control channel resources, e.g., as shown by structures 712 to 716 in FIG. 7A and structures 732 to 736 in FIG. 7C. Both CQI and ACK information may be sent and may be mapped to the resources for the control channel based on the structure, e.g., as shown by structures 722 to 726 in FIG. 7B and structures 742 to 746 in FIG. 7D.

[0077] **FIG. 9** shows a design of an apparatus 900 for sending control information. Apparatus 900 includes means for determining at least one type of control information being sent (module 912), means for determining a structure of a control channel based on operating configuration (e.g., asymmetry of downlink and uplink allocations) and/or other factors (module 914), and means for mapping the at least one type of control information to resources for the control channel based on the structure (module 916).

[0078] **FIG. 10** shows a design of a process 1000 for receiving control information. Process 1000 may be performed by a Node B for the uplink (e.g., as described above) or by a UE for the downlink. At least one type of control information being received may be determined (block 1012). A structure of a control channel may be determined based on operating configuration, which may indicate asymmetry of downlink and uplink allocations, and/or other factors (block 1014). The at least one type of control information may be received from resources for the control channel based on the structure (block 1016). For example, CQI information, or ACK information, or both CQI and ACK information may be received from the resources for the control channel based on the structure.

[0079] **FIG. 11** shows a design of an apparatus 1100 for receiving control information. Apparatus 1100 includes means for determining at least one type of control information being received (module 1112), means for determining a structure of a control channel based on operating configuration and/or other factors (module 1114), and means for receiving the at least one type of control information from resources for the control channel based on the structure (module 1116).

[0080] The modules in FIGS. 9 and 11 may comprise processors, electronics devices, hardware devices, electronics components, logical circuits, memories, etc., or any combination thereof.

[0081] **FIG. 12** shows a block diagram of a design of a Node B 110 and a UE 120, which are one of the Node Bs and one of the UEs in FIG. 1. At UE 120, a transmit (TX) data and control processor 1210 may receive uplink (UL) data from a data source (not shown) and/or control information from a controller/processor 1240. Processor 1210 may process (e.g., format, encode, interleave, and symbol map) the data and control information and provide modulation symbols. A modulator (MOD) 1220 may process the modulation symbols as described below and provide output chips. A transmitter (TMTR) 1222 may process (e.g., convert to analog, amplify, filter, and frequency upconvert) the output chips and generate an uplink signal, which may be transmitted via an antenna 1224.

[0082] At Node B 110, an antenna 1252 may receive the uplink signals from UE 120 and other UEs and may provide a received signal to a receiver (RCVR) 1254. Receiver 1254 may condition (e.g., filter, amplify, frequency downconvert, and digitize) the received signal and provide received samples. A demodulator (DEMOD) 1260 may process the received samples as described below and provide demodulated symbols. A receive (RX) data and control processor 1270 may process (e.g., symbol demap, deinterleave, and decode) the demodulated symbols to obtain decoded data and control information for UE 120 and other UEs.

[0083] On the downlink, at Node B 110, downlink (DL) data and control information to be sent to the UEs may be processed by a TX data and control processor 1290, modulated by a modulator 1292 (e.g., for OFDM), conditioned by a transmitter 1294, and transmitted via antenna 1252. At UE 120, the downlink signals from Node B 110 and possibly other Node Bs may be received by antenna 1224, conditioned by a receiver 1230, demodulated by a demodulator 1232 (e.g., for OFDM), and processed by an RX data and control processor 1234 to recover the downlink data and control information sent by Node B 110 to UE 120. In general, the processing for uplink transmission may be similar to or different from the processing for downlink transmission.

[0084] Controllers/processors 1240 and 1280 may direct the operations at UE 120 and Node B 110, respectively. Memories 1242 and 1282 may store data and program codes for UE 120 and Node B 110, respectively. A scheduler 1284 may schedule UEs for downlink and/or uplink transmission and may provide assignments of system resources (e.g., assignments of subcarriers for downlink and/or uplink) for the scheduled UEs.

[0085] **FIG. 13** shows a block diagram of a design of a modulator 1220a for control information. Modulator 1220a may be used for modulator 1220 at UE 120 in FIG. 12 when data is not sent.

[0086] A TX control processor 1310, which may be part of TX data and control processor 1210 in FIG. 12, may receive and process CQI and/or ACK information to be sent in a subframe. In one design, if only ACK information is being sent in a given slot, then TX control processor 1310 may generate a modulation symbol for the ACK/NAK for each HARQ process, e.g., by mapping an ACK to one QPSK value (e.g., $1+j$) and a NAK to another QPSK value (e.g., $-1-j$). Processor 1310 may then repeat the QPSK symbol for each HARQ process to obtain L modulation symbols for L symbol periods in one slot and may provide one modulation symbol in each symbol period. If only CQI information is being sent in a given slot, then TX control processor 1310 may encode the CQI information based on a block code to obtain code bits, map the code bits to L modulation symbols, and provide one modulation symbol in each symbol period. If both CQI and ACK information is being sent in a given slot, then TX control processor 1310 may encode the CQI and ACK information jointly based on another block code to obtain code bits, map the code bits to L modulation symbols, and provide one modulation symbol in each symbol period. In another design, processor 1310 may process the CQI and ACK information separately and may provide two modulation symbols for CQI and ACK for the two VFRs S1 and S2 in each symbol period, e.g., as shown in FIGS. 7A and 7B. TX control processor 1310 may also generate modulation symbols for CQI and/or ACK in other manners.

[0087] Within modulator 1220a, a unit 1322 may receive the modulation symbols for CQI and/or ACK from TX control processor 1310, e.g., one or two modulation symbols in each symbol period. For each modulation symbol, unit 1322 may modulate a CAZAC (constant amplitude zero auto-correlation) sequence with that modulation

symbol to obtain a corresponding modulated CAZAC sequence with modulated symbols. A CAZAC sequence is a sequence having good temporal characteristics (e.g., a constant time-domain envelope) and good spectral characteristics (e.g., a flat frequency spectrum). Some example CAZAC sequences include a Chu sequence, a Zadoff-Chu sequence, a Frank sequence, a generalized chirp-like (GCL) sequence, a Golomb sequence, P1, P3, P4 and Px sequences, etc., which are known in the art. In each symbol period, unit 1322 may provide M modulated symbols for the M subcarriers in the control segment assigned to UE 120.

[0088] A spectral shaping unit 1330 may perform spectral shaping on the M modulated symbols in each symbol period and provide M spectrally shaped symbols. A symbol-to-subcarrier mapping unit 1332 may map the M spectrally shaped symbols to the M subcarriers in the control segment assigned to UE 120 and may map zero symbols with signal value of zero to the remaining subcarriers. An inverse discrete Fourier transform (IDFT) unit 1334 may receive N mapped symbols for the N total subcarriers from mapping unit 1332, perform an N-point IDFT on these N symbols to transform the symbols from the frequency domain to the time domain, and provide N time-domain output chips. Each output chip is a complex value to be transmitted in one chip period. A parallel-to-serial converter (P/S) 1336 may serialize the N output chips and provide a useful portion of an SC-FDM symbol. A cyclic prefix generator 1338 may copy the last C output chips of the useful portion and append these C output chips to the front of the useful portion to form an SC-FDM symbol containing $N + C$ output chips. The cyclic prefix is used to combat inter-symbol interference (ISI) caused by frequency selective fading. The SC-FDM symbol may be sent in one SC-FDM symbol period, which may be equal to $N + C$ chip periods.

[0089] **FIG. 14** shows a block diagram of a design of a modulator 1220b for data and control information. Modulator 1220b may be used for modulator 1220 in FIG. 12 when data is sent. TX control processor 1310 may process control information and provide modulation symbols for control information to modulator 1220b. A TX data processor 1312, which may be part of TX data and control processor 1210 in FIG. 12, may receive data to send, encode the data based on a coding scheme to obtain code bits, interleave the code bits, and map the interleaved bits to modulation symbols based on a modulation scheme.

[0090] Within modulator 1220b, a serial-to-parallel converter (S/P) 1326 may receive the modulation symbols from TX control processor 1310 and the modulation symbols from TX data processor 1312. S/P 1326 may provide Q modulation symbols in each symbol period, where Q is the number of subcarriers in the data segment assigned to UE 120. A discrete Fourier transform (DFT) unit 1328 may perform a Q-point DFT on the Q modulation symbols to transform these symbols from the time domain to the frequency domain and may provide Q frequency-domain symbols. Spectral shaping unit 1330 may perform spectral shaping on the Q frequency-domain symbols and provide Q spectrally shaped symbols. Symbol-to-subcarrier mapping unit 1332 may map the Q spectrally shaped symbols to the Q subcarriers in the data segment and may map zero symbols to the remaining subcarriers. IDFT unit 1334 may perform an N-point IDFT on the N mapped symbols from unit 1332 and provide N time-domain output chips. P/S 1336 may serialize the N output chips, and cyclic prefix generator 1338 may append a cyclic prefix to form an SC-FDM symbol containing $N + C$ output chips.

[0091] FIGS. 13 and 14 show example designs for sending control information without and with data, respectively. Control information may also be sent in various other manners. In another design, when only control information is sent, CQI and/or ACK information may be encoded separately, multiplexed, transformed with a DFT, and mapped to subcarriers for the control segment, similar to the design shown in FIG. 14. In another design, CQI and/or ACK information may be jointly encoded, multiplexed, transformed with a DFT, and mapped to subcarriers for the control segment. Control information may also be sent with data based on other designs beside the design shown in FIG. 14.

[0092] In the designs shown in FIGS. 13 and 14, control information may be processed based on a first processing scheme when data is not sent and based on a second processing scheme when data is sent. When sent alone, control information may be sent using a CAZAC sequence to achieve a lower PAR. When sent with data, control information may be multiplexed with data and processed in similar manner as data. Control information may also be processed in other manners. For example, control information may be sent using CDM, e.g., by spreading each modulation symbol

for control information with an orthogonal code and mapping the spread modulation symbols to the resources for the control channel.

[0093] **FIG. 15** shows a block diagram of a design of demodulator 1260 at Node B 110 in FIG. 12. Within demodulator 1260, a cyclic prefix removal unit 1510 may obtain $N+C$ received samples in each SC-FDM symbol period, remove C received samples corresponding to the cyclic prefix, and provide N received samples for the useful portion of a received SC-FDM symbol. An S/P 1512 may provide the N received samples in parallel. A DFT unit 1514 may perform an N-point DFT on the N received samples and provide N received symbols for the N total subcarriers. These N received symbols may contain data and control information for all UEs transmitting to Node B 150. The processing to recover control information and/or data from UE 120 is described below.

[0094] If control information and data are sent by UE 120, then a symbol-to-subcarrier demapping unit 1516 may provide Q received symbols from the Q subcarriers for the data segment assigned to UE 120 and may discard the remaining received symbols. A unit 1518 may scale the Q received symbols based on the spectral shaping performed by UE 120. Unit 1518 may further perform data detection (e.g., matched filtering, equalization, etc.) on the Q scaled symbols with channel gain estimates and provide Q detected symbols. An IDFT unit 1520 may perform a Q-point IDFT on the Q detected symbols and provide Q demodulated symbols for data and control information. A P/S 1522 may provide demodulated symbols for data to an RX data processor 1550 and may provide demodulated symbols for control information to a multiplexer (Mux) 1532, which may provide these symbols to an RX control processor 1552. Processors 1550 and 1552 may be part of RX data and control processor 1270 in FIG. 12. RX data processor 1550 may process (e.g., symbol demap, deinterleave, and decode) the demodulated symbols for data and provide decoded data. RX control processor 1552 may process the demodulated symbols for control information and provide decoded control information, e.g., CQI and/or ACK.

[0095] If control information and no data is sent by UE 120, then symbol-to-subcarrier demapping unit 1516 may provide M received symbols from the M subcarriers for the control segment assigned to UE 120 and may discard the remaining received symbols. A CAZAC sequence detector 1530 may detect one or more

modulation symbols most likely to have been sent in a symbol period based on the M received symbols for that symbol period. Detector 1530 may provide demodulated symbols for control information, which may be routed through multiplexer 1532 and provided to RX control processor 1552.

[0096] It is understood that the specific order or hierarchy of steps in the processes disclosed is an example of exemplary approaches. Based upon design preferences, it is understood that the specific order or hierarchy of steps in the processes may be rearranged while remaining within the scope of the disclosure. The accompanying method claims present elements of the various steps in a sample order, and are not meant to be limited to the specific order or hierarchy presented.

[0097] Those of skill in the art would understand that information and signals may be represented using any of a variety of different technologies and techniques. For example, data, instructions, commands, information, signals, bits, symbols, and chips that may be referenced throughout the above description may be represented by voltages, currents, electromagnetic waves, magnetic fields or particles, optical fields or particles, or any combination thereof.

[0098] Those of skill would further appreciate that the various illustrative logical blocks, modules, circuits, and algorithm steps described in connection with the disclosure herein may be implemented as electronic hardware, computer software, or combinations of both. To clearly illustrate this interchangeability of hardware and software, various illustrative components, blocks, modules, circuits, and steps have been described above generally in terms of their functionality. Whether such functionality is implemented as hardware or software depends upon the particular application and design constraints imposed on the overall system. Skilled artisans may implement the described functionality in varying ways for each particular application, but such implementation decisions should not be interpreted as causing a departure from the scope of the present disclosure.

[0099] The various illustrative logical blocks, modules, and circuits described in connection with the disclosure herein may be implemented or performed with a general-purpose processor, a digital signal processor (DSP), an application specific integrated circuit (ASIC), a field programmable gate array (FPGA) or other programmable logic device, discrete gate or transistor logic, discrete hardware components, or any

combination thereof designed to perform the functions described herein. A general-purpose processor may be a microprocessor, but in the alternative, the processor may be any conventional processor, controller, microcontroller, or state machine. A processor may also be implemented as a combination of computing devices, e.g., a combination of a DSP and a microprocessor, a plurality of microprocessors, one or more microprocessors in conjunction with a DSP core, or any other such configuration.

[00100] The steps of a method or algorithm described in connection with the disclosure herein may be embodied directly in hardware, in a software module executed by a processor, or in a combination of the two. A software module may reside in RAM memory, flash memory, ROM memory, EPROM memory, EEPROM memory, registers, hard disk, a removable disk, a CD-ROM, or any other form of storage medium known in the art. An exemplary storage medium is coupled to the processor such that the processor can read information from, and write information to, the storage medium. In the alternative, the storage medium may be integral to the processor. The processor and the storage medium may reside in an ASIC. The ASIC may reside in a user terminal. In the alternative, the processor and the storage medium may reside as discrete components in a user terminal.

[00101] The previous description of the disclosure is provided to enable any person skilled in the art to make or use the disclosure. Various modifications to the disclosure will be readily apparent to those skilled in the art, and the generic principles defined herein may be applied to other variations without departing from the scope of the disclosure. Thus, the disclosure is not intended to be limited to the examples and designs described herein but is to be accorded the widest scope consistent with the principles and novel features disclosed herein.

[00102] WHAT IS CLAIMED IS:

CLAIMS

1. An apparatus comprising:

at least one processor configured to determine at least one type of control information being sent, to determine a structure of a control channel based on operating configuration, and to map the at least one type of control information to resources for the control channel based on the structure; and

a memory coupled to the at least one processor.

2. The apparatus of claim 1, wherein the resources for the control channel comprise at least one of time resources, frequency resources, and code resources.

3. The apparatus of claim 1, wherein the at least one processor is configured to determine the structure of the control channel from among a plurality of structures supported for the control channel.

4. The apparatus of claim 1, wherein the operating configuration is determined based on at least one of system configuration and user equipment (UE) configuration.

5. The apparatus of claim 4, wherein the system configuration is indicative of allocations for downlink and uplink, and wherein the at least one processor is configured to determine the structure of the control channel based on asymmetry of the downlink and uplink allocations.

6. The apparatus of claim 4, wherein the at least one processor is configured to determine the structure of the control channel based on number of subframes allocated for downlink and number of subframes allocated for uplink as indicated by the system configuration

7. The apparatus of claim 1, wherein the at least one processor is configured to determine the structure of the control channel further based on at least one

of an amount of resources for the control channel, the at least one type of control information being sent, and an amount of control information for each type of control information being sent.

8. The apparatus of claim 1, wherein the at least one type of control information being sent comprises acknowledgement (ACK) information, and wherein the at least one processor is configured to determine the structure of the control channel further based on number of hybrid automatic retransmission (HARQ) processes to be acknowledged by the ACK information.

9. The apparatus of claim 1, wherein the at least one processor is configured to map each type of control information to a respective portion of the resources for the control channel based on the structure.

10. The apparatus of claim 1, wherein the at least one type of control information being sent comprises only channel quality indicator (CQI) information, and wherein the at least one processor is configured to map the CQI information to all of the resources for the control channel.

11. The apparatus of claim 1, wherein the at least one type of control information being sent comprises only acknowledgement (ACK) information, and wherein the at least one processor is configured to map the ACK information to all of the resources for the control channel.

12. The apparatus of claim 1, wherein the at least one type of control information being sent comprises channel quality indicator (CQI) and acknowledgement (ACK) information, and wherein the at least one processor is configured to map the CQI and ACK information to the resources for the control channel based on the structure.

13. The apparatus of claim 1, wherein the at least one processor is configured to determine the structure of the control channel further based on whether or not data is being sent.

14. The apparatus of claim 13, wherein the control channel comprises a fixed amount of resources when data is not sent and comprises a variable amount of resources when data is sent.

15. The apparatus of claim 1, wherein the at least one processor is configured to determine the resources for the control channel from a control segment if data is not being sent, and to determine the resources for the control channel from a data segment if data is being sent, and wherein the control segment and the data segment occupy different frequency locations.

16. The apparatus of claim 1, wherein the at least one processor is configured to process the at least one type of control information in accordance with a first processing scheme if data is not being sent, and to process the at least one type of control information in accordance with a second processing scheme if data is being sent.

17. The apparatus of claim 16, wherein for the first processing scheme the at least one processor is configured to process the at least one type of control information to obtain modulation symbols, to modulate a CAZAC (constant amplitude zero auto-correlation) sequence with each of the modulation symbols to obtain a corresponding modulated CAZAC sequence, and to map modulated CAZAC sequences for the modulation symbols to the resources for the control channel.

18. The apparatus of claim 16, wherein for the second processing scheme the at least one processor is configured to process the at least one type of control information to obtain modulation symbols, to combine the modulation symbols for the at least one type of control information with modulation symbols for data, and to map the combined modulation symbols to resources for a data segment, and wherein the resources for the control channel are a subset of the resources for the data segment.

19. The apparatus of claim 18, wherein for the second processing scheme the at least one processor is configured to transform the combined modulation symbols

from time domain to frequency domain to obtain frequency-domain symbols, and to map the frequency-domain symbols to the resources for the data segment.

20. A method comprising:

determining at least one type of control information being sent;

determining a structure of a control channel based on operating configuration; and

mapping the at least one type of control information to resources for the control channel based on the structure.

21. The method of claim 20, wherein the operating configuration is determined based on at least one of system configuration and user equipment (UE) configuration, wherein the system configuration is indicative of allocations for downlink and uplink, and wherein the determining the structure of the control channel comprises determining the structure of the control channel based on asymmetry of the downlink and uplink allocations.

22. The method of claim 20, wherein the at least one type of control information being sent comprises acknowledgement (ACK) information, and wherein the determining the structure of the control channel comprises determining the structure of the control channel further based on number of hybrid automatic retransmission (HARQ) processes to be acknowledged by the ACK information.

23. The method of claim 20, wherein the determining the structure of the control channel comprises determining the structure of the control channel further based on whether or not data is being sent, and wherein the control channel comprises a fixed amount of resources when data is not sent and comprises a variable amount of resources when data is sent.

24. The method of claim 20, further comprising:

determining whether or not data is being sent;

determining the resources for the control channel from a control segment if data is not being sent; and

determining the resources for the control channel from a data segment if data is being sent, wherein the control segment and the data segment occupy different frequency locations.

25. The method of claim 20, wherein the at least one type of control information being sent comprises only channel quality indicator (CQI) information, or only acknowledgement (ACK) information, or both CQI and ACK information, and wherein the mapping the at least one type of control information comprises mapping the CQI information, or the ACK information, or both the CQI and ACK information to the resources for the control channel based on the structure.

26. An apparatus comprising:

means for determining at least one type of control information being sent;

means for determining a structure of a control channel based on operating configuration; and

means for mapping the at least one type of control information to resources for the control channel based on the structure.

27. The apparatus of claim 26, wherein the operating configuration is determined based on at least one of system configuration and user equipment (UE) configuration, wherein the system configuration is indicative of allocations for downlink and uplink, and wherein the means for determining the structure of the control channel comprises means for determining the structure of the control channel based on asymmetry of the downlink and uplink allocations.

28. The apparatus of claim 26, wherein the at least one type of control information being sent comprises acknowledgement (ACK) information, and wherein the means for determining the structure of the control channel comprises means for determining the structure of the control channel further based on number of hybrid

automatic retransmission (HARQ) processes to be acknowledged by the ACK information.

29. The apparatus of claim 26, wherein the means for determining the structure of the control channel comprises means for determining the structure of the control channel further based on whether or not data is being sent, and wherein the control channel comprises a fixed amount of resources when data is not sent and comprises a variable amount of resources when data is sent.

30. The apparatus of claim 26, further comprising:

means for determining whether or not data is being sent;

means for determining the resources for the control channel from a control segment if data is not being sent; and

means for determining the resources for the control channel from a data segment if data is being sent, wherein the control segment and the data segment occupy different frequency locations.

31. The apparatus of claim 26, wherein the at least one type of control information being sent comprises only channel quality indicator (CQI) information, or only acknowledgement (ACK) information, or both CQI and ACK information, and wherein the means for mapping the at least one type of control information comprises means for mapping the CQI information, or the ACK information, or both the CQI and ACK information to the resources for the control channel based on the structure.

32. A machine-readable medium comprising instructions which, when executed by a machine, cause the machine to perform operations including:

determining at least one type of control information being sent;

determining a structure of a control channel based on operating configuration; and

mapping the at least one type of control information to resources for the control channel based on the structure.

33. The machine-readable medium of claim 32, wherein the operating configuration is determined based on at least one of system configuration and user equipment (UE) configuration, and wherein the machine-readable medium further comprises instructions which, when executed by a machine, cause the machine to perform operations including:

determining the structure of the control channel based on asymmetry of allocations for downlink and uplink as indicated by the system configuration.

34. An apparatus comprising:

at least one processor configured to determine at least one type of control information being received, to determine a structure of a control channel based on operating configuration, and to receive the at least one type of control information from resources for the control channel based on the structure; and

a memory coupled to the at least one processor.

35. The apparatus of claim 34, wherein the operating configuration is determined based on at least one of system configuration and user equipment (UE) configuration, wherein the system configuration is indicative of allocations for downlink and uplink, and wherein the at least one processor is configured to determine the structure of the control channel based on asymmetry of the downlink and uplink allocations.

36. The apparatus of claim 34, wherein the at least one type of control information being received comprises acknowledgement (ACK) information, and wherein the at least one processor is configured to determine the structure of the control channel further based on number of hybrid automatic retransmission (HARQ) processes to be acknowledged by the ACK information.

37. The apparatus of claim 34, wherein the at least one processor is configured to determine the structure of the control channel further based on whether or not data is being received, and wherein the control channel comprises a fixed amount of

resources when data is not received and comprises a variable amount of resources when data is received.

38. The apparatus of claim 34, wherein the at least one processor is configured determine whether or not data is being received, determine the resources for the control channel from a control segment if data is not being received, and determine the resources for the control channel from a data segment if data is being received, and wherein the control segment and the data segment occupy different frequency locations.

39. The apparatus of claim 34, wherein the at least one type of control information being received comprises only channel quality indicator (CQI) information, or only acknowledgement (ACK) information, or both CQI and ACK information, and wherein the at least one processor is configured to receive the CQI information, or the ACK information, or both the CQI and ACK information from the resources for the control channel based on the structure.

40. A method comprising:

determining at least one type of control information being received;

determining a structure of a control channel based on operating configuration; and

receiving the at least one type of control information from resources for the control channel based on the structure.

41. The method of claim 40, wherein the operating configuration is determined based on at least one of system configuration and user equipment (UE) configuration, wherein the system configuration is indicative of allocations for downlink and uplink, and wherein the determining the structure of the control channel comprises determining the structure of the control channel based on asymmetry of the downlink and uplink allocations.

42. An apparatus comprising:

means for determining at least one type of control information being received;

means for determining a structure of a control channel based on operating configuration; and

means for receiving the at least one type of control information from resources for the control channel based on the structure.

43. The apparatus of claim 42, wherein the operating configuration is determined based on at least one of system configuration and user equipment (UE) configuration, wherein the system configuration is indicative of allocations for downlink and uplink, and wherein the means for determining the structure of the control channel comprises means for determining the structure of the control channel based on asymmetry of the downlink and uplink allocations.