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(54) Title: PHASE DETECTOR ESTIMATOR

(57) Abstract

A phase detector (26) for a timing control loop provided in a signal sampling system to control taking samples by a sampler (20) of input signals provided to a signal sampling system to result in a signal sample sequence output using a slope estimator (30 and 31) and an error determiner (21) with a phase error estimator combiner (48, 49, 51). The combiner output is provided to a summer (40) directly and through a delay element (19) to form phase error estimates.

