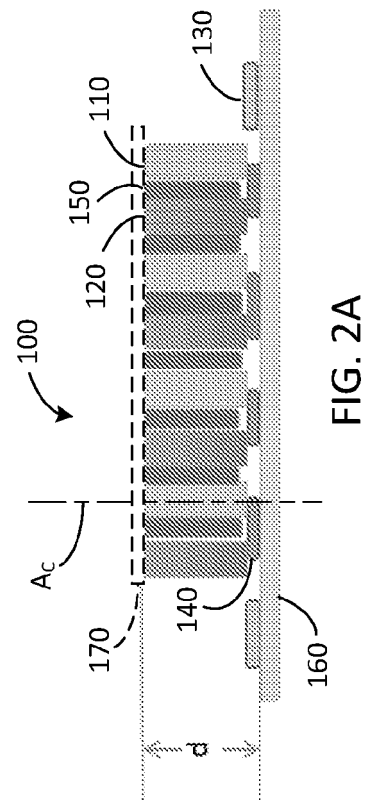
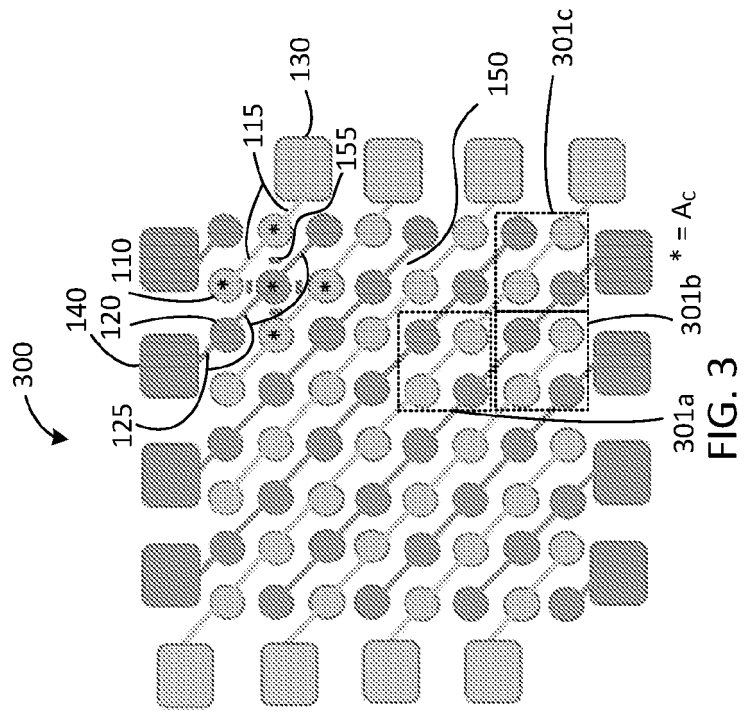
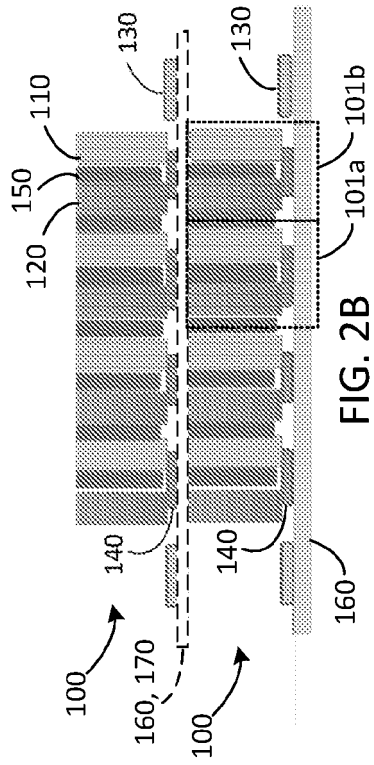
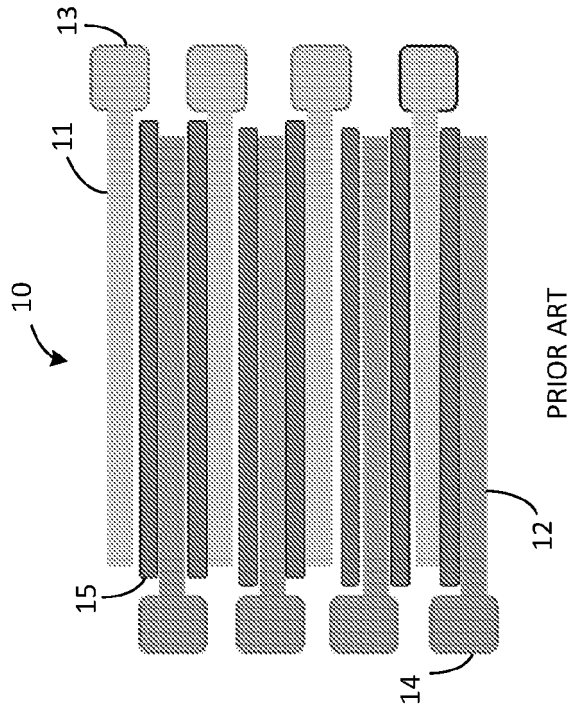
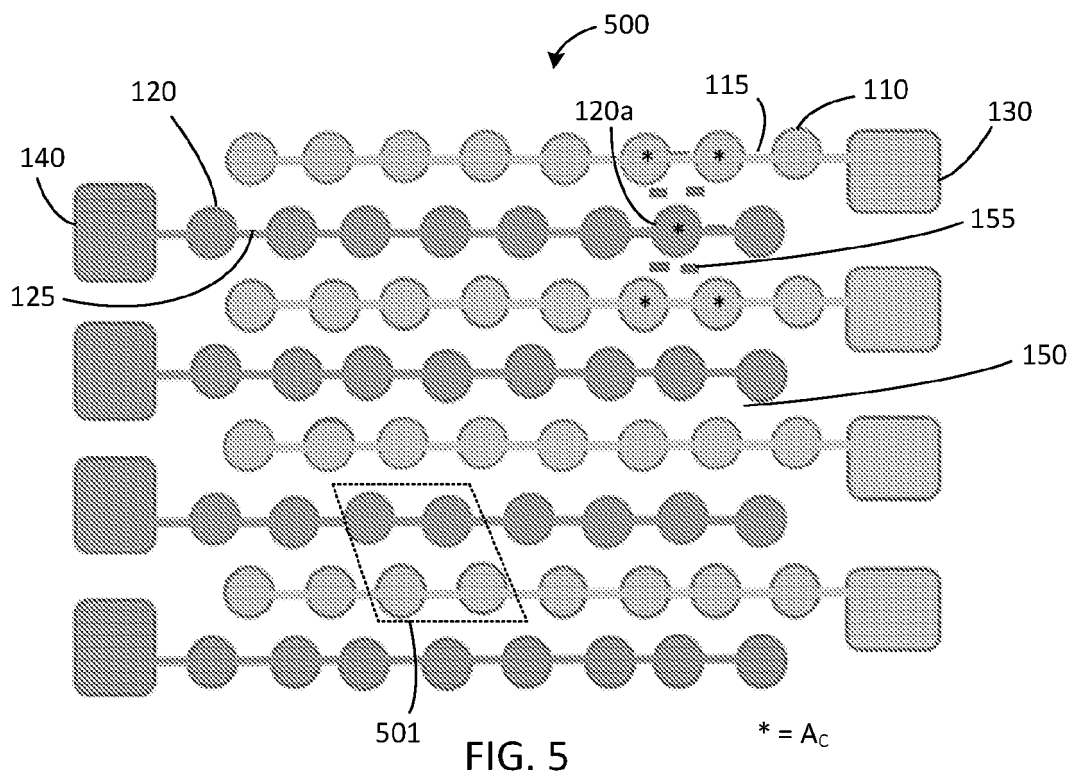
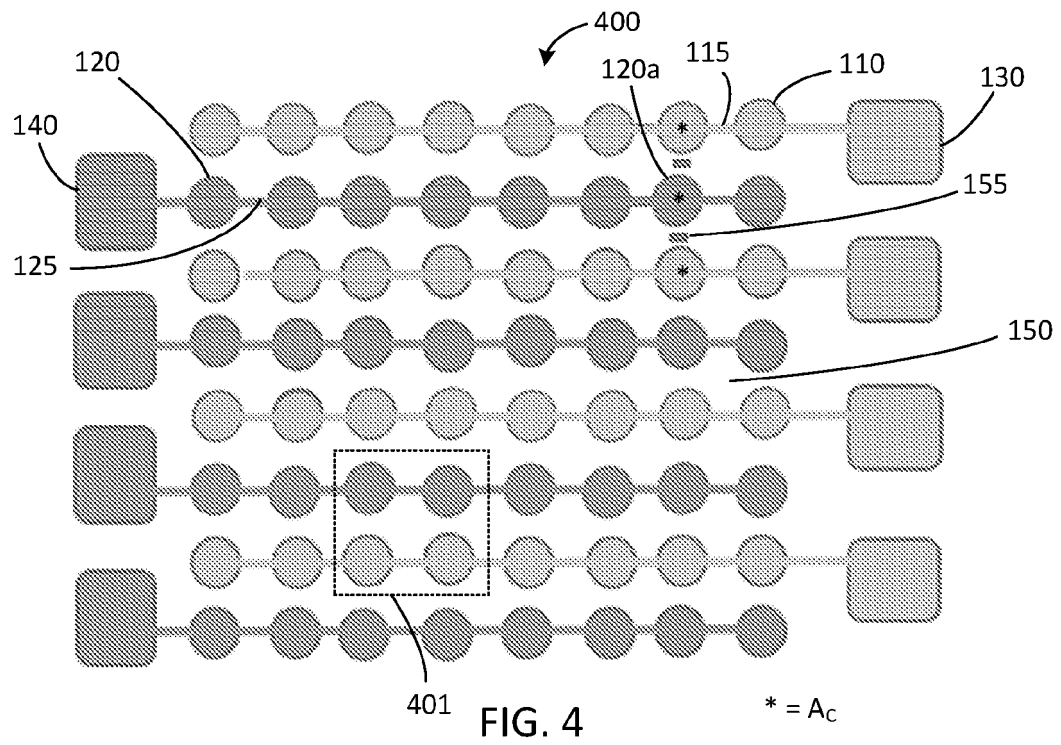
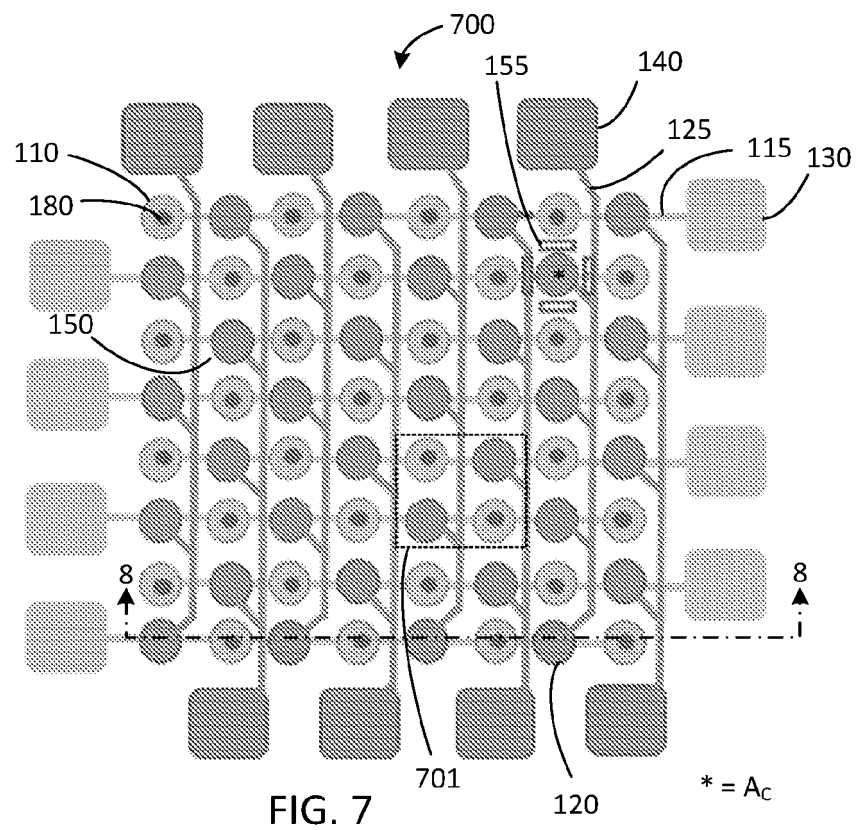
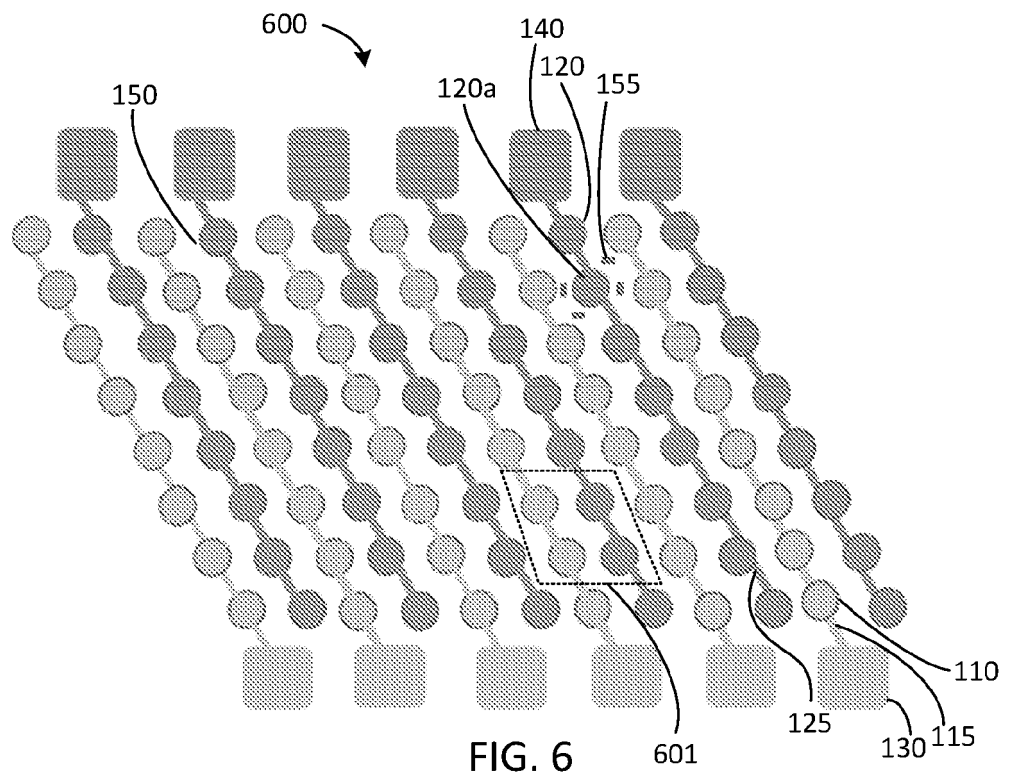
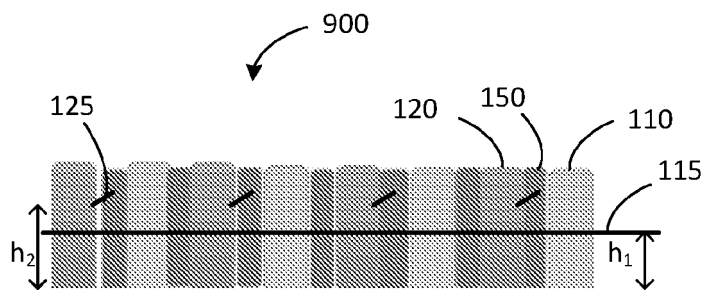
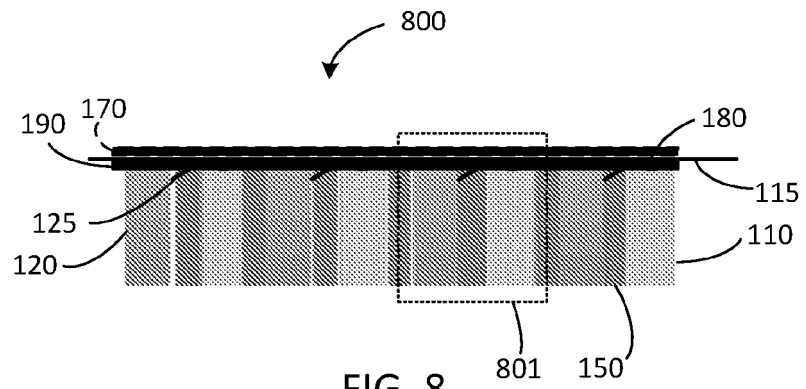










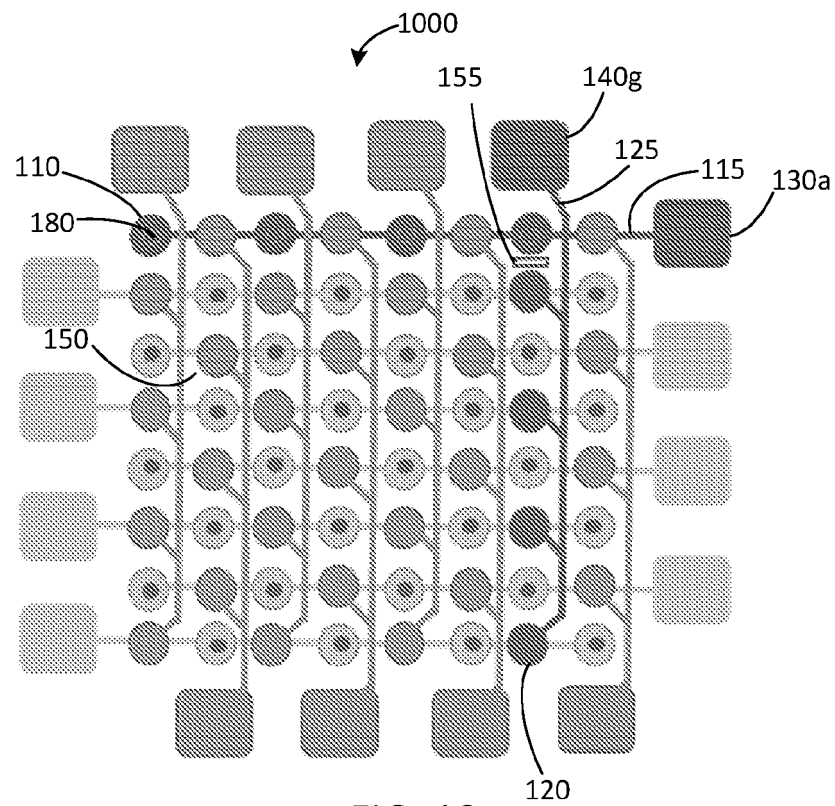


FIG. 10

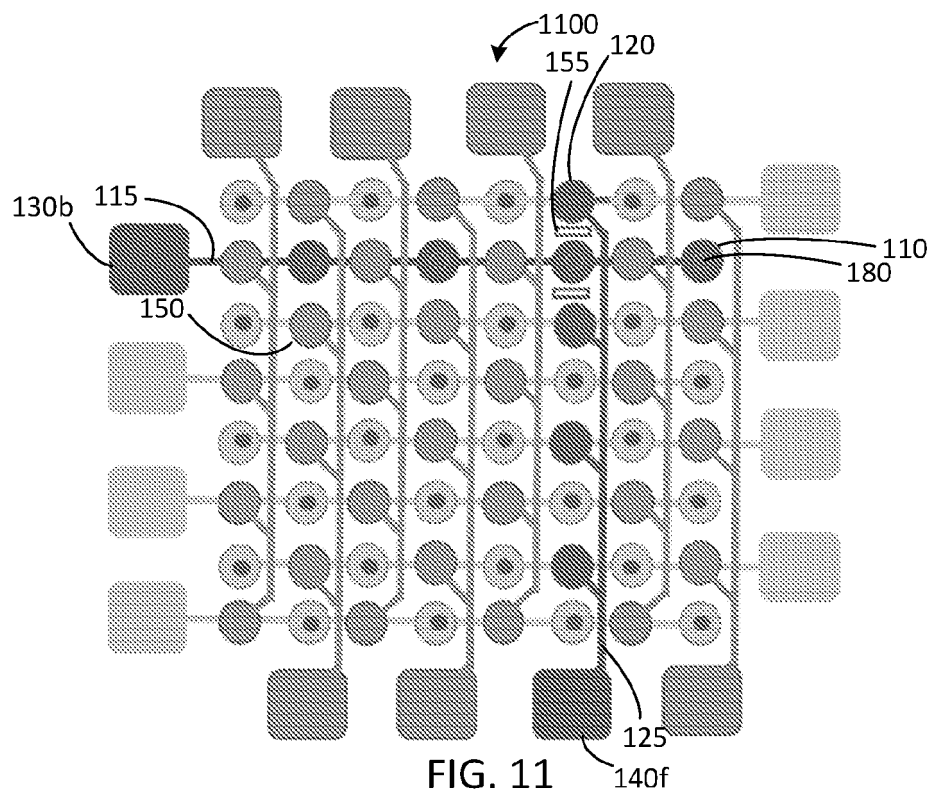


FIG. 11

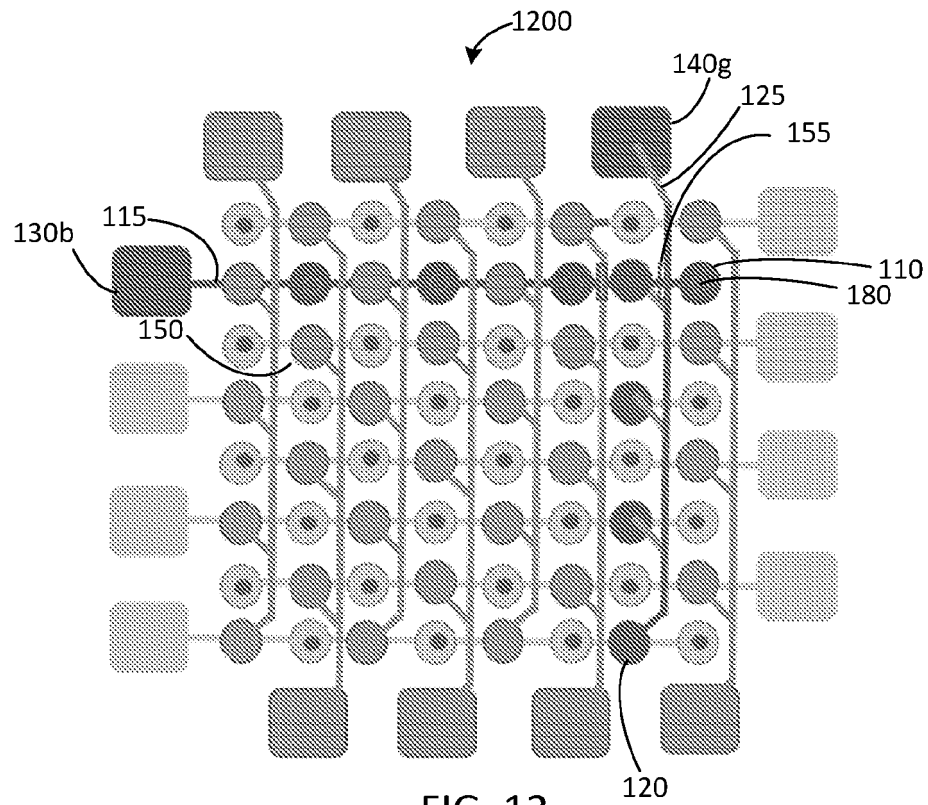


FIG. 12

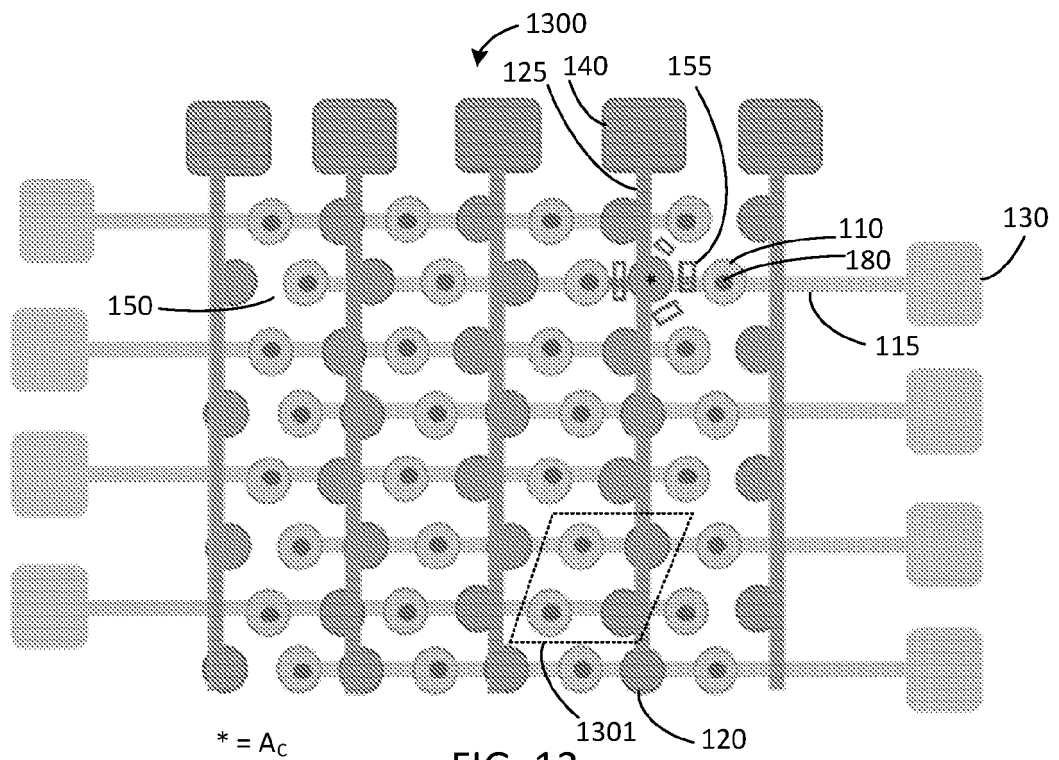
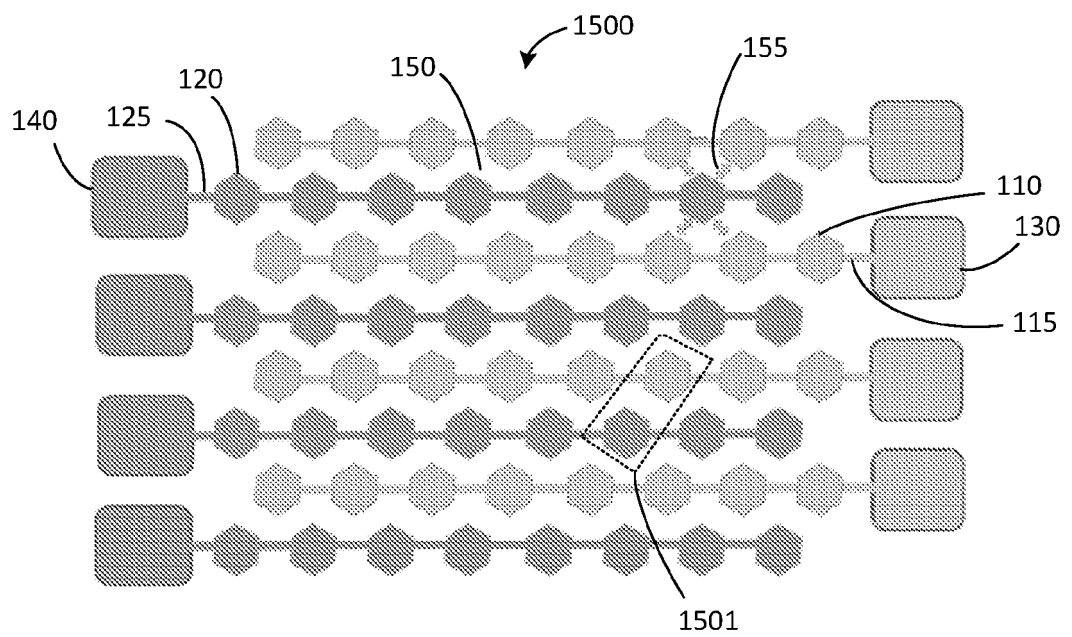
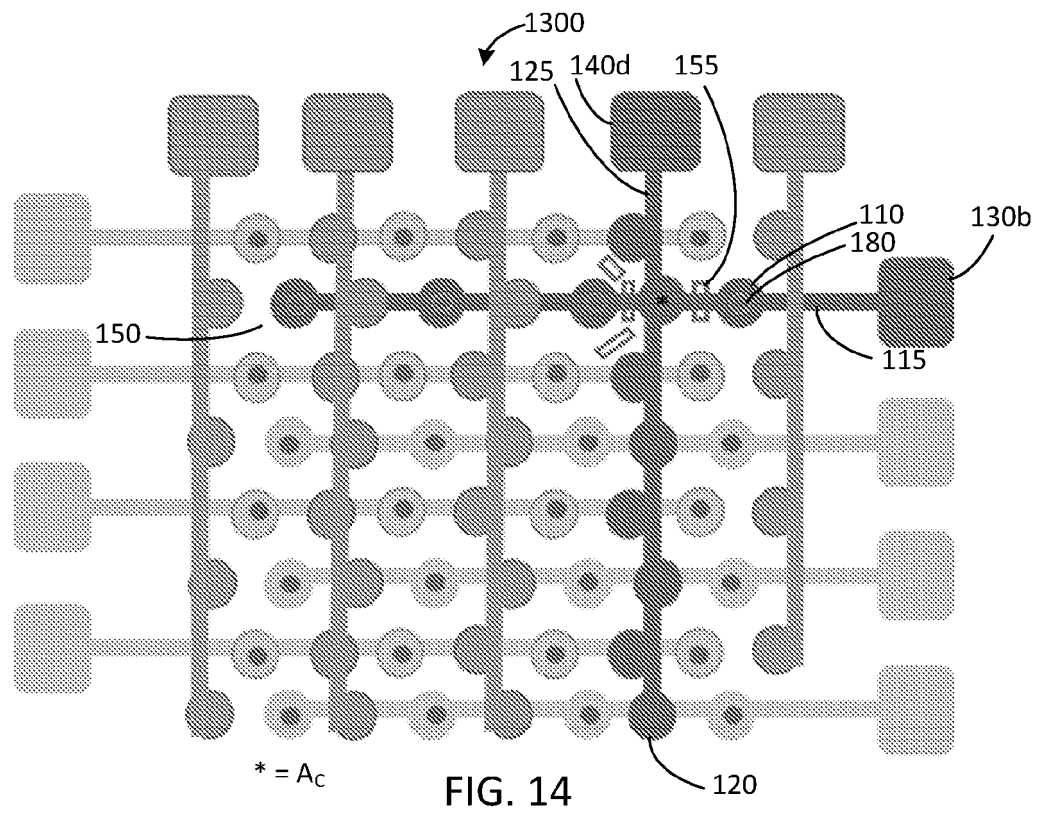
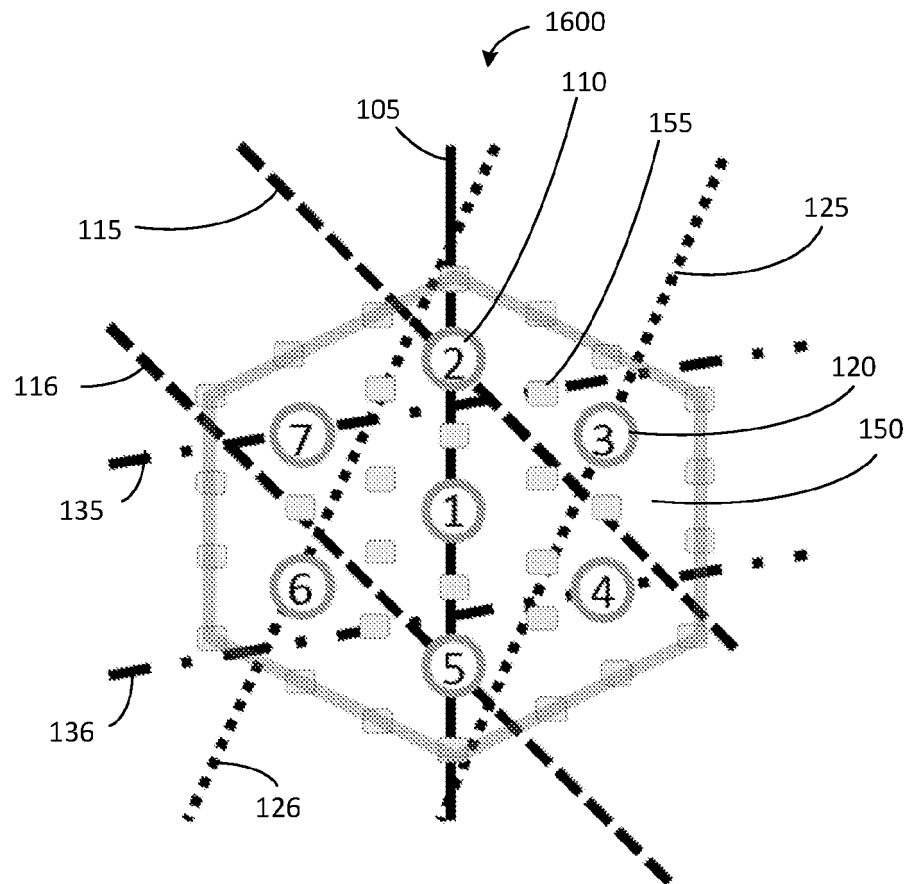


FIG. 13





Legend

-  Capacitor memory element
-  Address lines 1, only used for electrode 1
-  Address lines 4, 7 used for electrodes 4 and 7
-  Address lines 3, 6 used for electrodes 3 and 6
-  Address lines 2, 5 used for electrodes 2, 5
-  Electrode with number
-  Unit cell outline

FIG. 16

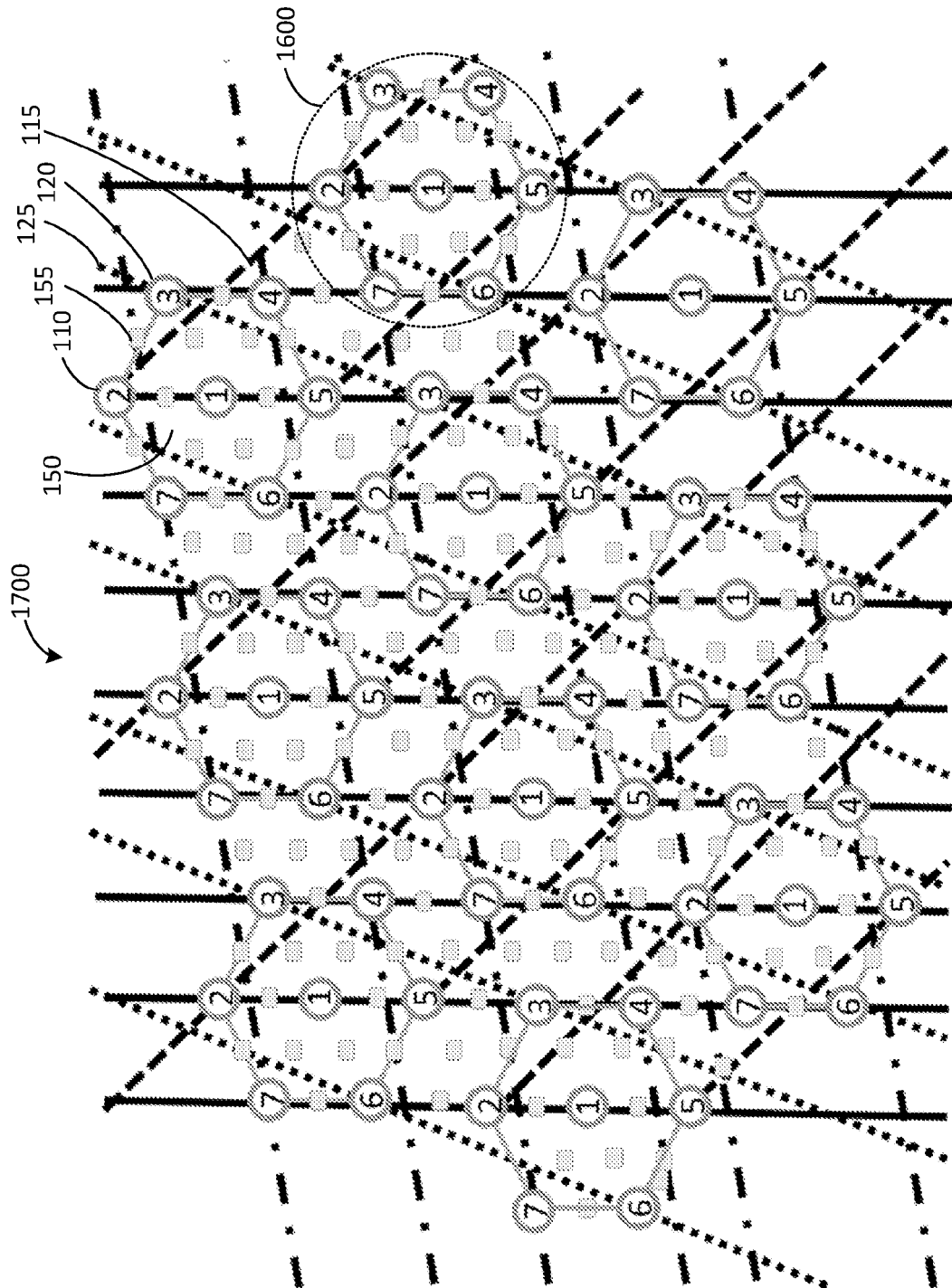


FIG. 17

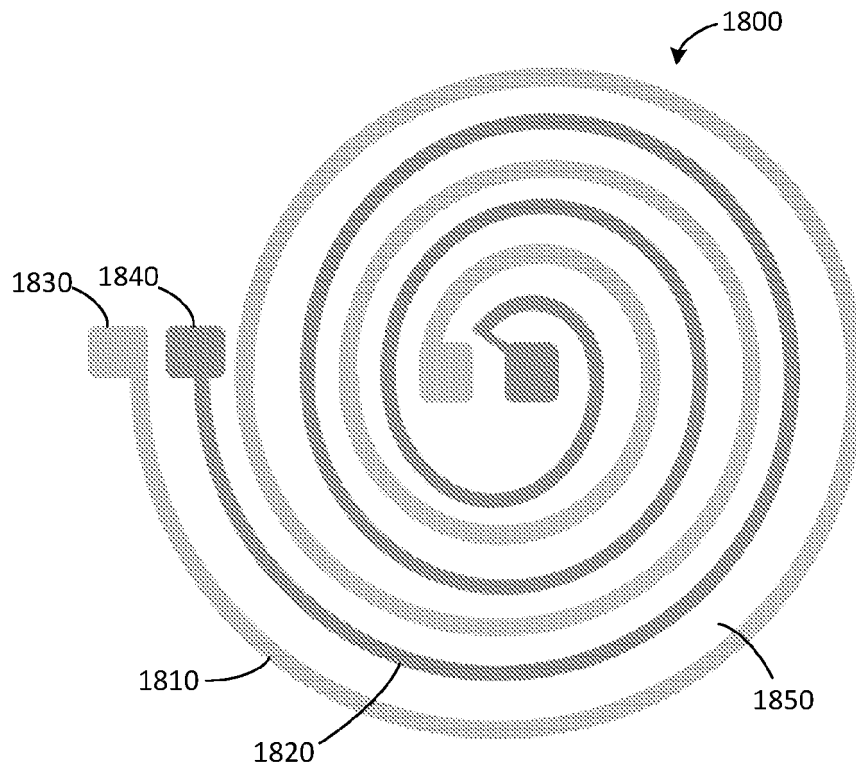


FIG. 18

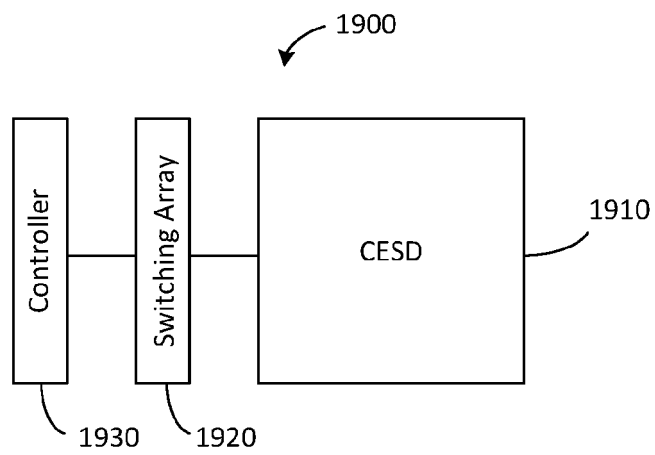


FIG. 19

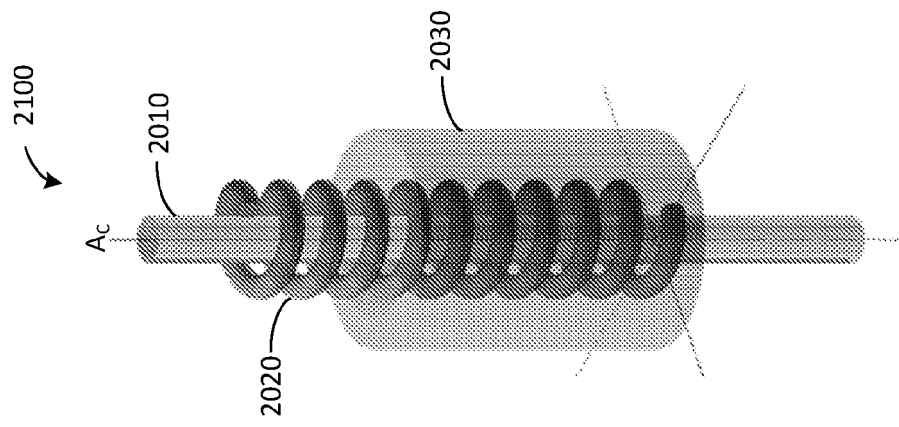


FIG. 21

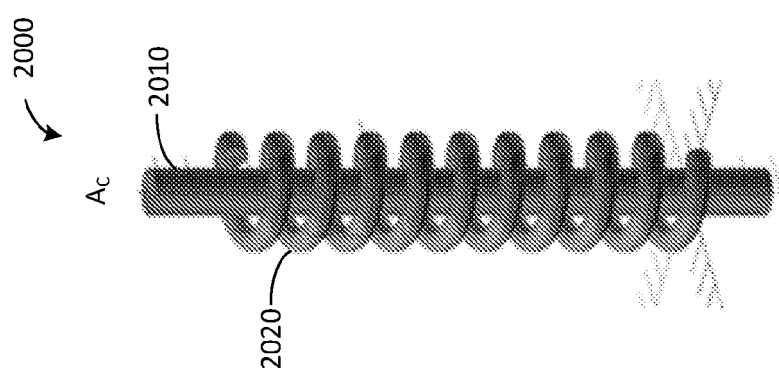


FIG. 20

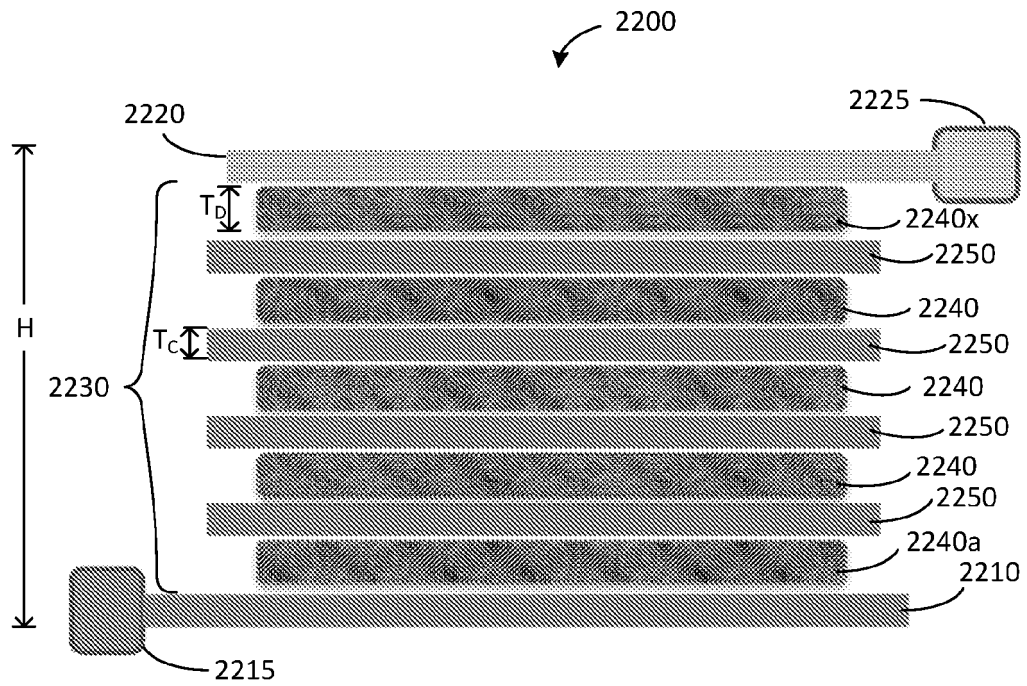


FIG. 22A

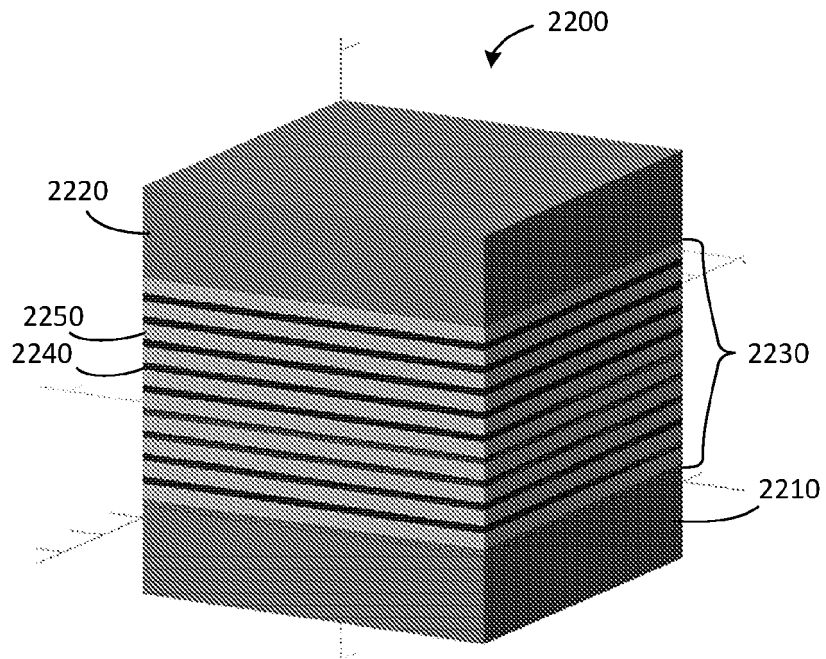
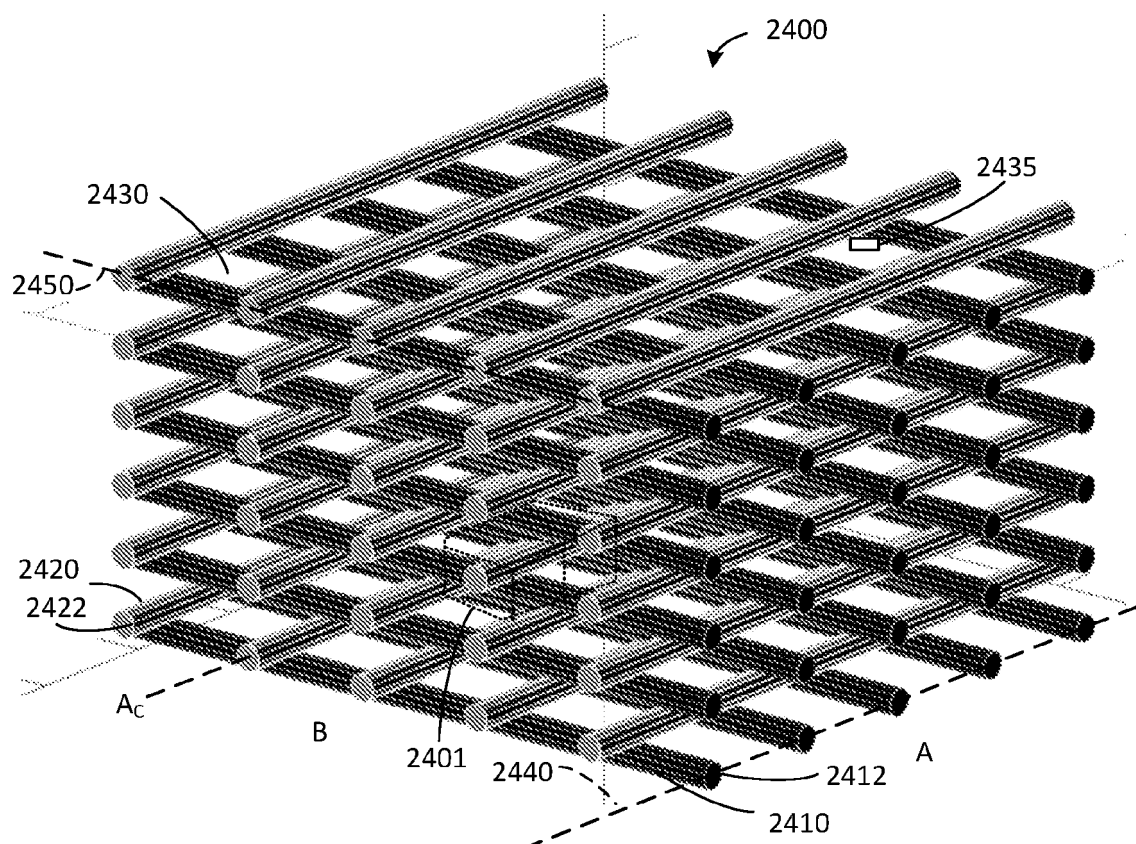
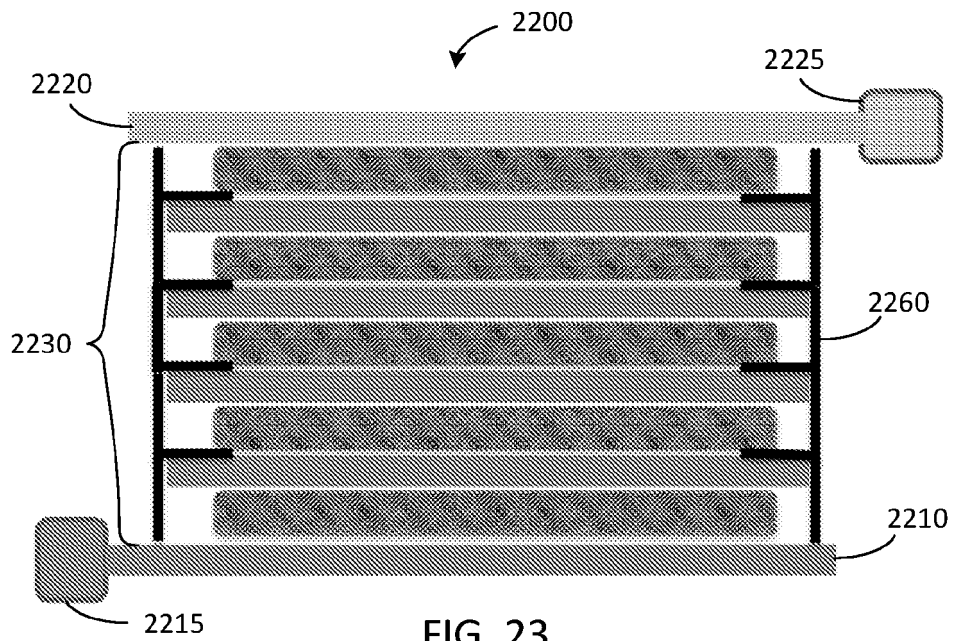


FIG. 22B



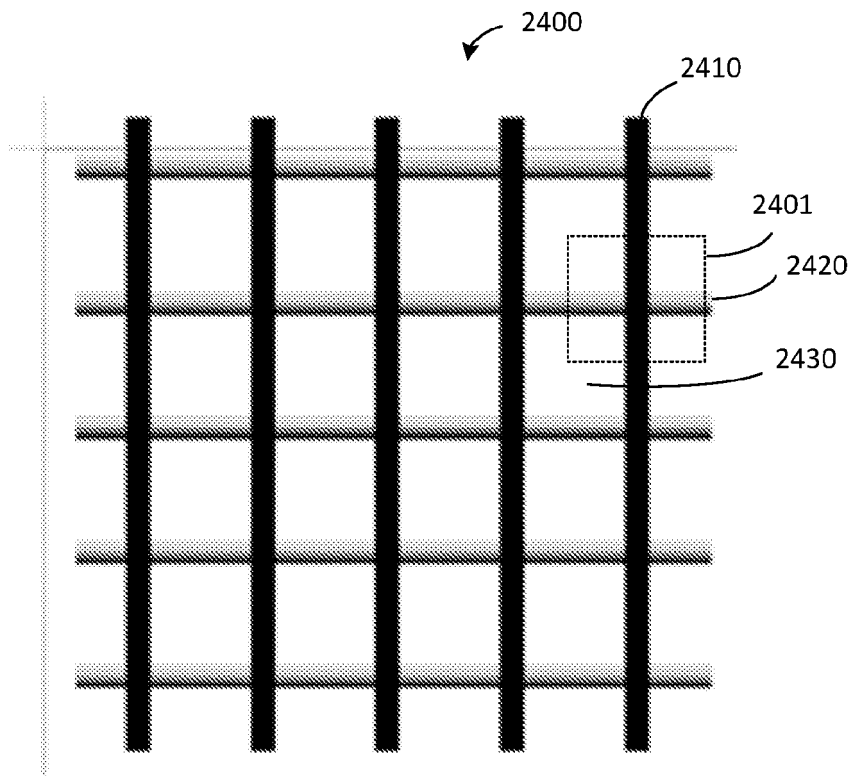


FIG. 24B

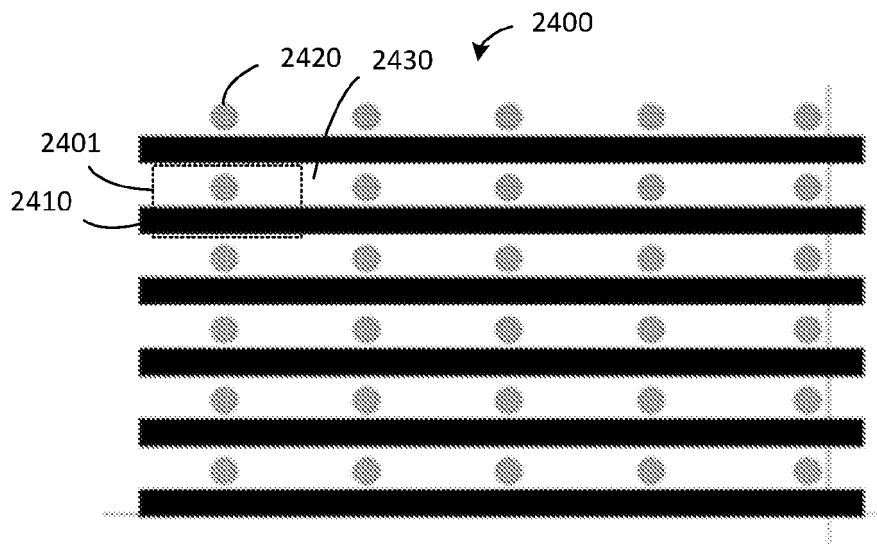
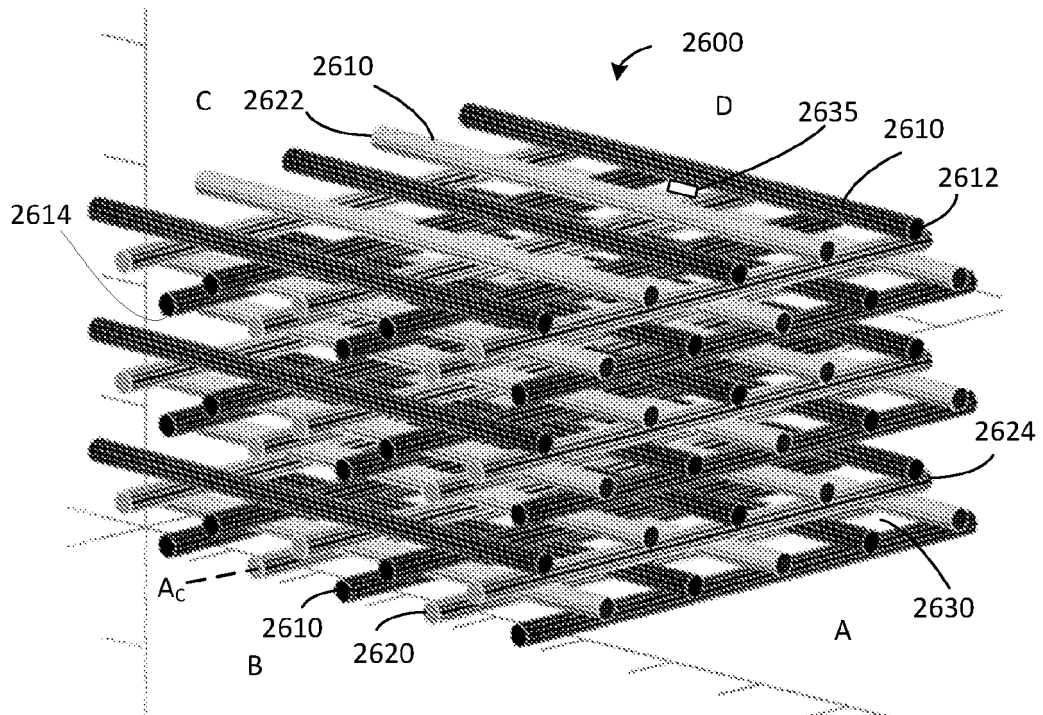
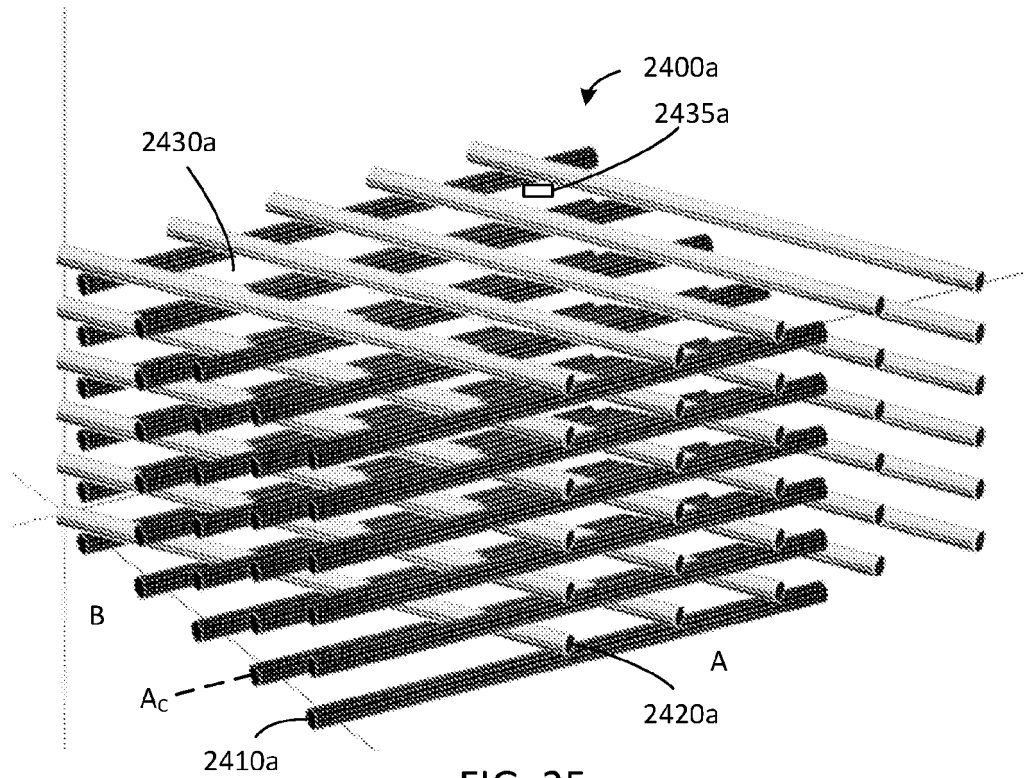


FIG. 24C



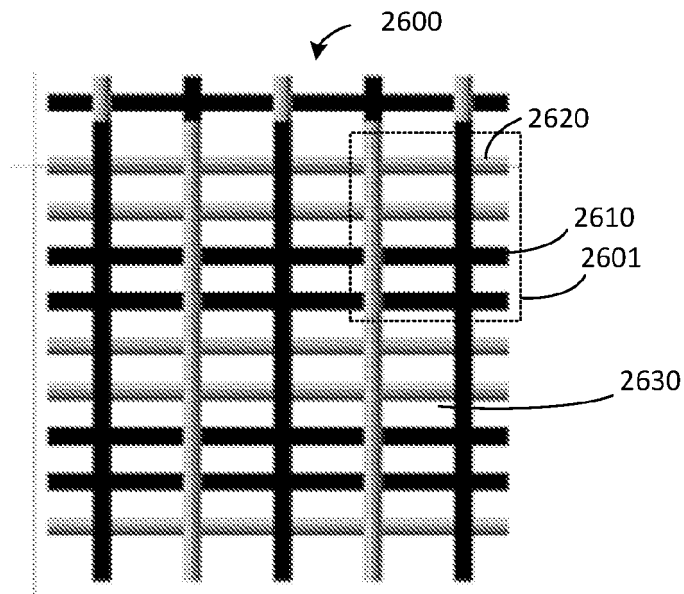


FIG. 26B

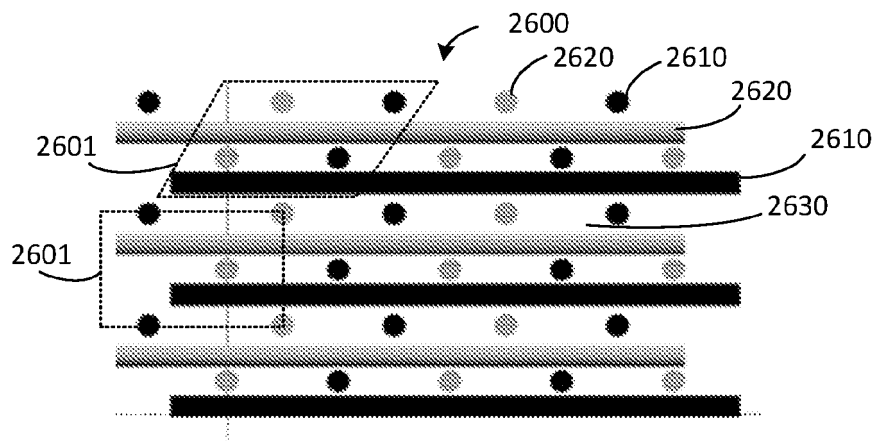


FIG. 26C

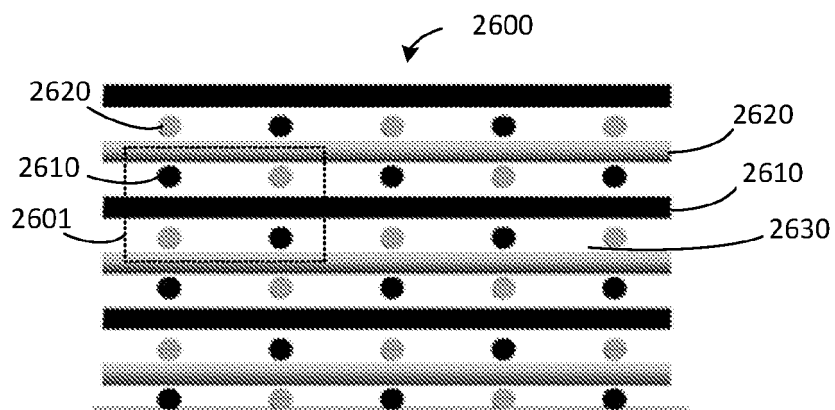
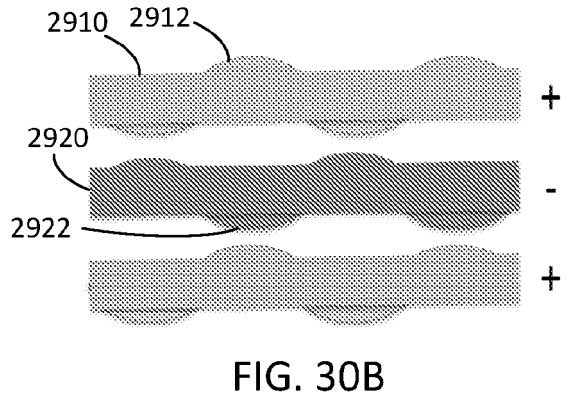
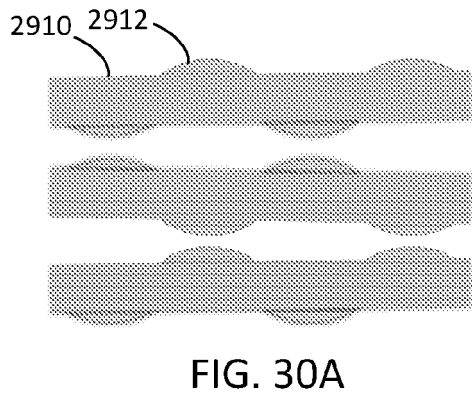
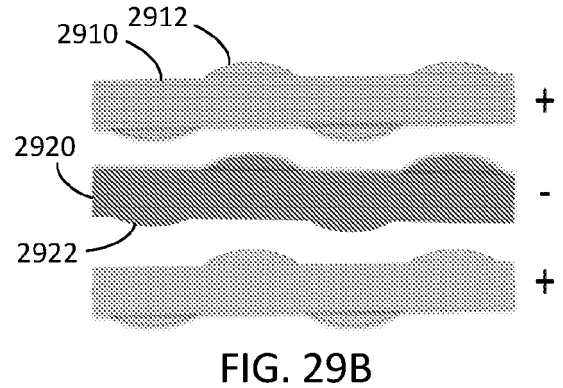
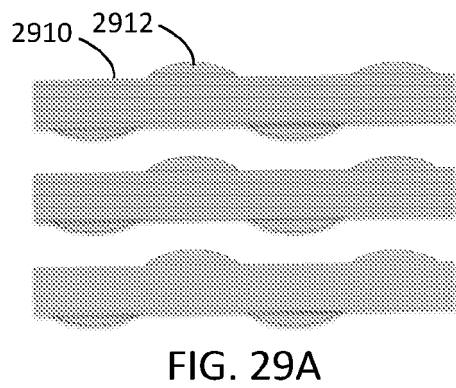
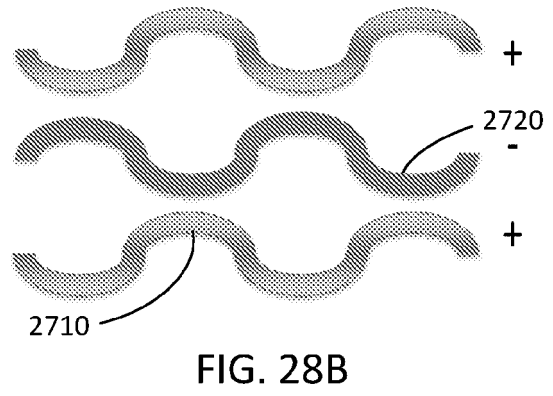
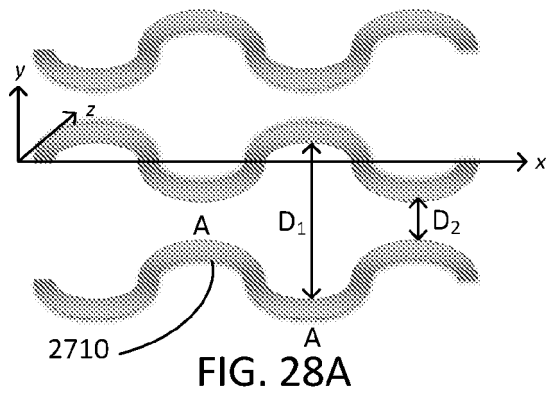
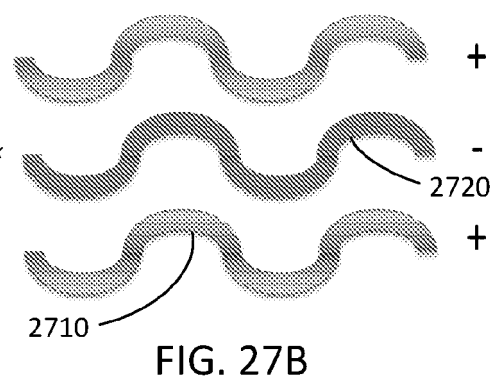
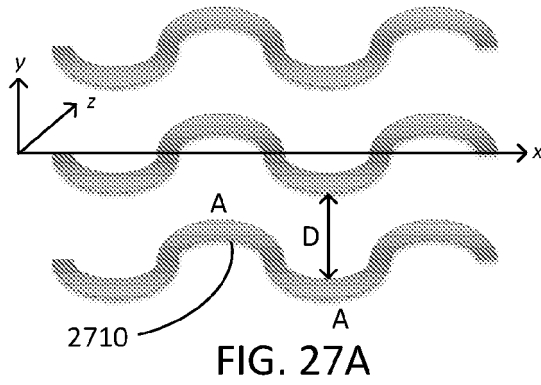
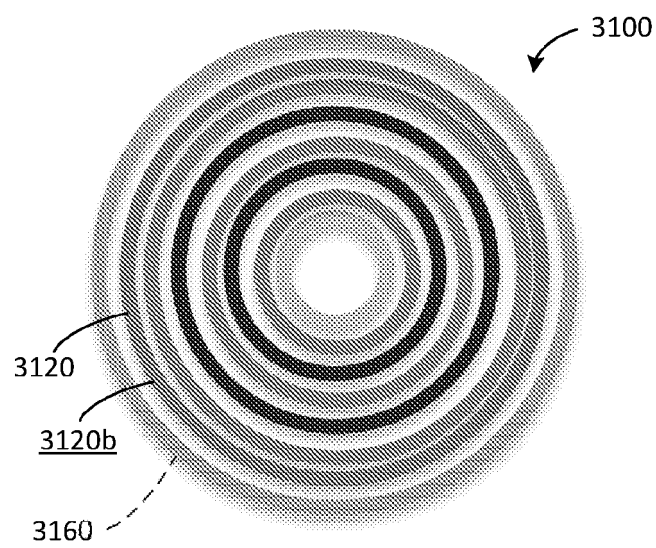
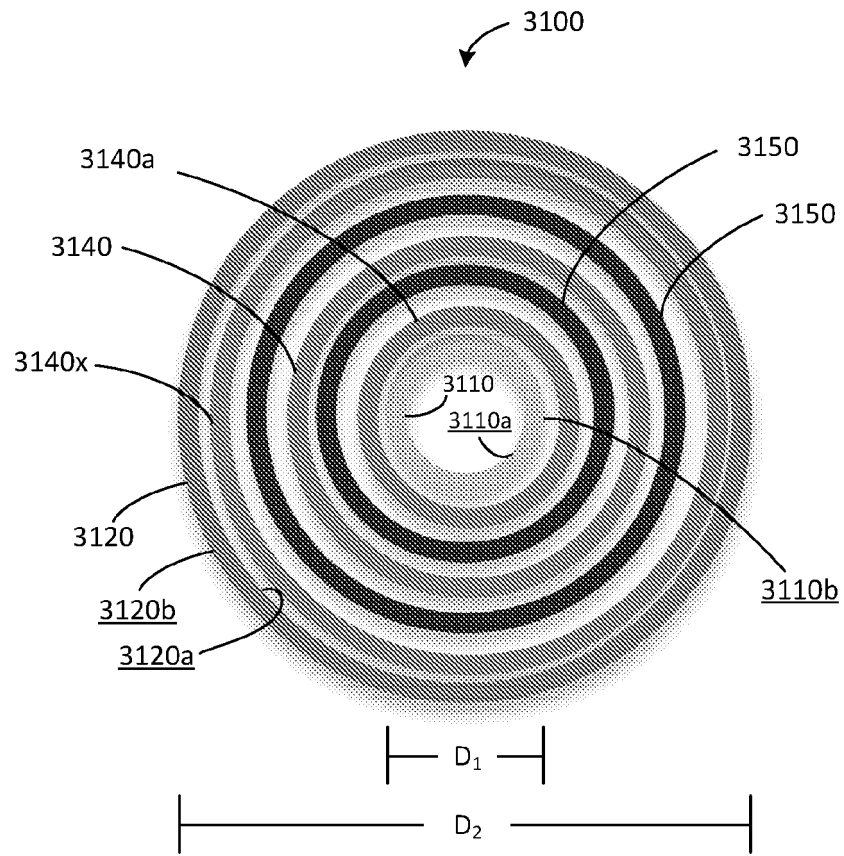


FIG. 26D





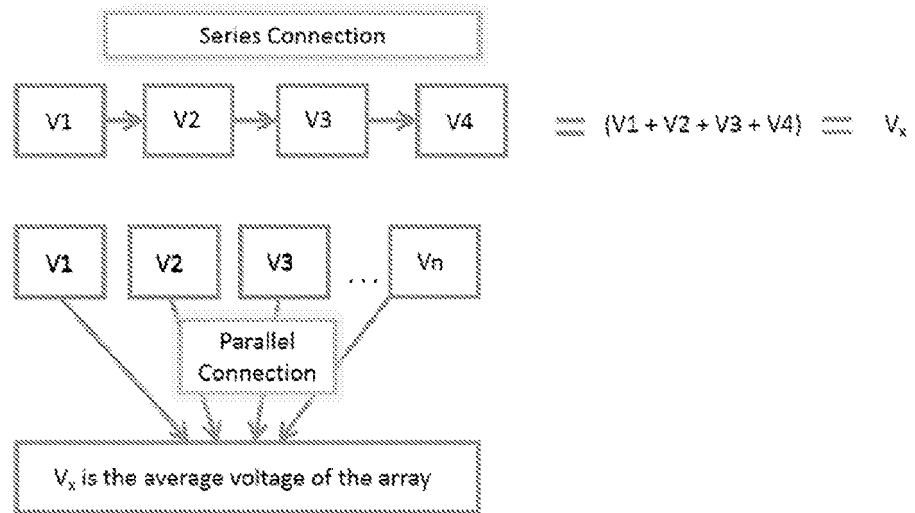


FIG. 33

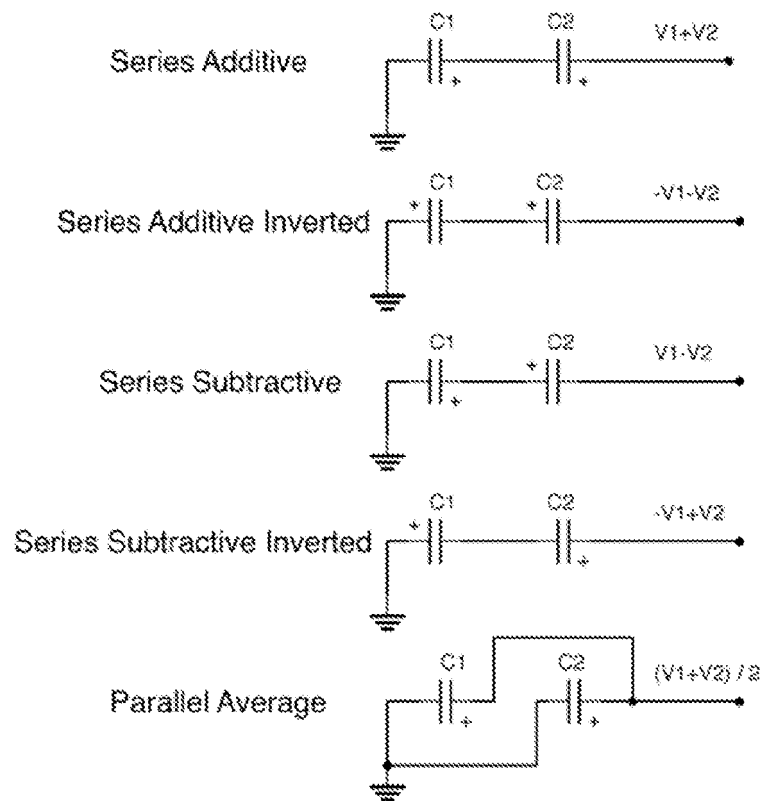


FIG. 34

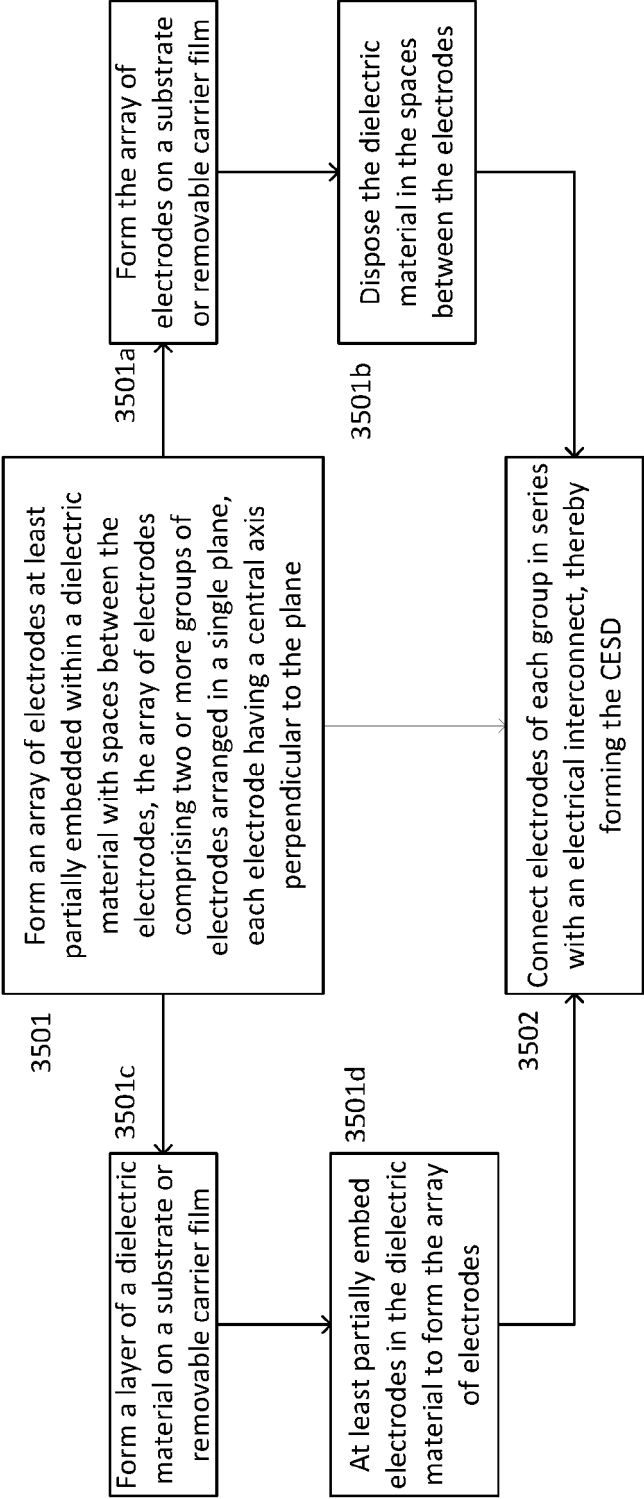


FIG. 35

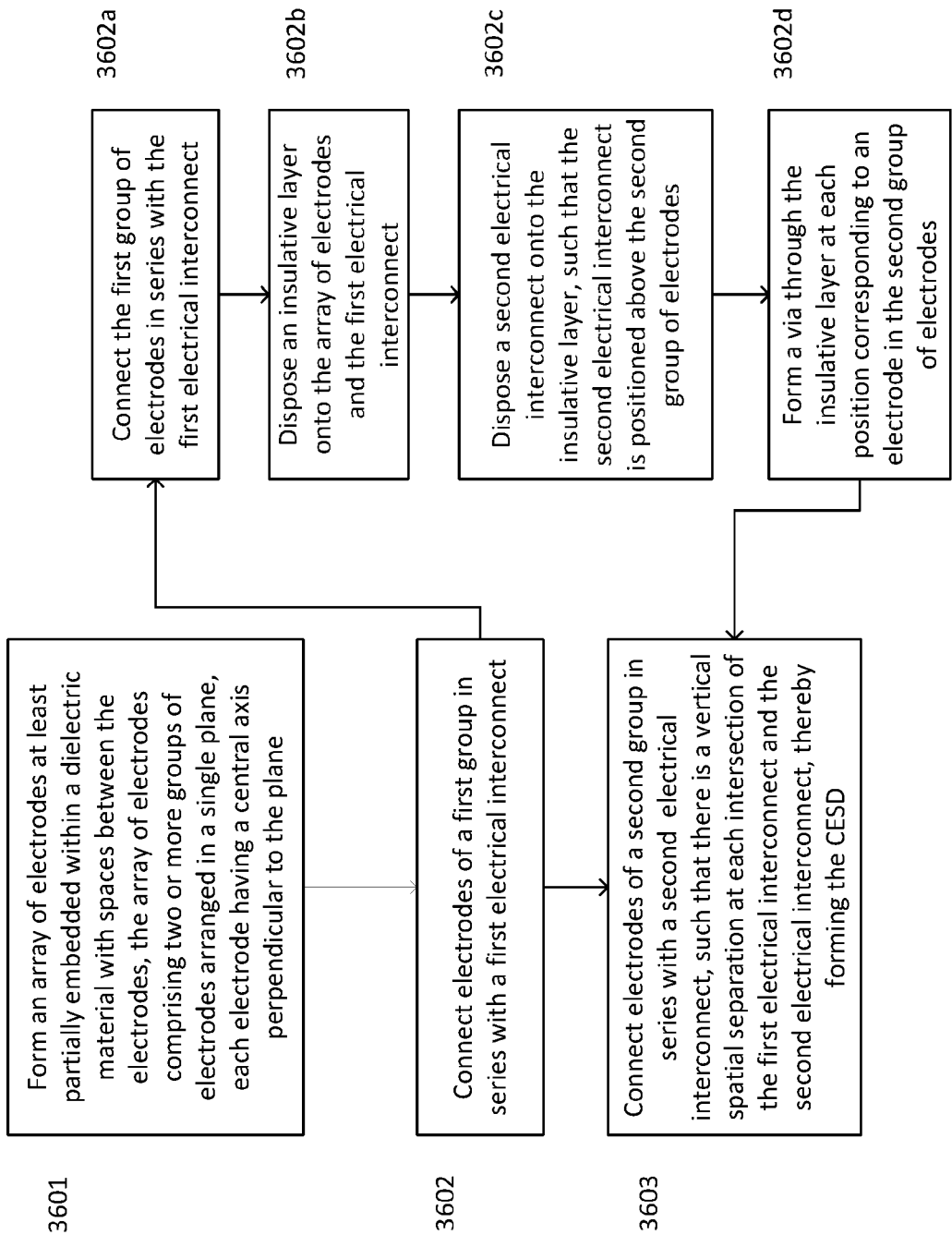


FIG. 36

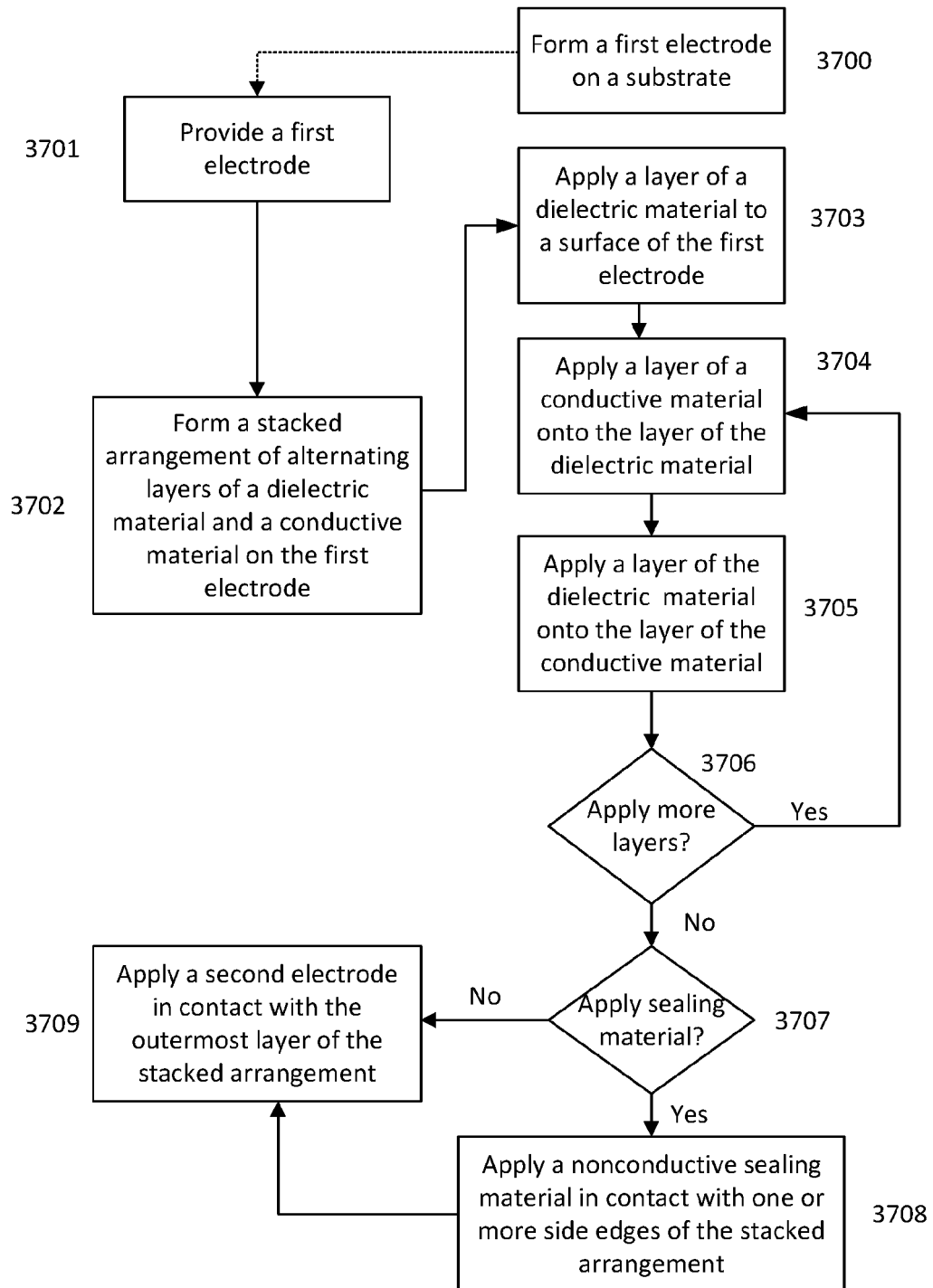


FIG. 37

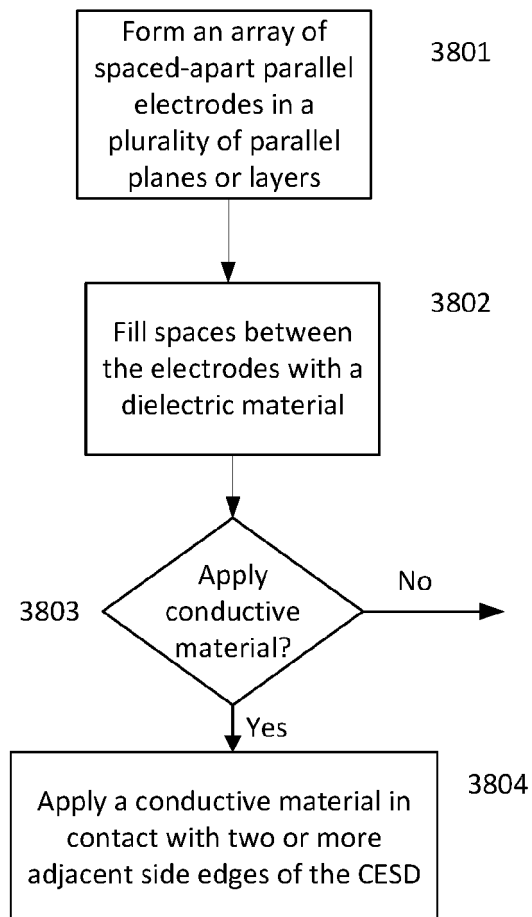


FIG. 38

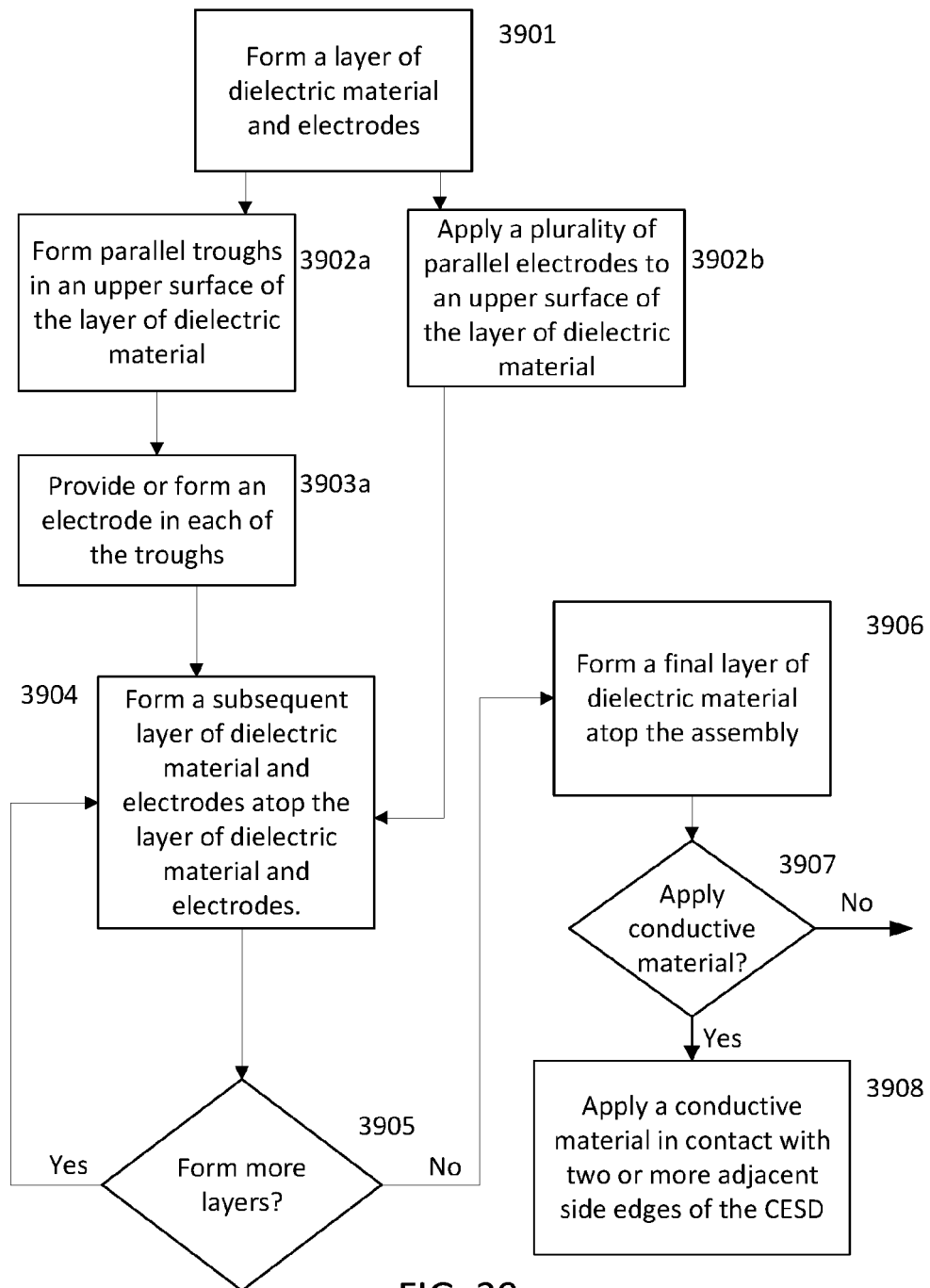
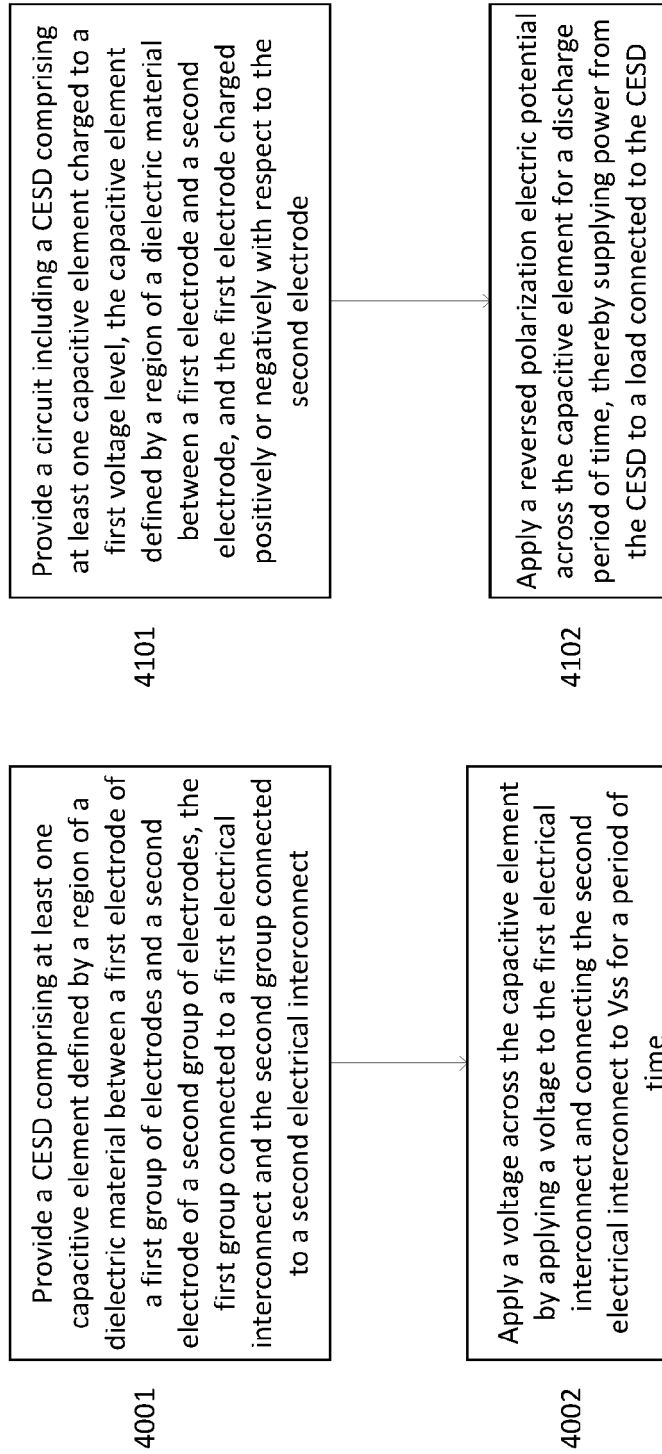
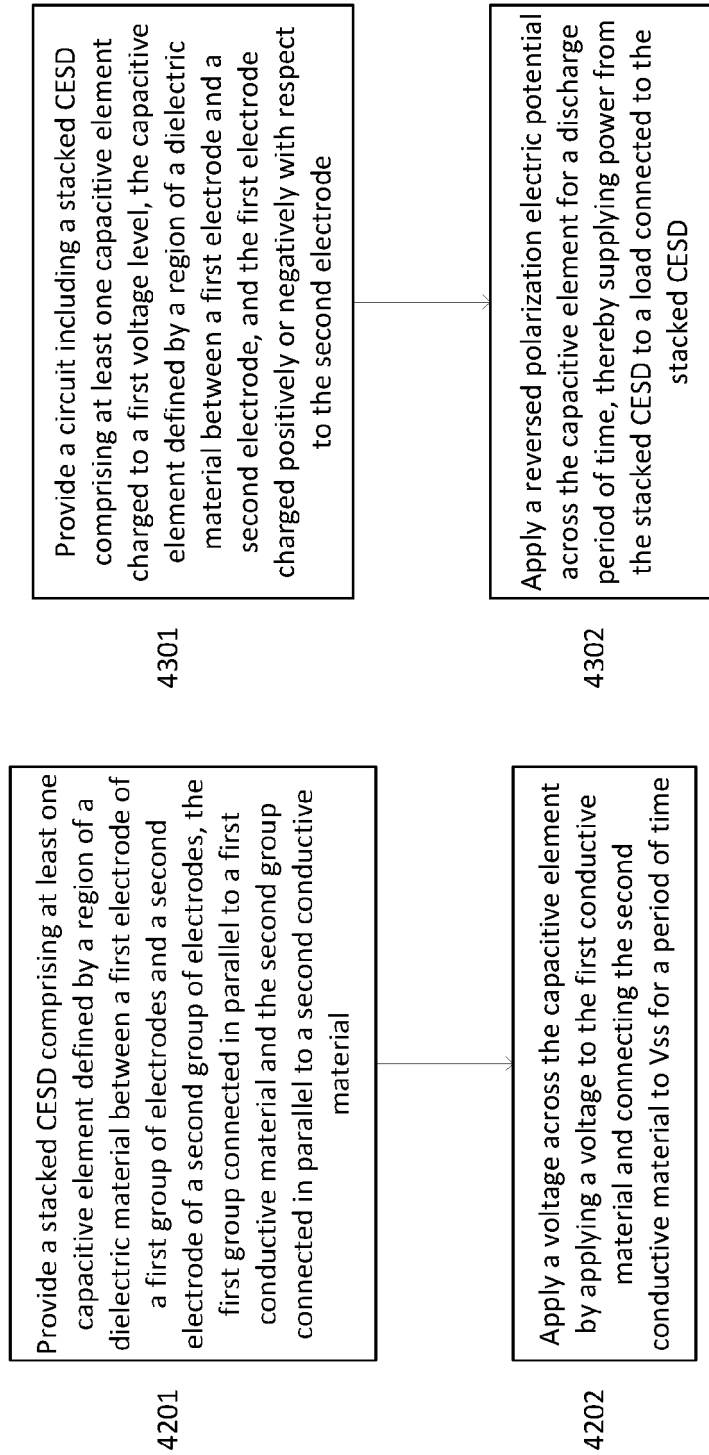


FIG. 39





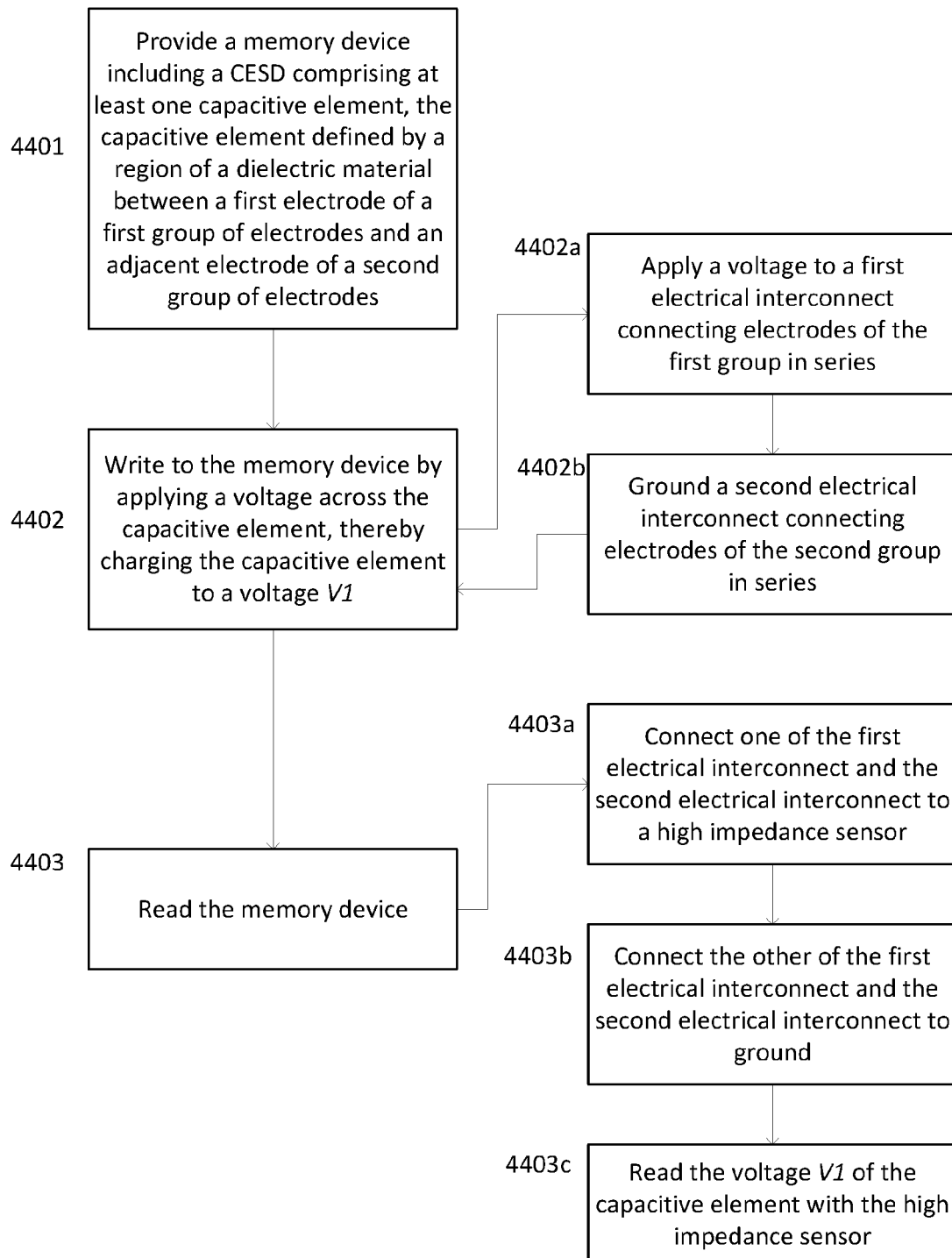


FIG. 44

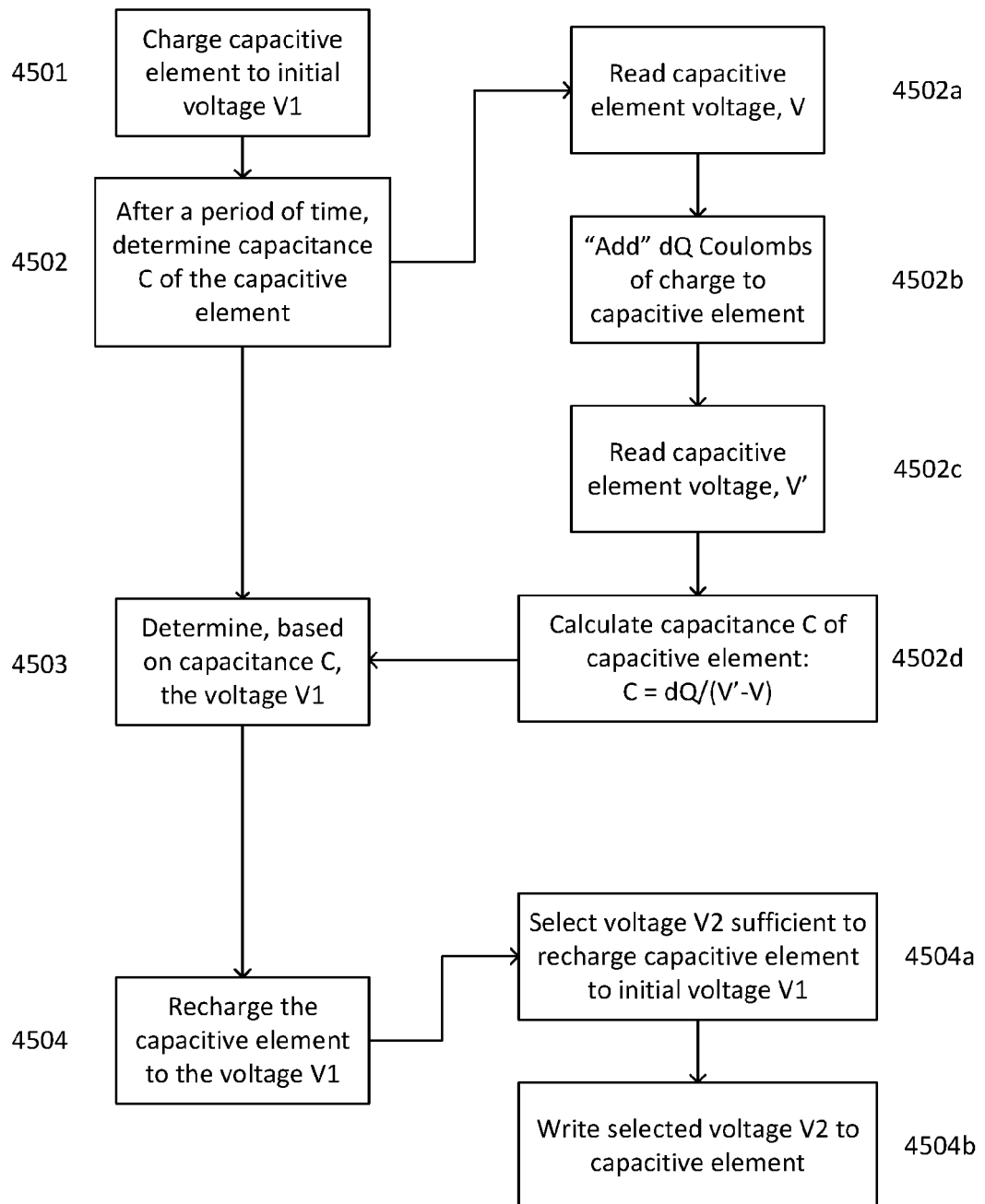


FIG. 45

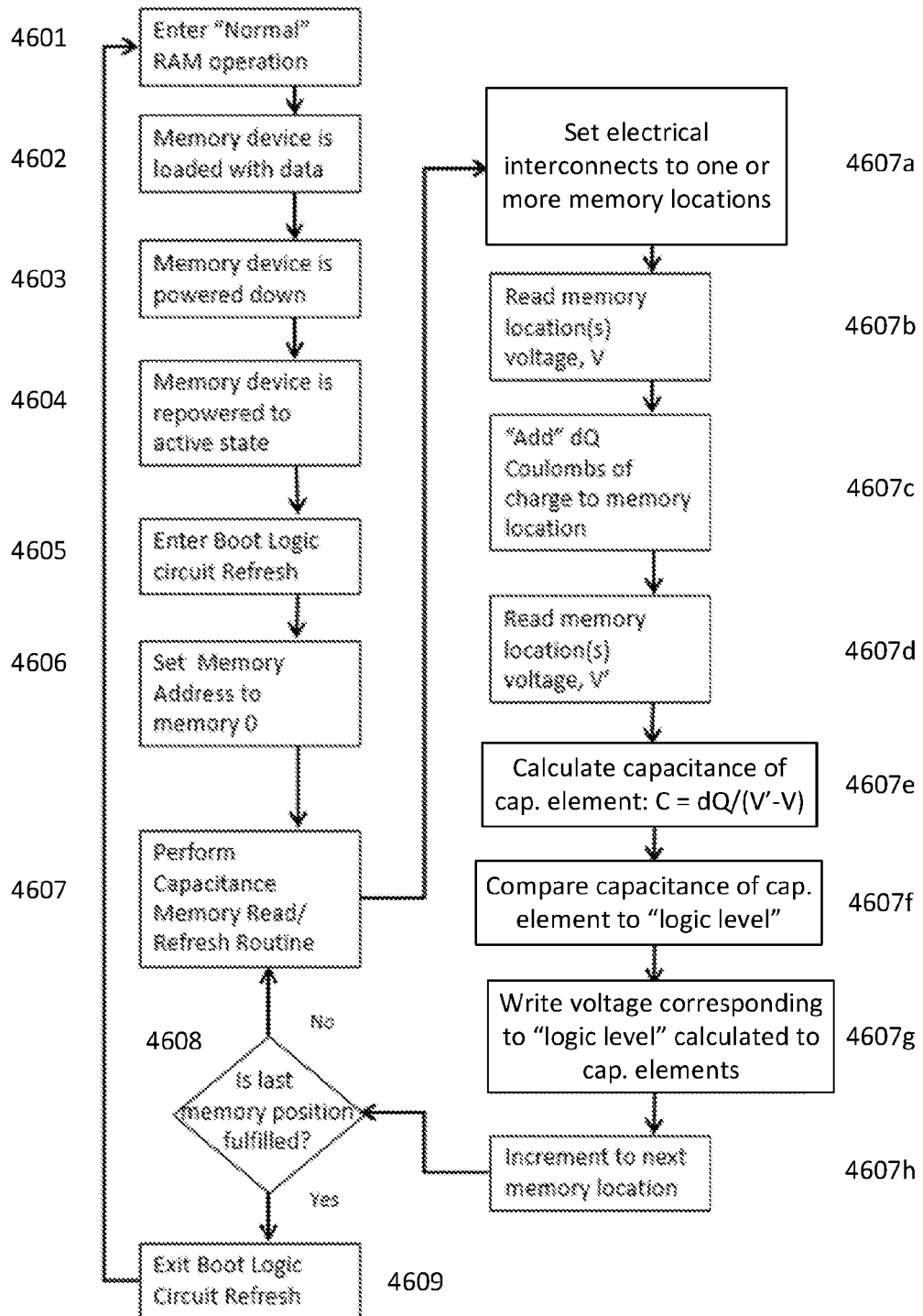


FIG. 46