CLAIMS

1. A method of making a multilayer electronic device, the method comprising:

placing a screen printing mask on a layer of support material;

printing a conductive pattern on the layer of support material using the screen printing mask, the conductive pattern including a plurality of electrode shapes including respective central enlarged portions, the central enlarged portions including respective edge portions that slope toward a lateral centerline of the at least one of the plurality of electrode shapes at an angle with respect to the longitudinal direction that is greater than 0 degrees and less than 90 degrees such that the respective edge portions form a point at the lateral centerline;

cutting the layer of support material and conductive pattern along a plurality of cutting lines intersecting the central enlarged portions such that at least one of the plurality of electrode shapes is divided into a pair of electrodes along a cutting width, and wherein the cutting width of the at least one of the plurality of electrode shapes is indicative of a cutting accuracy associated with at least one of the cutting lines; and

cutting the layer of support material and conductive pattern along a longitudinal cutting line in the longitudinal direction, wherein the respective edge portions at the cutting width have a lateral distance from the longitudinal cutting line in a lateral direction that is perpendicular to the longitudinal direction.

2. The method of claim 1, wherein printing the conductive pattern includes applying an electrode material through a plurality of openings in the screen printing mask, the plurality of openings including respective central enlarged sections.

3. The method of claim 1, wherein:

printing the conductive pattern comprises forming the plurality of electrode shapes such that the plurality of electrode shapes have respective lengths extending in a longitudinal direction; and

cutting the layer of support material and conductive pattern comprises cutting along the plurality of cutting lines that extend substantially in a lateral direction that is perpendicular to the longitudinal direction.

4. The method of claim 1, further comprising measuring the cutting width of the at least one of the plurality of electrode shapes.

5. The method of claim 4, further comprising determining the cutting accuracy based on the cutting width of the at least one of the plurality of electrode shapes, wherein the cutting accuracy is the longitudinal offset between the at least one of the cutting lines and a desired cutting location.

6. The method of claim 5, wherein determining the cutting accuracy comprises referencing a width profile that relates the cutting width with the longitudinal offset between the at least one of the cutting lines and the desired cutting location.

7. The method of claim 1, wherein cutting the layer of support material along the plurality of cutting lines comprises cutting the at least one of the plurality of electrode shapes substantially along the lateral centerline of the central enlarged portion.

8. The method of claim 1, further comprising applying a non-conductive coating to the multilayer electronic device on less than all of an exposed portion of the pair of electrodes.

9. The method of claim 1, further comprising forming a first terminal electrically connected with one of the pair of electrodes and a second terminal electrically connected with the other of the pair of electrodes.

10. A multilayer electronic device comprising a plurality of layers, the plurality of layers comprising a plurality of electrodes, and at least one of the plurality of electrodes comprising:

a main portion extending in a longitudinal direction, the main portion having a main width in a lateral direction perpendicular to the longitudinal direction; and

a base portion having a width profile in the longitudinal direction from the main portion to a lateral edge of the respective layer of the plurality of layers that comprises the at least one of the plurality of electrodes, the lateral edge extending in the lateral direction, a width of the base portion at any location along the width profile being greater than the main width, wherein the width profile is sloped at a first angle with respect to the longitudinal direction, the first angle being greater than 0 degrees and less than 90 degrees along the width profile from the main

portion to an edge of the respective layer of the plurality of layers that comprises the at least one of the plurality of electrodes,

wherein the width profile has a lateral distance in the lateral direction from a longitudinal edge of the respective layer that extends in the longitudinal direction.

11. The multilayer electronic device of claim 10, wherein the base portion has a length in the longitudinal direction that is less than 300 µm.

12. The multilayer electronic device of claim 10, wherein the multilayer electronic device has an overall length in the longitudinal direction and the base portion has a length in the longitudinal direction, and wherein a ratio of the length of the overall length of multilayer electronic device to the length of the base portion is greater than 5.

13. The multilayer electronic device of claim 10, wherein the portion of the width profile that is sloped has a length in the longitudinal direction that is less than 200 µm.

14. The multilayer electronic device of claim 10, wherein the width profile is further sloped at a second angle with respect to the longitudinal direction, the second angle being greater than 0 degrees and less than 90 degrees along the width profile from the main portion to a lateral edge of the respective layer of the plurality of layers that comprises the at least one of the plurality of electrodes.