



ABSTRACT OF THE DISCLOSURE

Pixel information and control information transmitted by a facsimile machine 1a is received by a procedure controlling circuit 13 having a MODEM 13a for facsimile machines. A facsimile procedure controller 14 decodes the pixel information and the control information. A connection/transfer controller 15 edits the decoded information and produces a control packet and a pixel packet. The control packet and the pixel packet are output to a local area network (LAN) 4 via a LAN controller 17 and a LAN control circuit 18. When receiving a facsimile, the control packet signal transmitted via the LAN 4 is converted into pixel control information by the connection/transfer controller 15, and the pixel control information is supplied to the facsimile procedure controller 14. The connection/transfer controller 15 also converts the received pixel packet signal into pixel information. The pixel information is temporarily stored in a memory 16. When a predetermined amount of pixel information is accumulated in the memory 16, said predetermined amount of pixel information is supplied to the facsimile procedure controller 14, and finally transmitted to the facsimile machine 1a via the procedure control circuit 13.