

ABSTRACT

In a horizontal deflection circuit output stage, a retrace pulse voltage is developed in a primary winding (X1) of a flyback transformer (T1) and transformer-coupled to a secondary winding and rectified for developing a high voltage (U). A portion (HV/3) of the transformer-coupled rectified voltage is combined in a voltage divider (200) with a second rectified voltage (VOUT). The second rectified voltage is produced in a peak rectifier (D2) from the retrace pulse voltage in the primary winding (X1) in a manner that bypasses the secondary winding. A screen grid voltage (VS) is developed at a terminal (200b) of the voltage divider. A change in beam current loading (iBEAM) produces variations in opposite sense in the combined rectified voltages in a manner to regulate the screen grid voltage.

Single FIGURE

This Sheet Has Been Replaced

