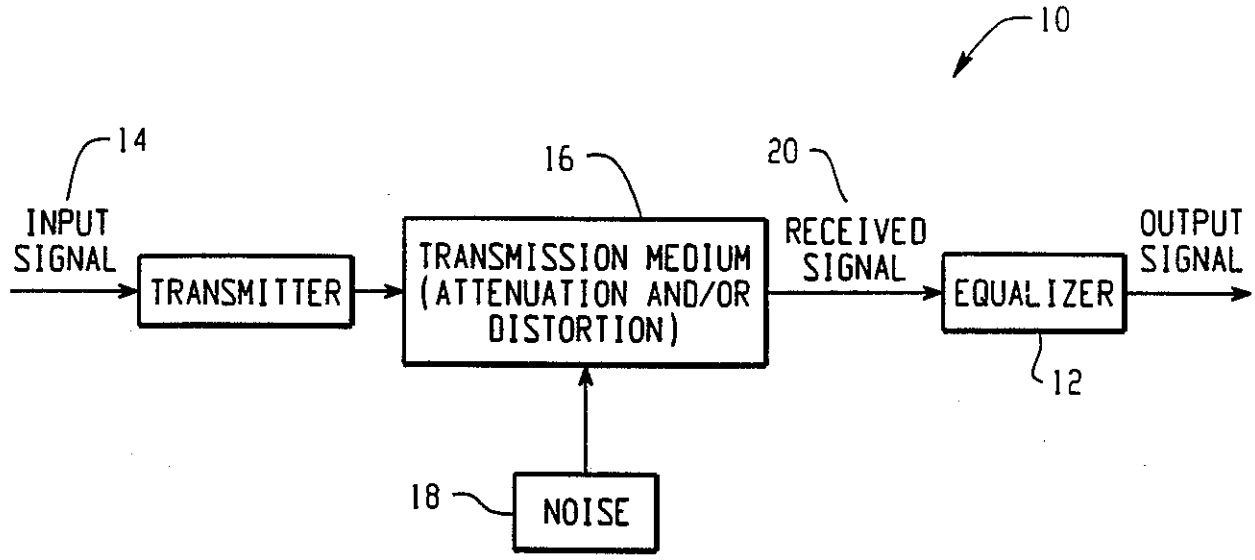


Fig. 1
RELATED ART

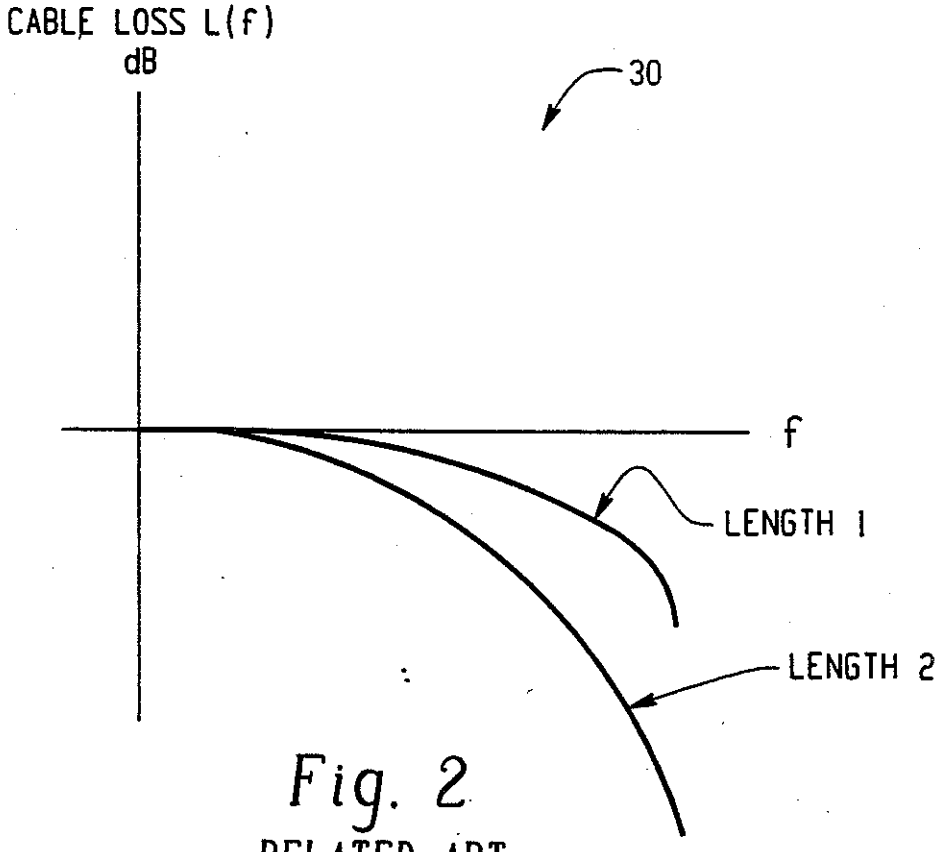


Fig. 2
RELATED ART

- 2 MAR 2006

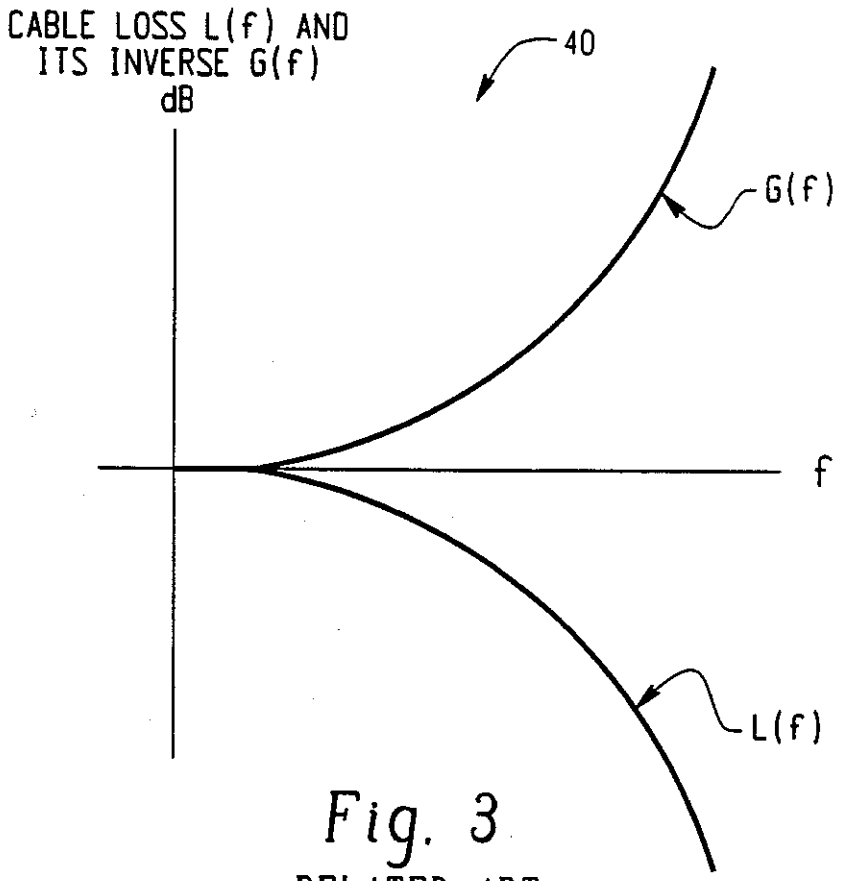


Fig. 3
RELATED ART

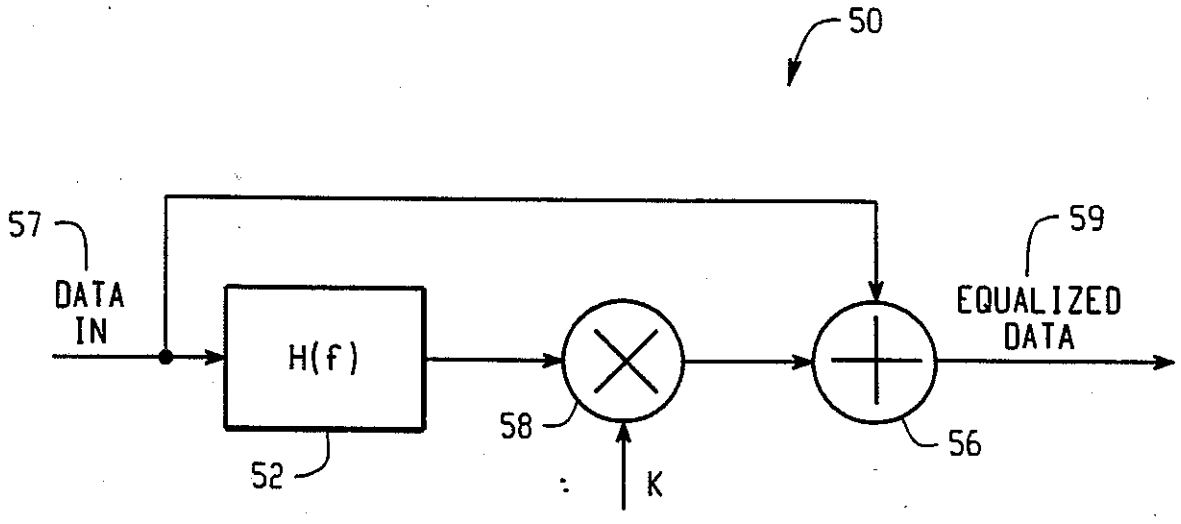


Fig. 4
RELATED ART

- 2 MAR 2006

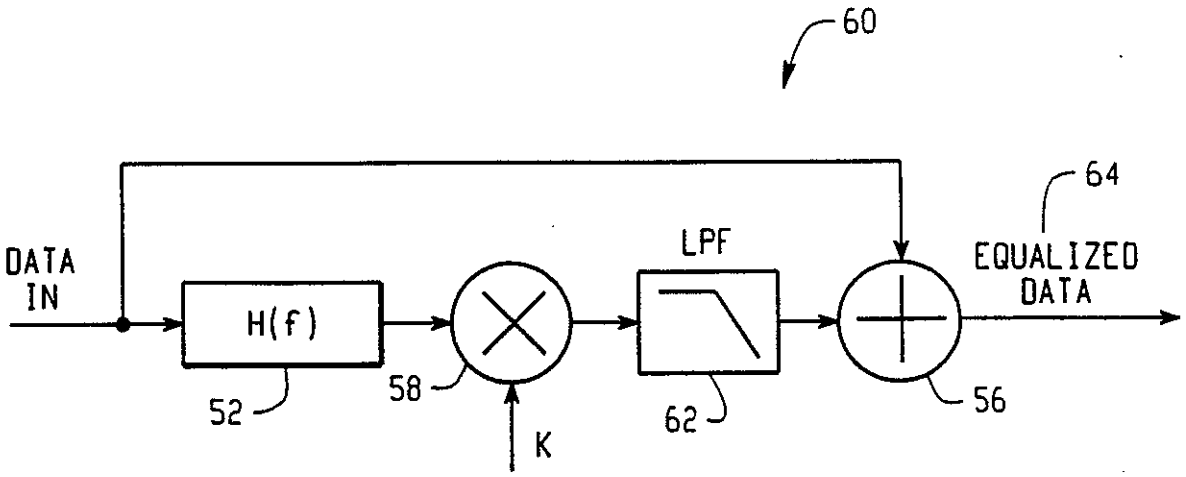


Fig. 5
RELATED ART

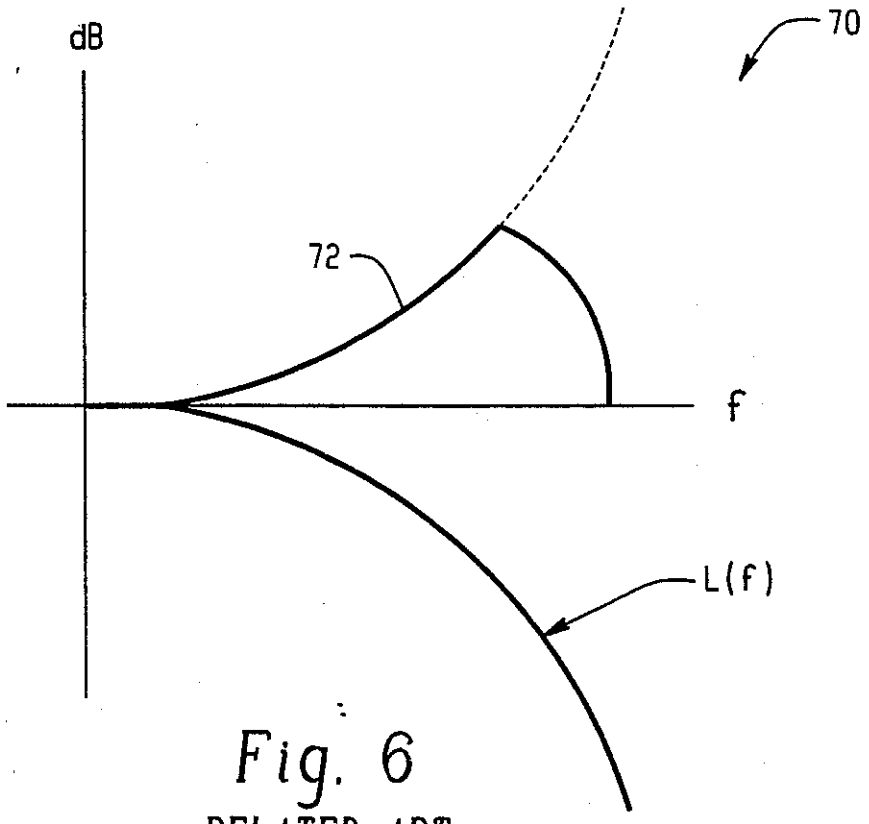


Fig. 6
RELATED ART

2 MAR 2006

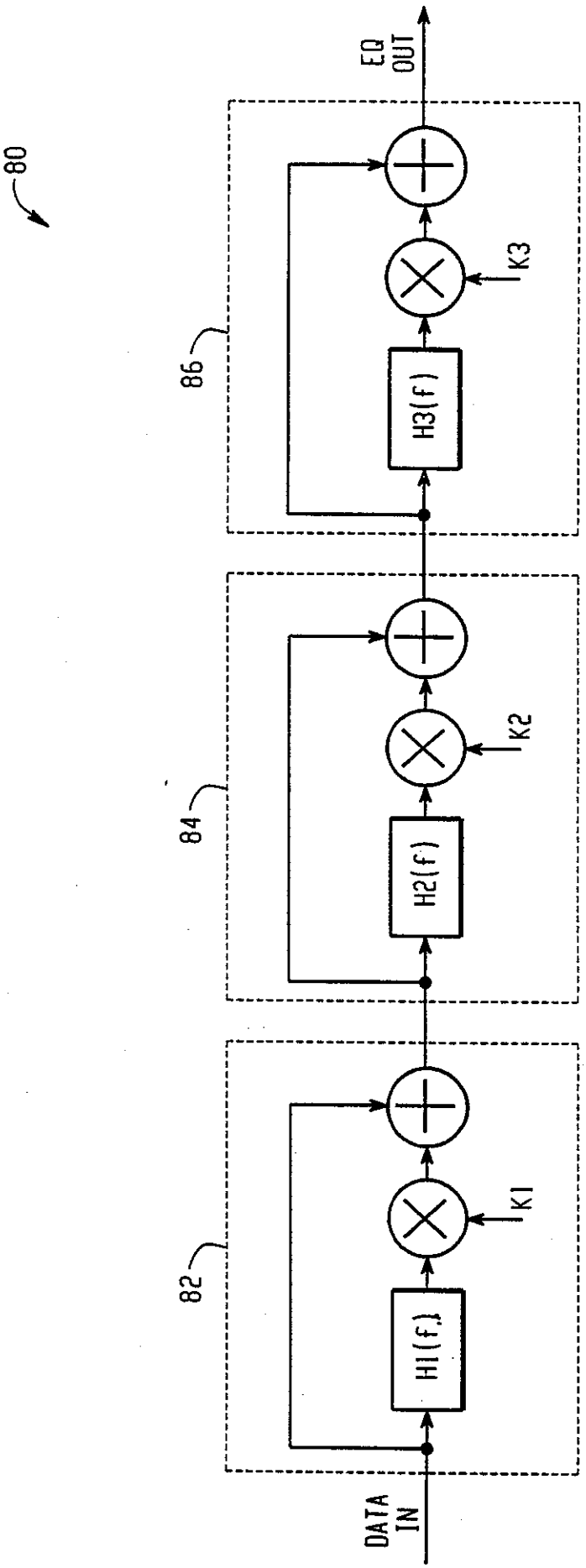


Fig. 7
RELATED ART

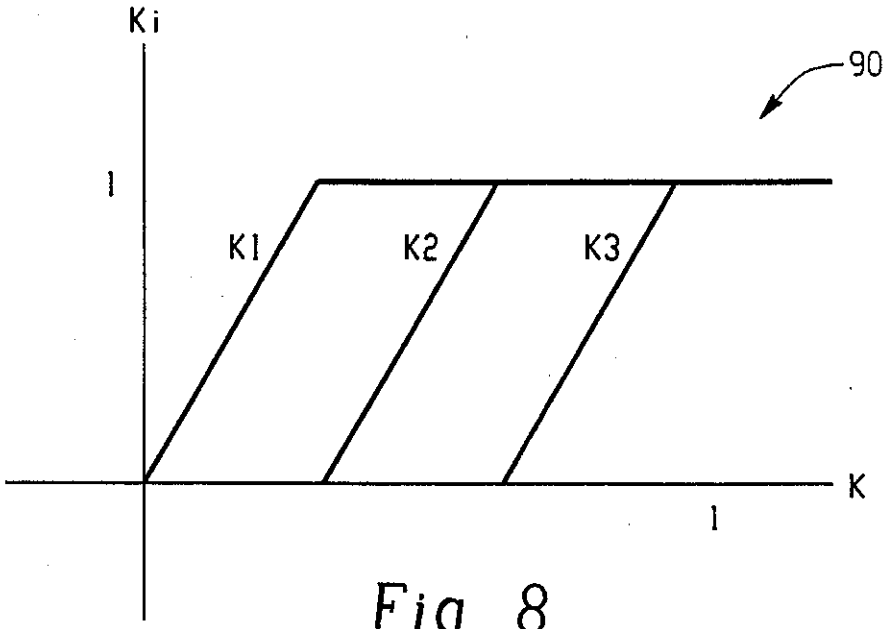


Fig. 8
RELATED ART

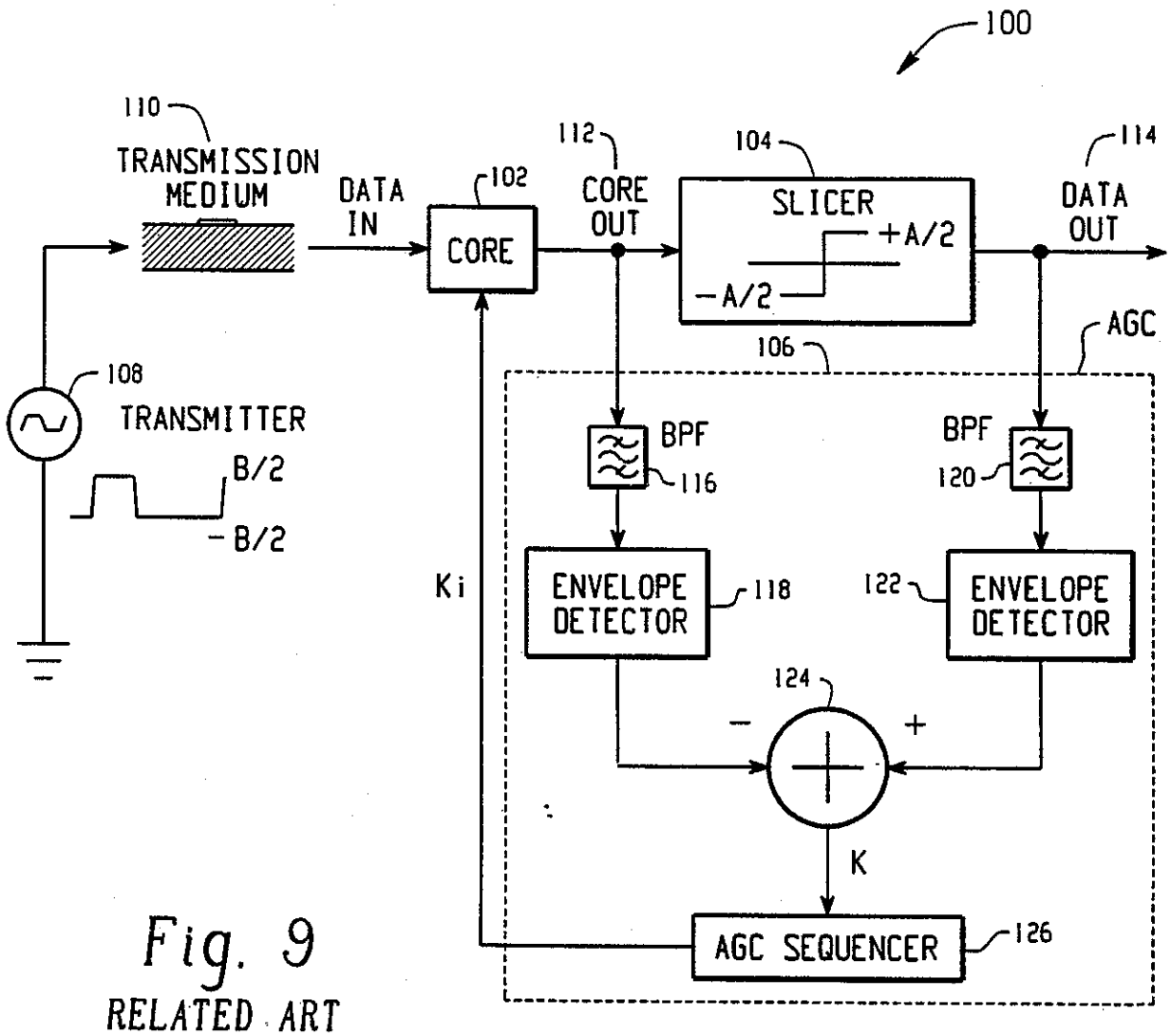


Fig. 9
RELATED ART

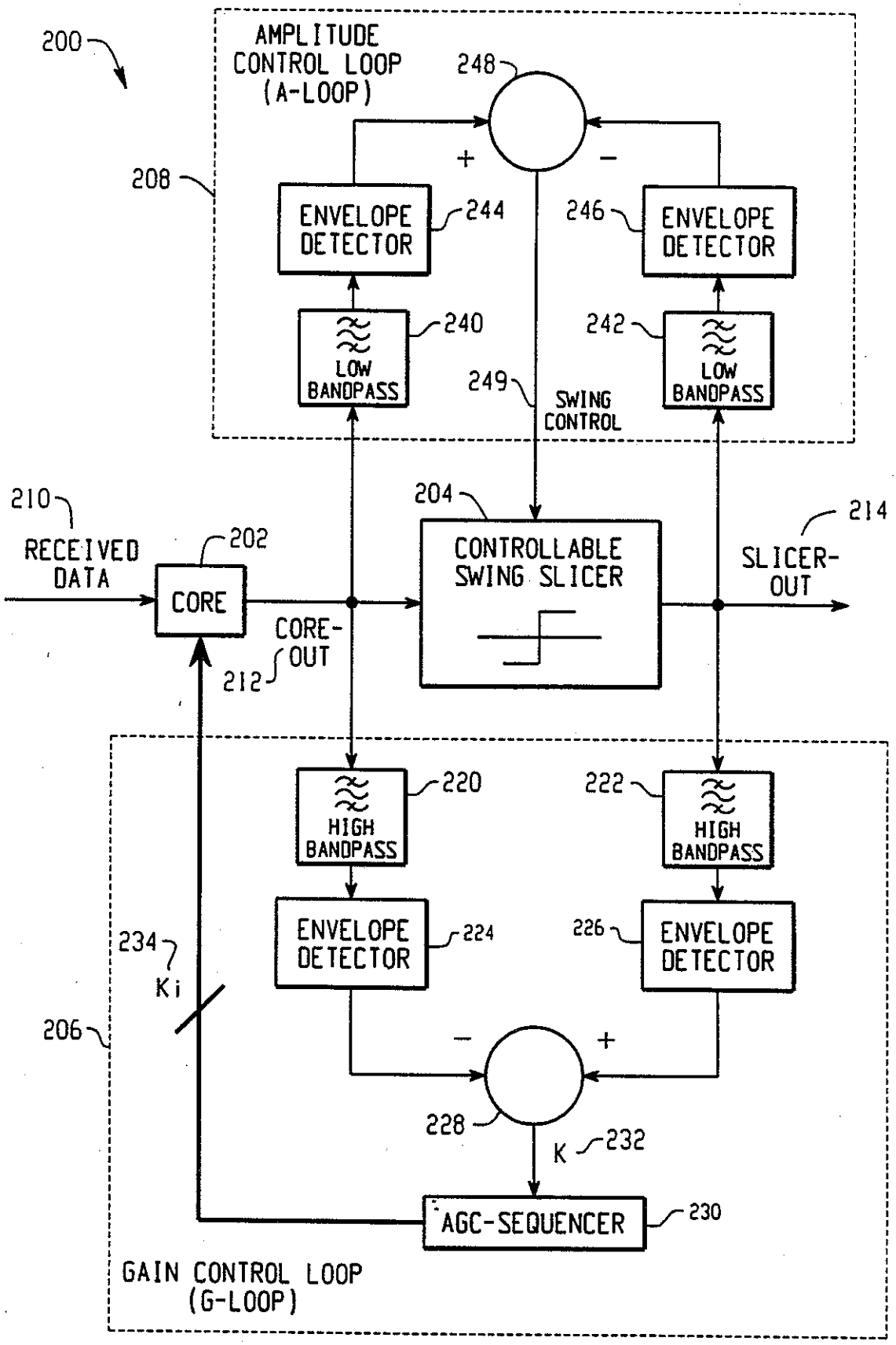

200
AMPLITUDE CONTROL LOOP (A-LOOP)
248
208
+
−
ENVELOPE DETECTOR
244
246
ENVELOPE DETECTOR
240
LOW BANDPASS
242
LOW BANDPASS
249
SWING CONTROL
210
RECEIVED DATA
202
CORE
204
CONTROLLABLE SWING SLICER
214
SLICER-OUT
CORE-OUT
212
220
HIGH BANDPASS
222
HIGH BANDPASS
234
Ki
206
ENVELOPE DETECTOR
224
226
ENVELOPE DETECTOR
−
+
228
K
232
AGC-SEQUENCER
230
GAIN CONTROL LOOP (G-LOOP)

Fig. 10

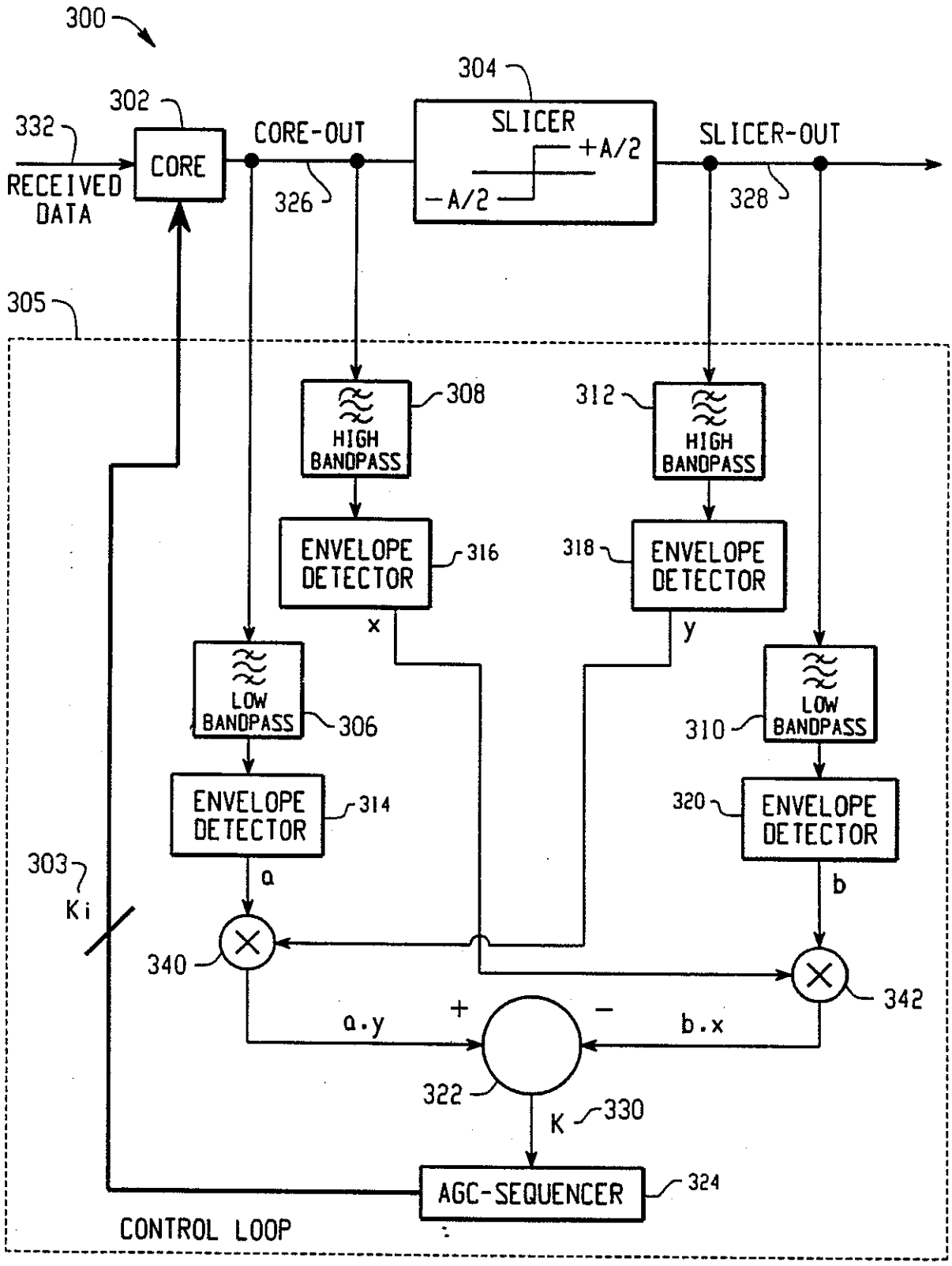

300
302
304
332
CORE
CORE-OUT
SLICER
+A/2
-A/2
SLICER-OUT
RECEIVED DATA
326
328
305
308
312
HIGH BANDPASS
HIGH BANDPASS
ENVELOPE DETECTOR
316
318
ENVELOPE DETECTOR
x
y
LOW BANDPASS
306
310
LOW BANDPASS
ENVELOPE DETECTOR
314
320
ENVELOPE DETECTOR
303
Ki
a
b
340
342
a.y
+
-
b.x
322
K
330
AGC-SEQUENCER
324
CONTROL LOOP

Fig. 11

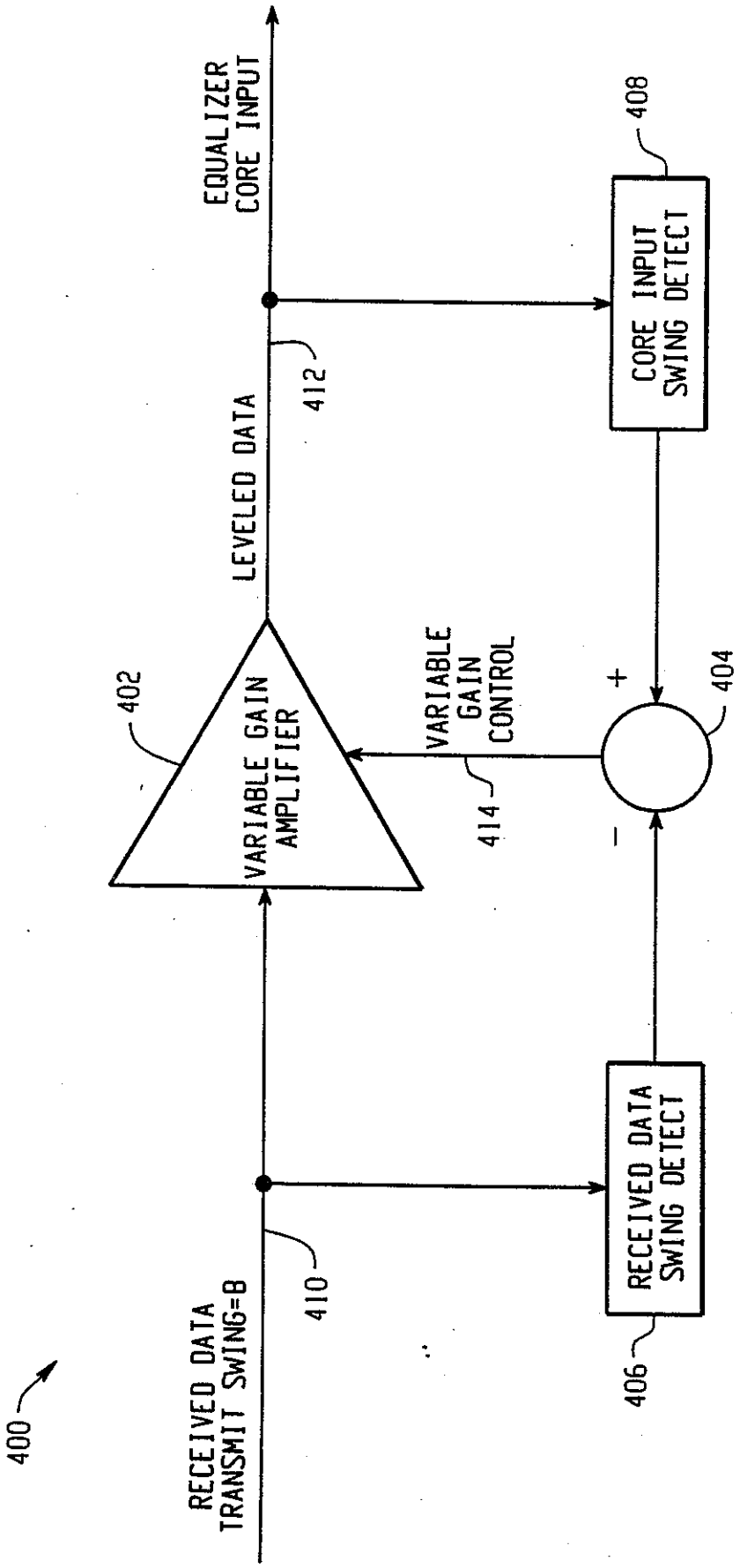

400
RECEIVED DATA
TRANSMIT SWING=B
410
RECEIVED DATA
SWING DETECT
406
CORE INPUT
SWING DETECT
408
EQUALIZER
CORE INPUT
LEVELED DATA
412
402
VARIABLE GAIN
AMPLIFIER
VARIABLE
GAIN
CONTROL
414
+
-
404

Fig. 12

2 MAR 2006

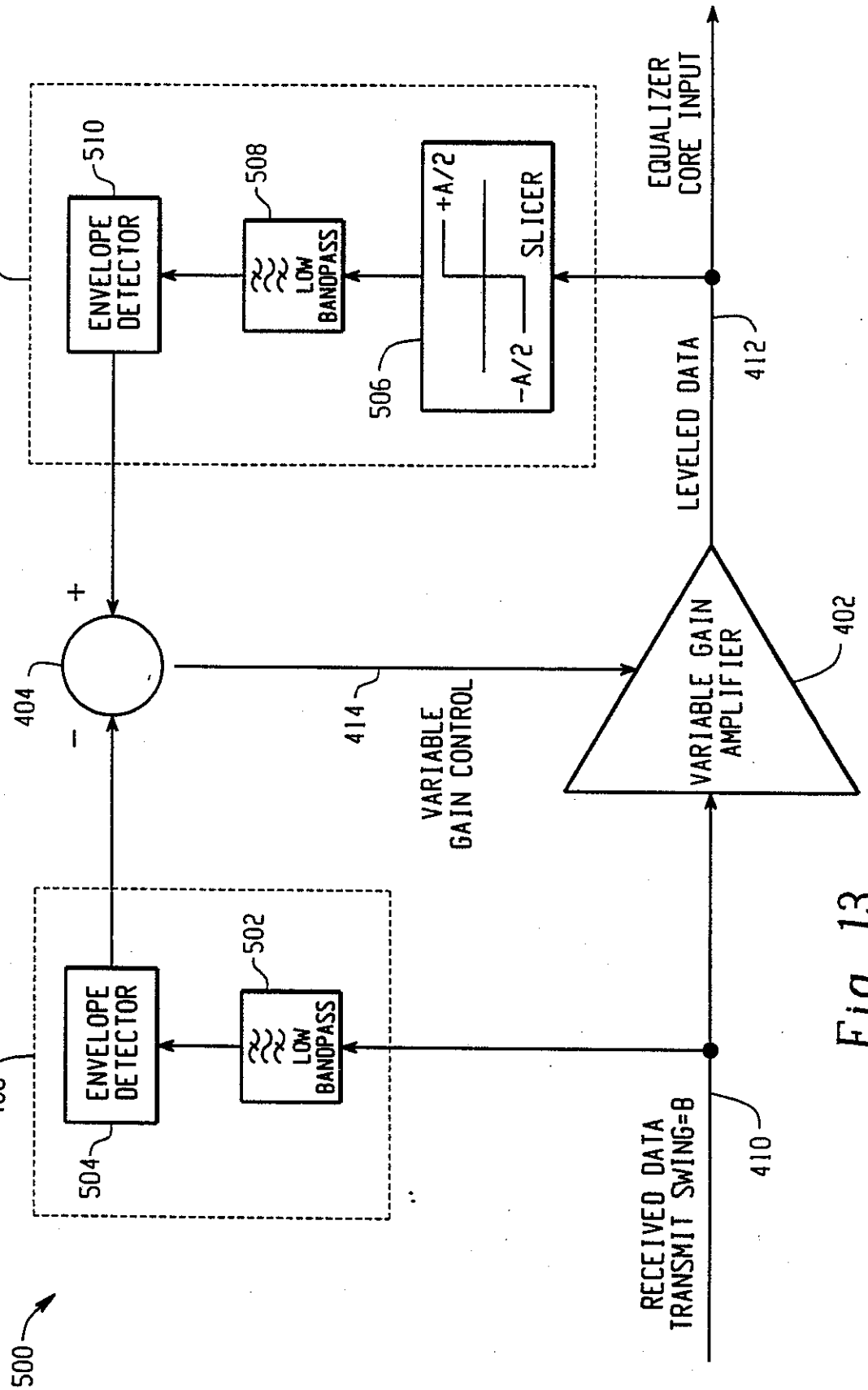


Fig. 13

2 MAR 2006

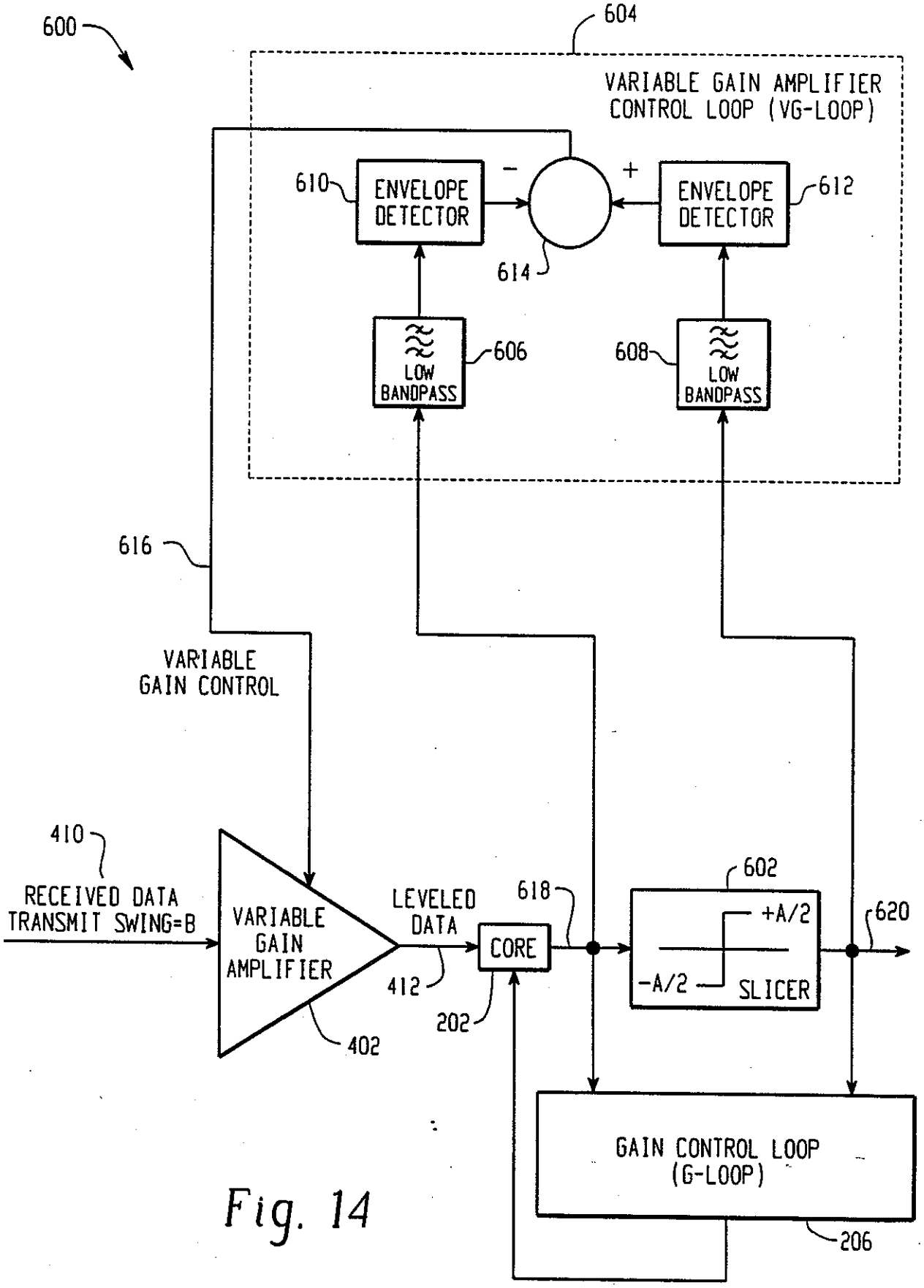


Fig. 14