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ABSTRACT OF THE DISCLOSURE

CAPACITIVE DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVERTERS

A digital-to-analog converter (DAC) comprises a capacitive DAC that comprises N first capacitances that are connected in parallel and that have first ends and second ends, wherein N is an integer greater than one, and N first switches that selectively connect a selected one of the second ends of the N first capacitances to a common node and non-selected ones of the second ends of the N first capacitances to one of a voltage potential and a reference potential. Capacitance values of the N first capacitances are substantially equal. A second DAC communicates with the common node.

FIG. 9B

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