

ABSTRACT

Embodiments of the present disclosure provide an ECS circuit and method, and a memory. The ECS circuit includes an ECS control module, a command generation module, an address counting module, and an error tracking and recording module. The ECS control module is
5 configured to: receive a mode control signal, and generate an ECS command signal based on the mode control signal. The command generation module is configured to generate an internal command signal based on the ECS command signal. The internal command signal is configured to perform a corresponding ECS operation. The address counting module is configured to: perform address counting based on the internal command signal, and generate
10 a counting end signal when counting is completed for a target address. The error tracking and recording module is configured to: receive an error signal, and generate an error tracking signal based on the counting end signal and the error signal. The error tracking signal is configured to record error information of the ECS operation.

15 [Fig. 1]