

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
12 July 2001 (12.07.2001)

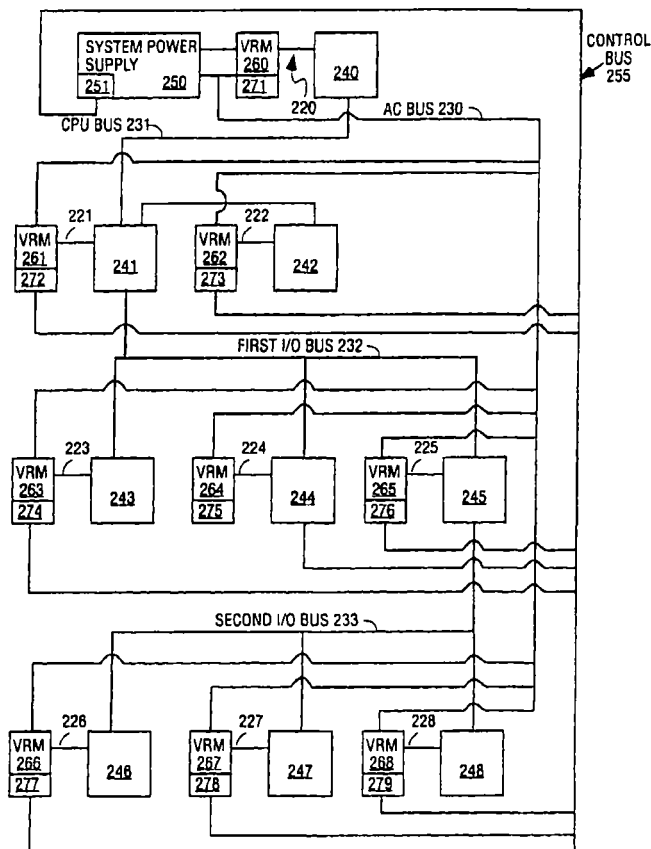
(10) International Publication Number
WO 01/50570 A1

PCT

- | | |
|---|---|
| <p>(51) International Patent Classification⁷:
G06F 1/26</p> <p>(21) International Application Number: PCT/US00/24391</p> <p>(22) International Filing Date:
5 September 2000 (05.09.2000)</p> <p>(25) Filing Language: English</p> <p>(26) Publication Language: English</p> <p>(30) Priority Data:
09/475,483 30 December 1999 (30.12.1999) US</p> <p>(71) Applicant (for all designated States except US): INTEL CORPORATION [US/US]; 2200 Mission College Boulevard, Santa Clara, CA 95052 (US).</p> | <p>(72) Inventor; and
(75) Inventor/Applicant (for US only): DROBNIK, Josef [CA/US]; 37 W. 779 Volentine Farm Road, Batavia, IL 60510 (US).</p> <p>(74) Agents: MALLIE, Michael, J. et al.; Blakely, Sokoloff, Taylor & Zafman LLP, 7th Floor, 12400 Wilshire Boulevard, Los Angeles, CA 90025 (US).</p> <p>(81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW.</p> <p>(84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian</p> |
|---|---|

[Continued on next page]

(54) Title: METHOD AND APPARATUS FOR MULTIFREQUENCY POWER DISTRIBUTION



(57) Abstract: A method for distributing power in an electronic system, comprising receiving power in a first domain at a system power supply converting the power to a second domain having alternating current (AC) signal components at a plurality of frequencies, and transmitting the converted power in the second domain with AC signal components at multiple frequencies to a plurality of AC voltage regulator modules (VRM) of the electronic system. The method further comprises receiving a feedback signal from the plurality of AC VRM's and adjusting the AC signal components being transmitted to the plurality of VRM based at least in part of the feedback signal. The method further comprises receiving a control signal from a central processing unit (CPU) and adjusting the AC signal components being transmitted to the plurality of VRM based at least in part on the control signal.

WO 01/50570 A1



162162



ACTION

WO 01/50570 A1

patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

Published:

— *With international search report.*