

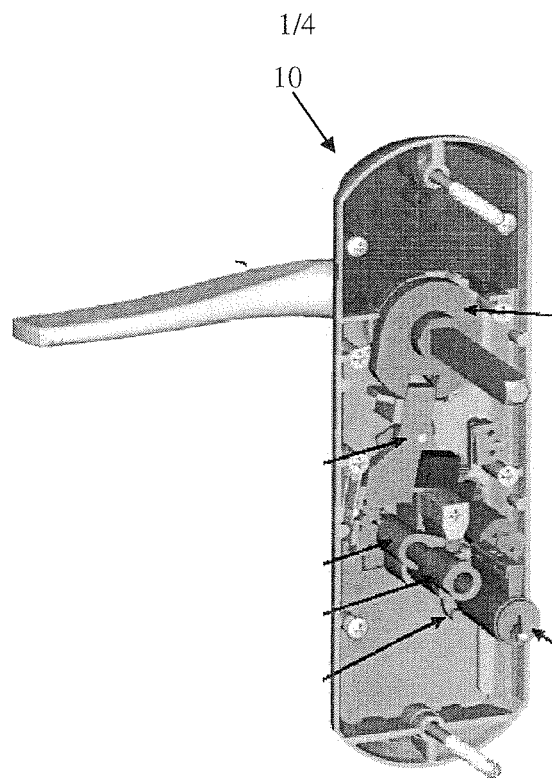


FIG 1 PRIOR ART LOCK

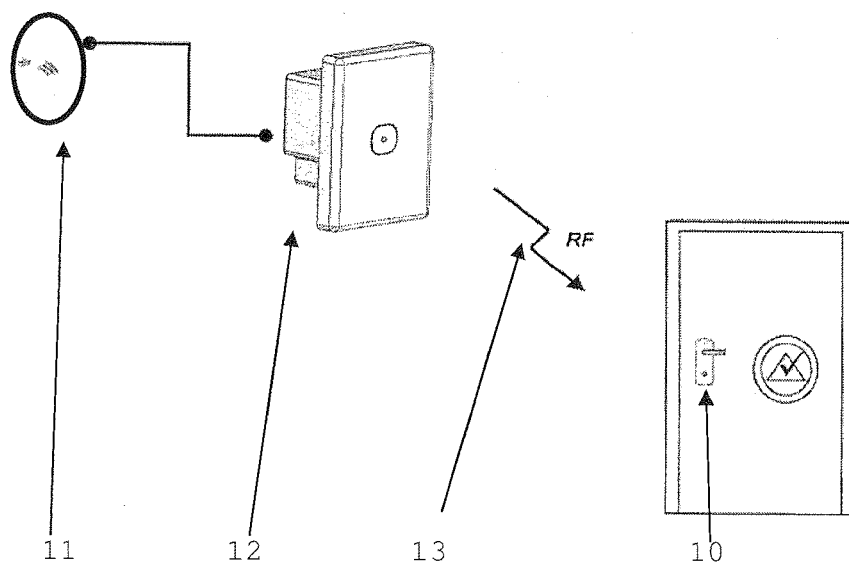


FIG 2

General Interface Module

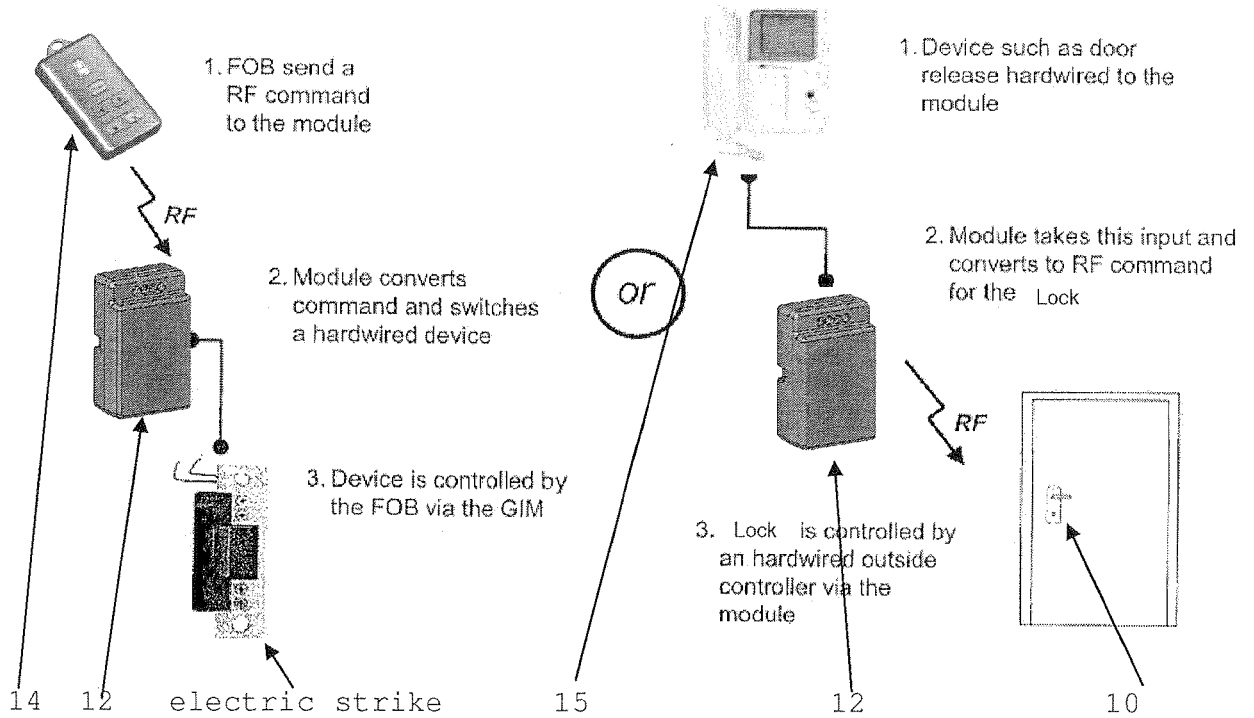


FIG 3

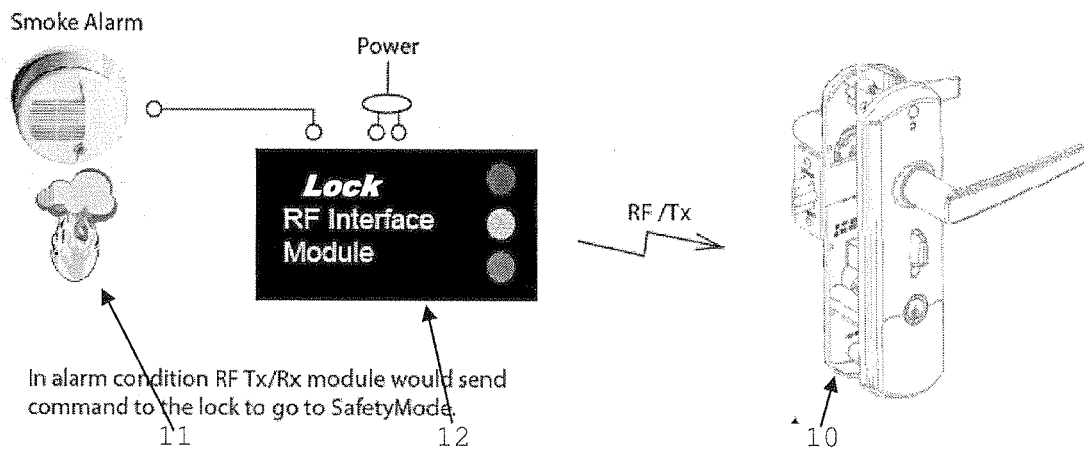


FIG 4

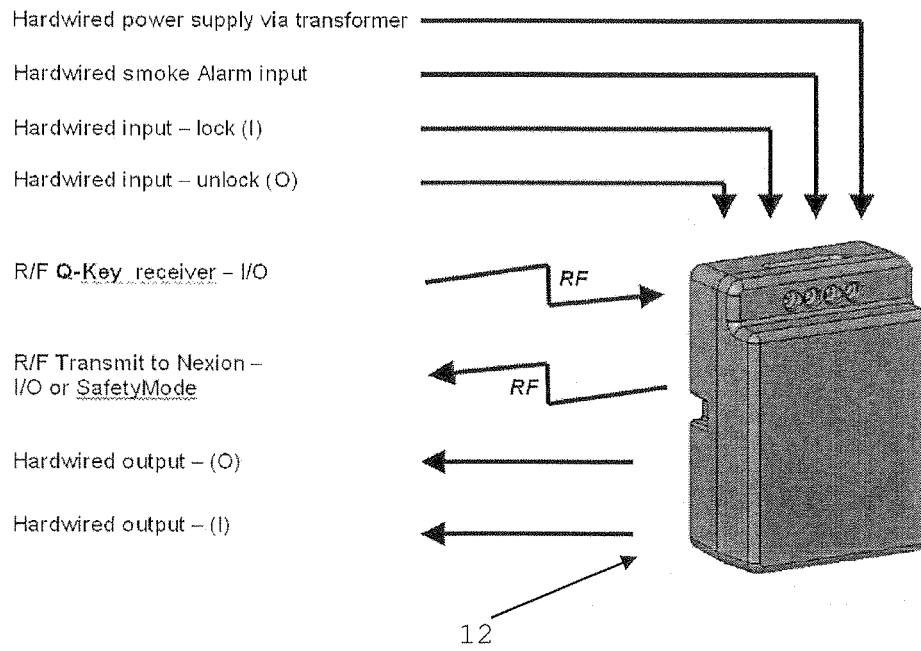


FIG 5

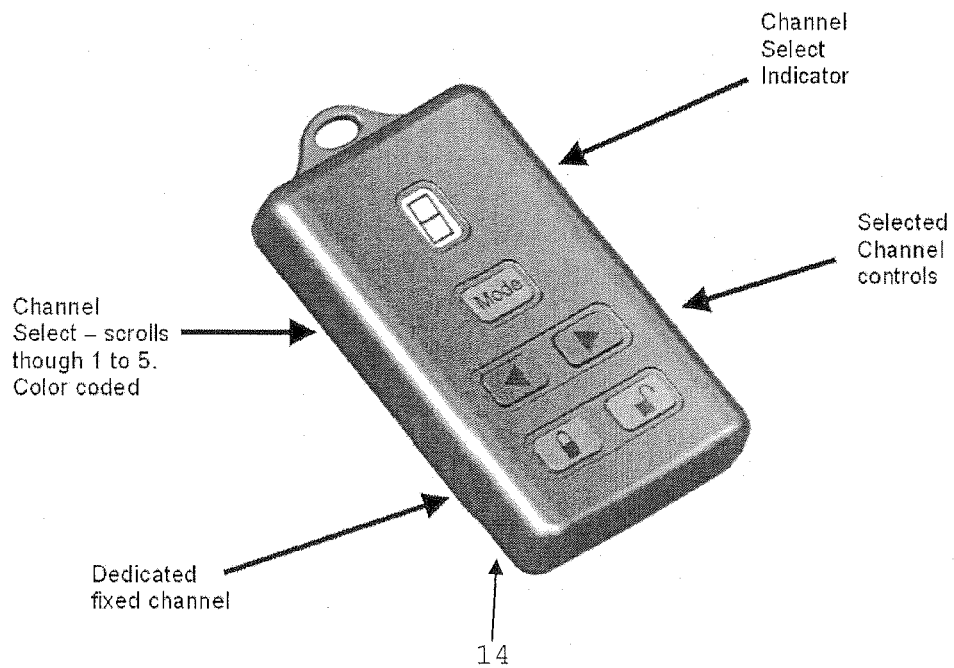


FIG 6

1 IM per function/channel

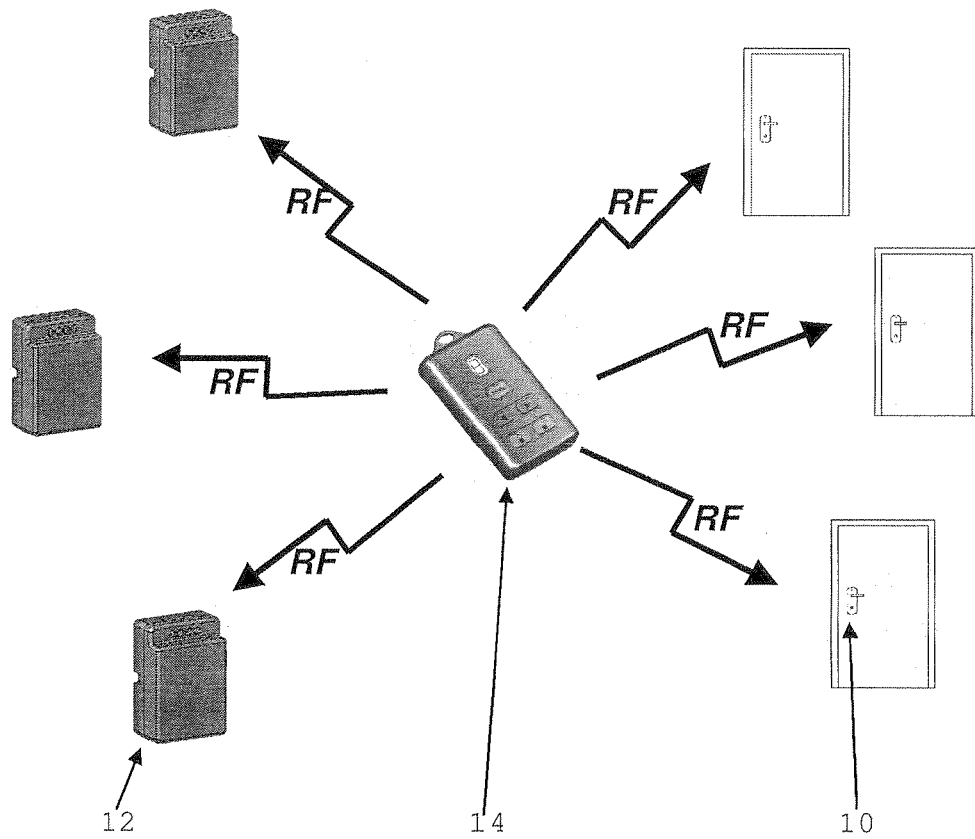


FIG 7