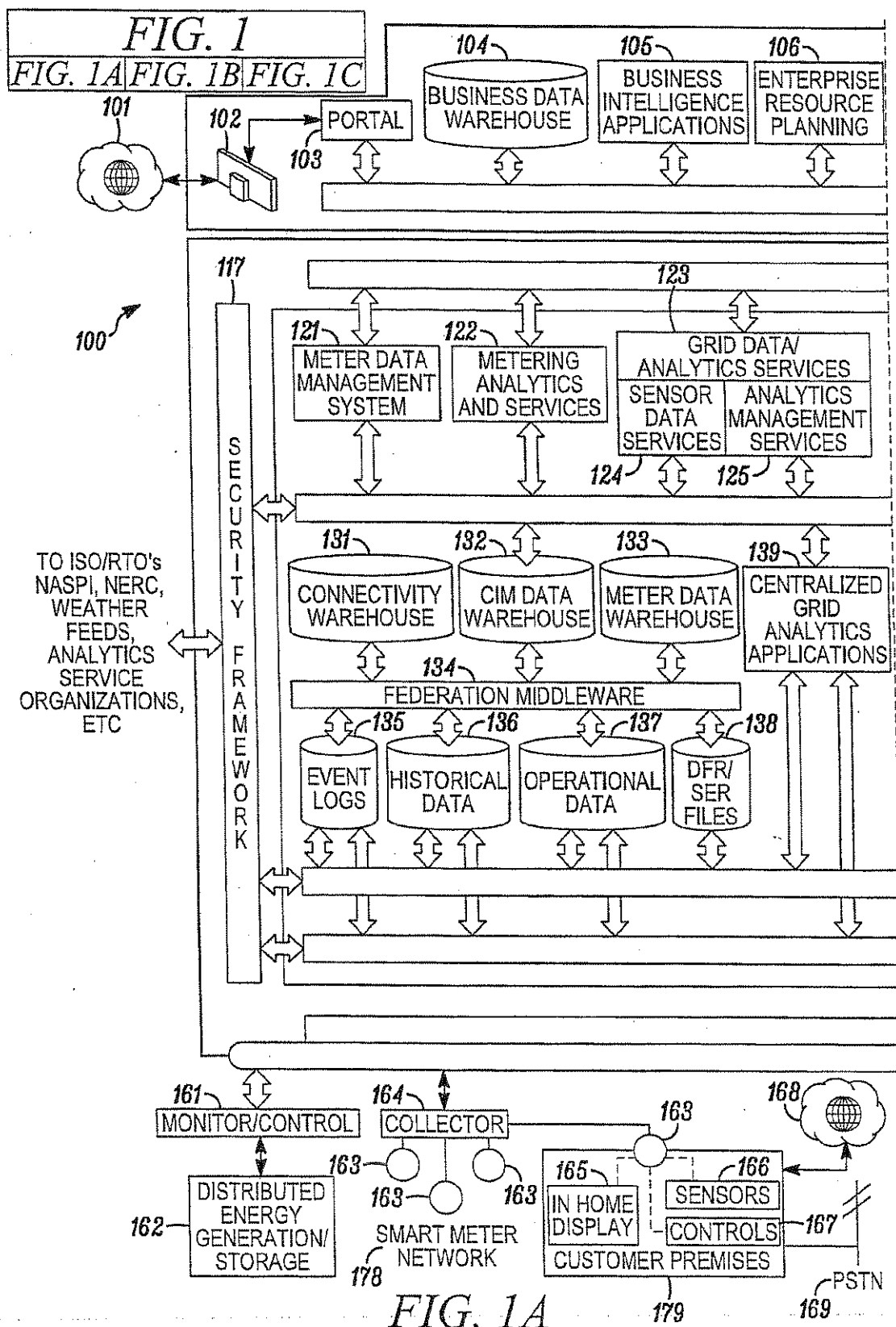
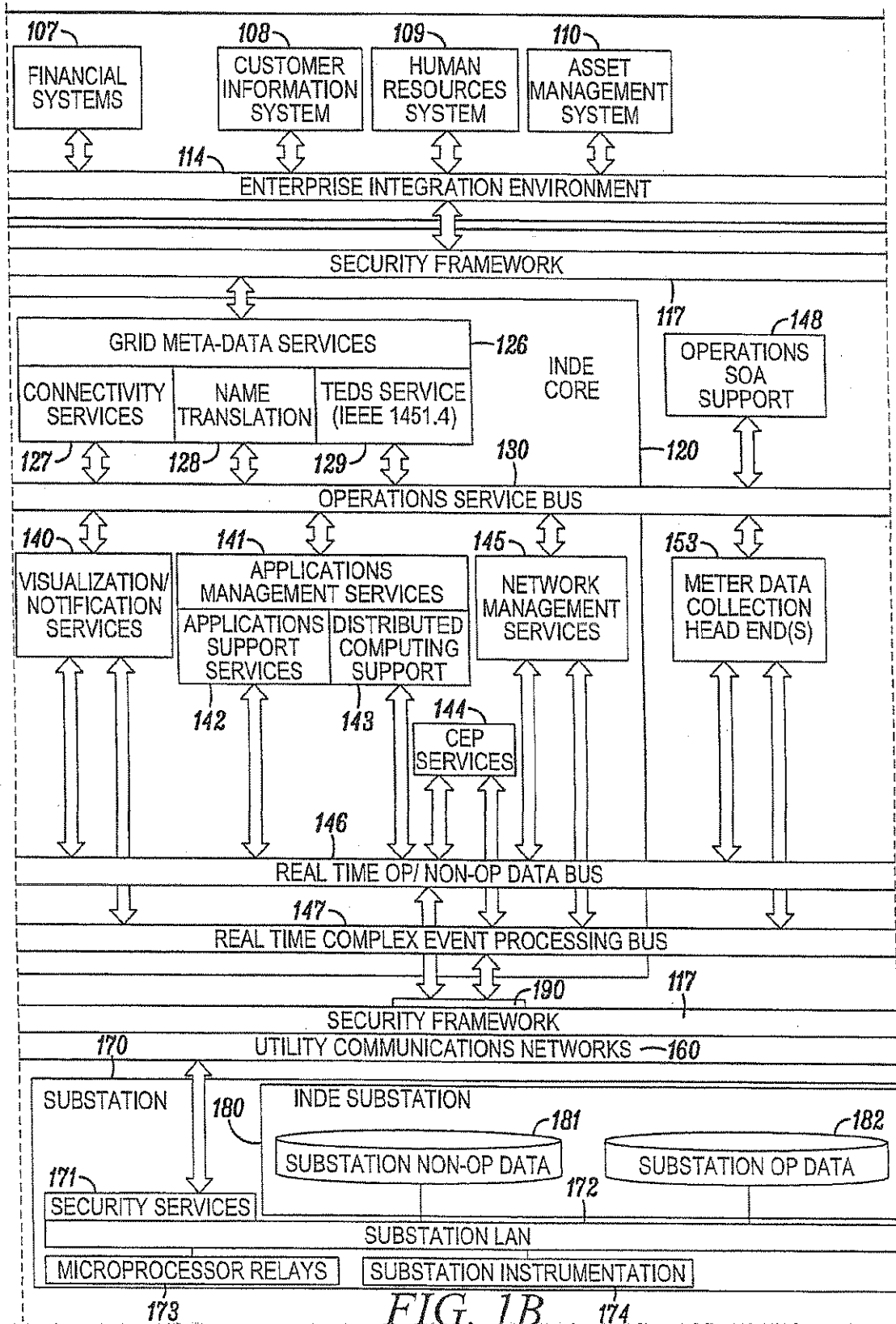






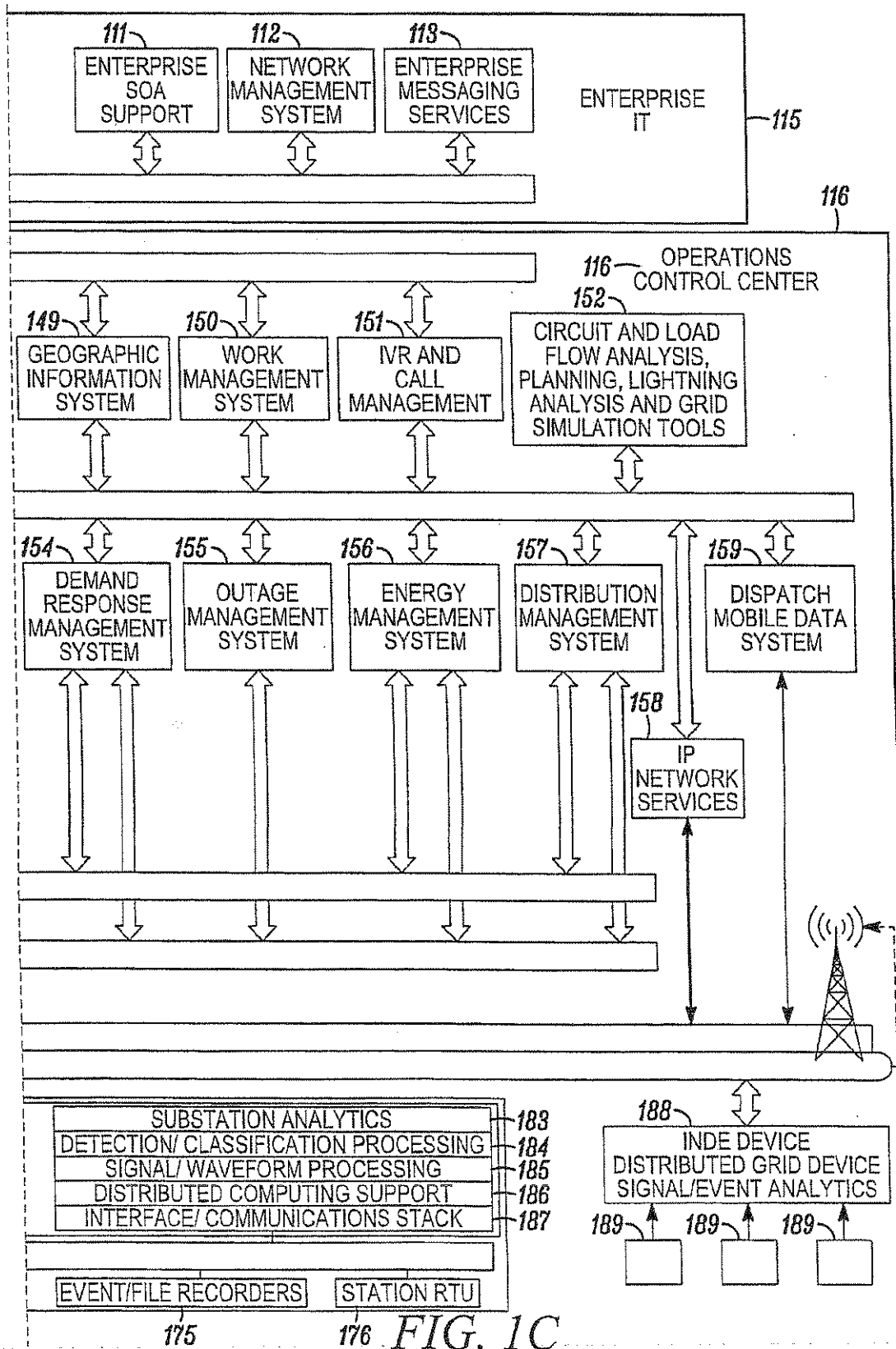


FIG. 1C

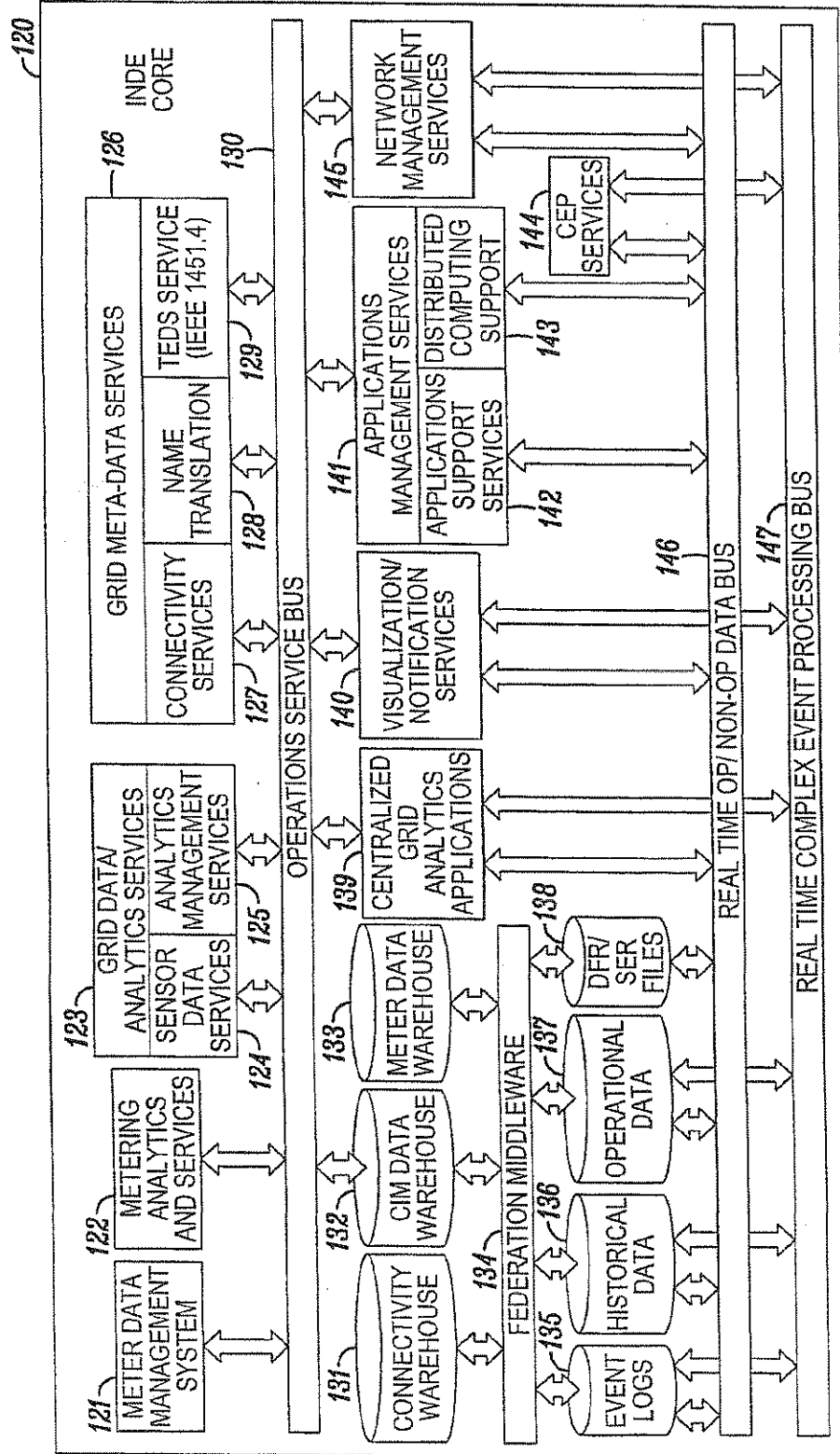


FIG. 2

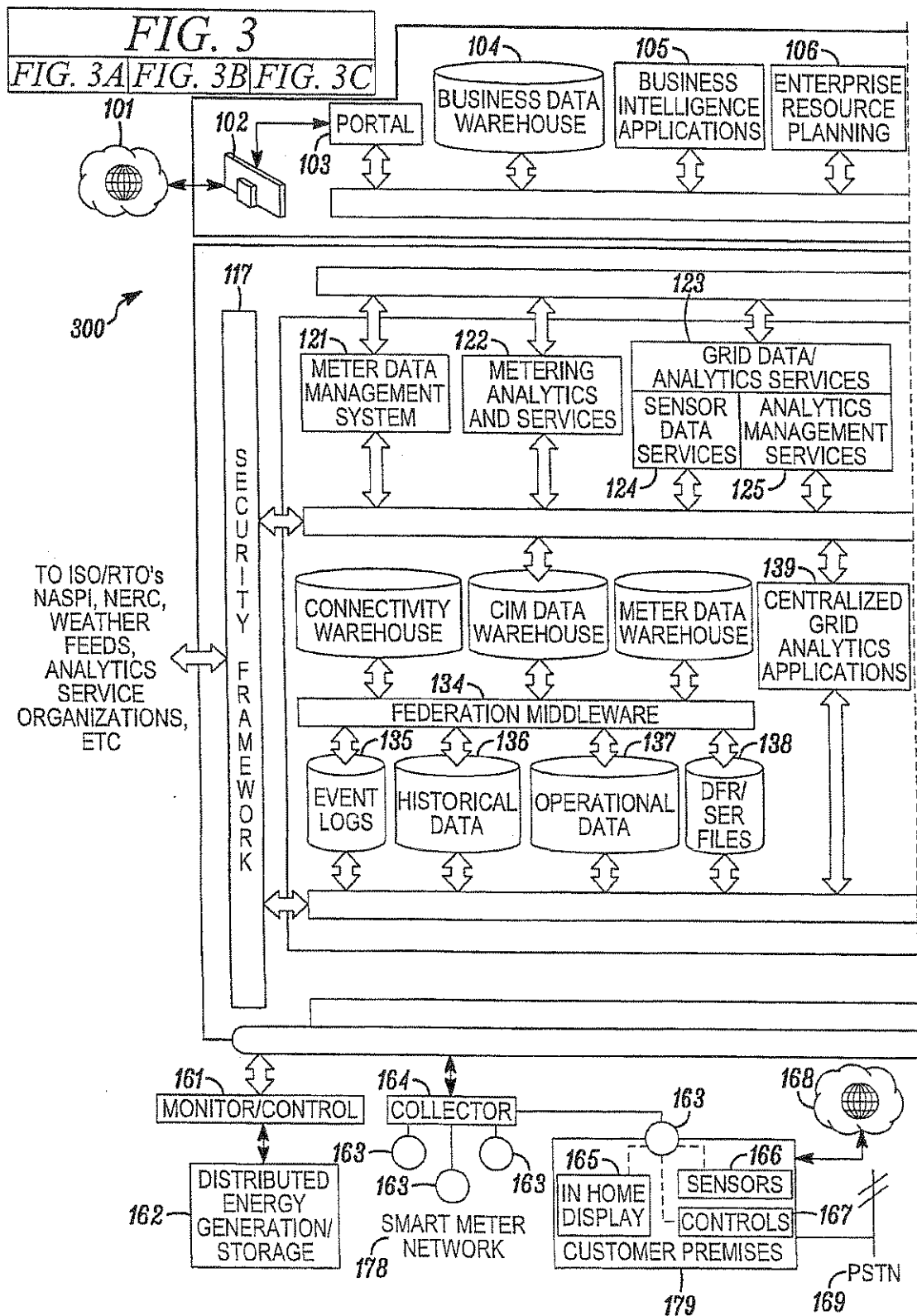


FIG. 3A

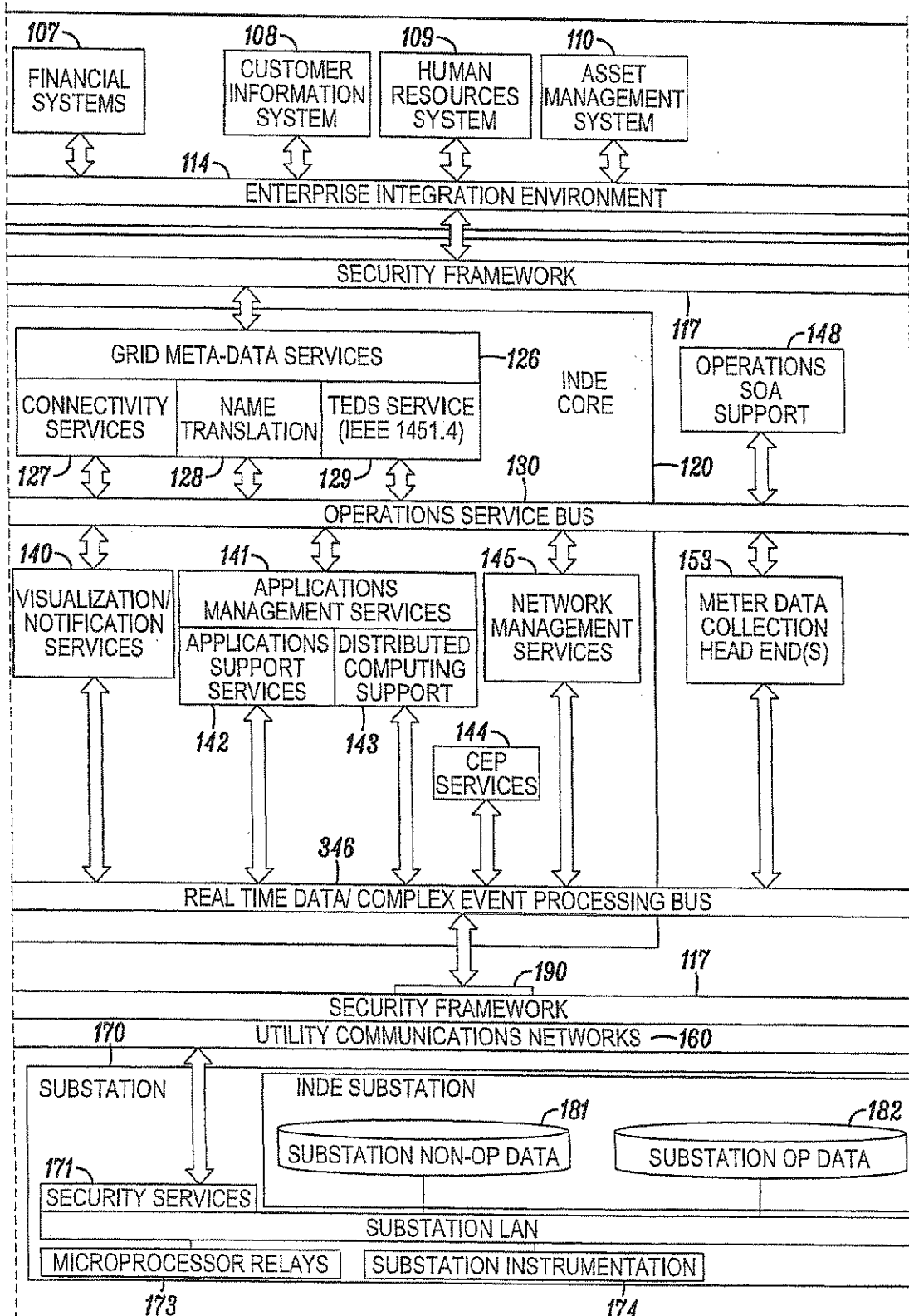


FIG. 3B

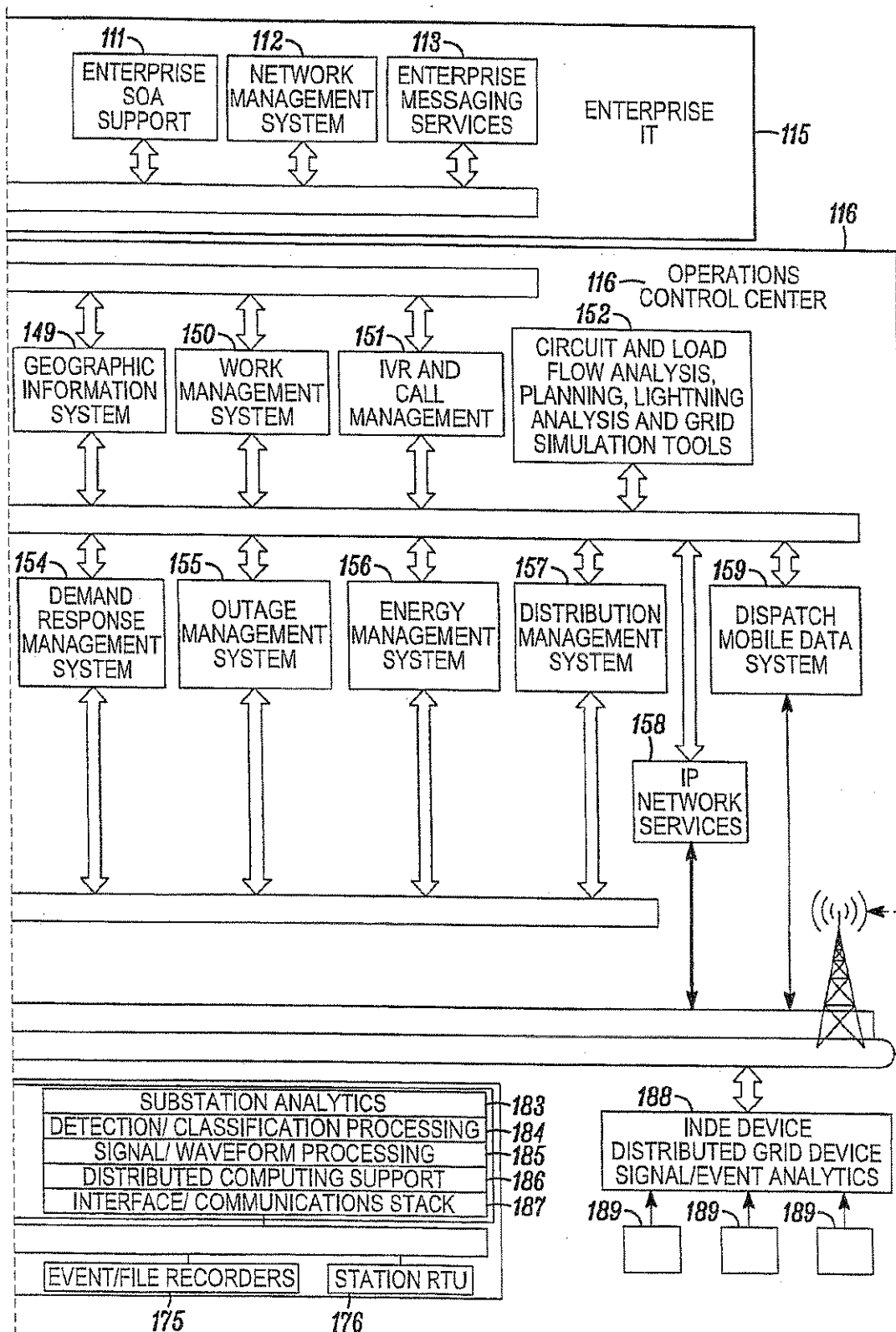


FIG. 3C

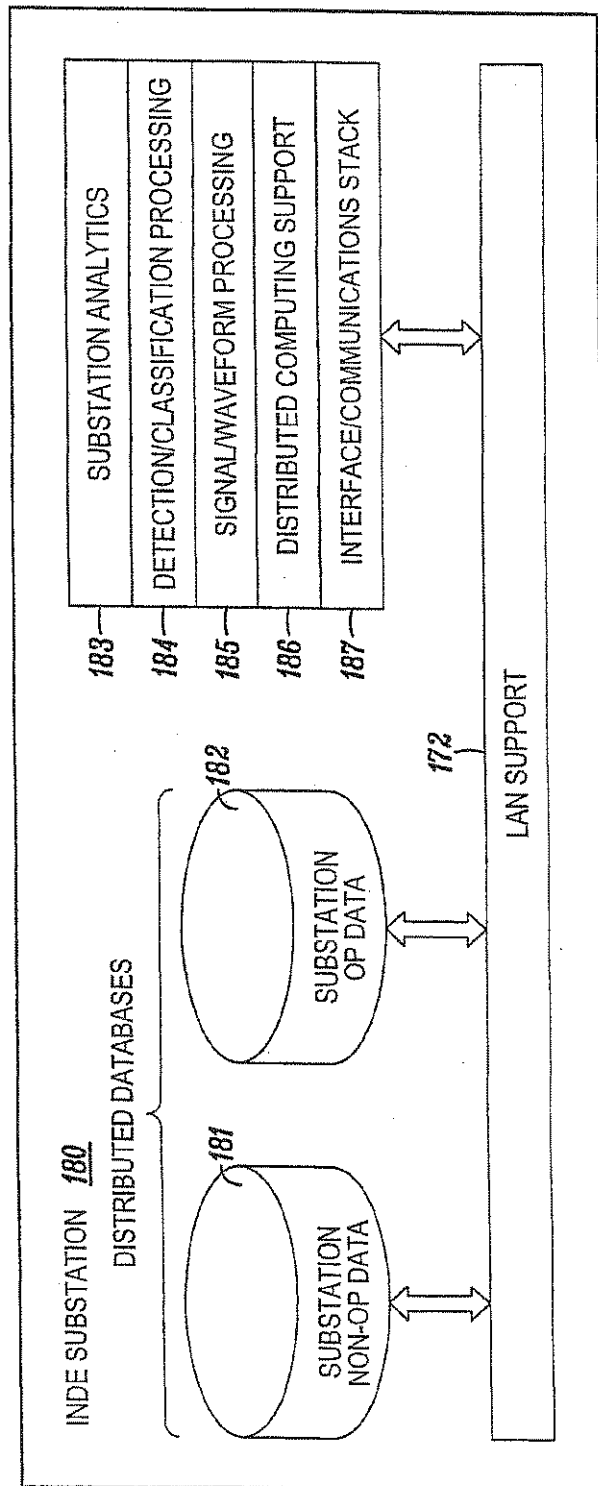


FIG. 4

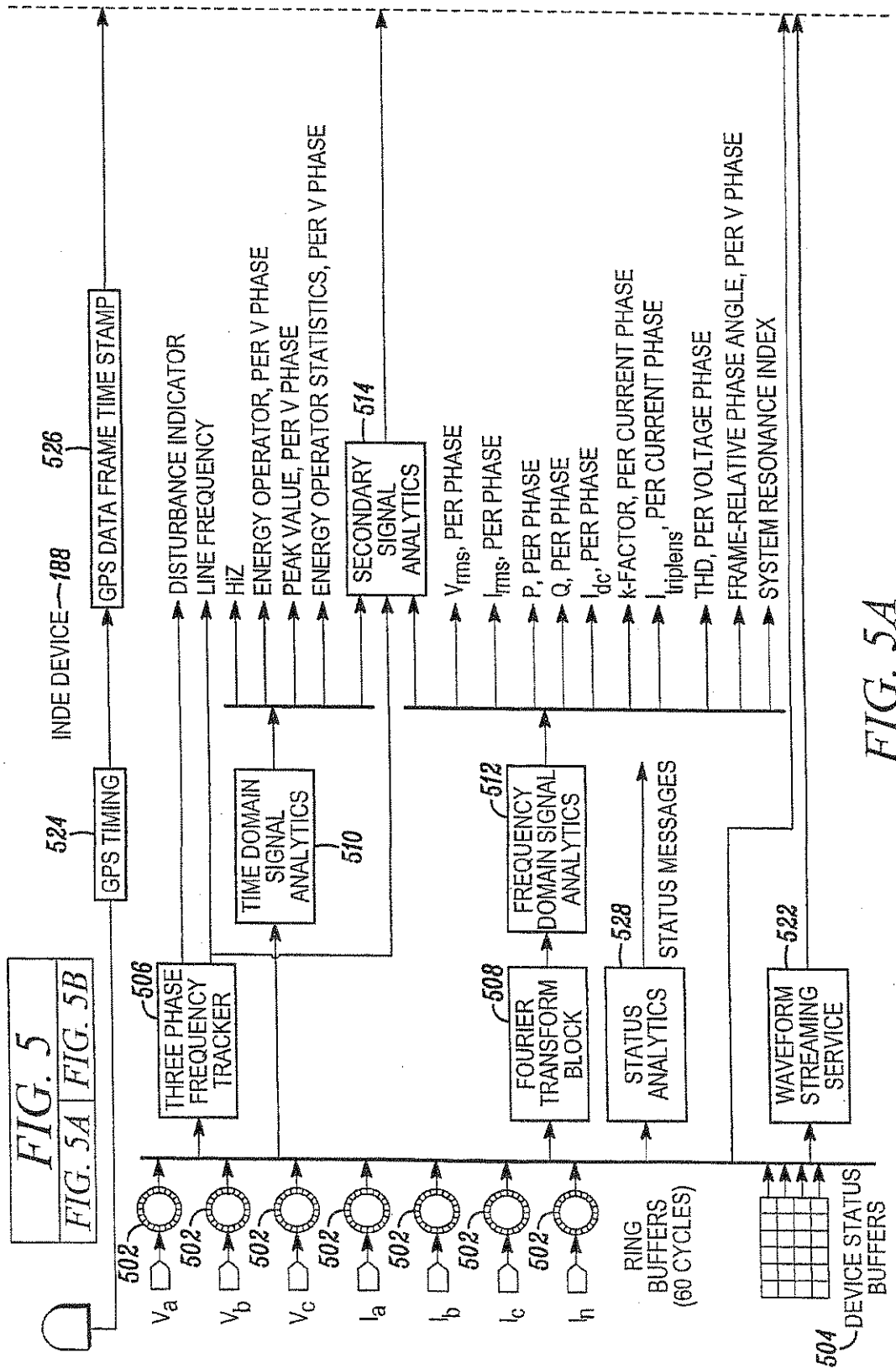


FIG. 5A

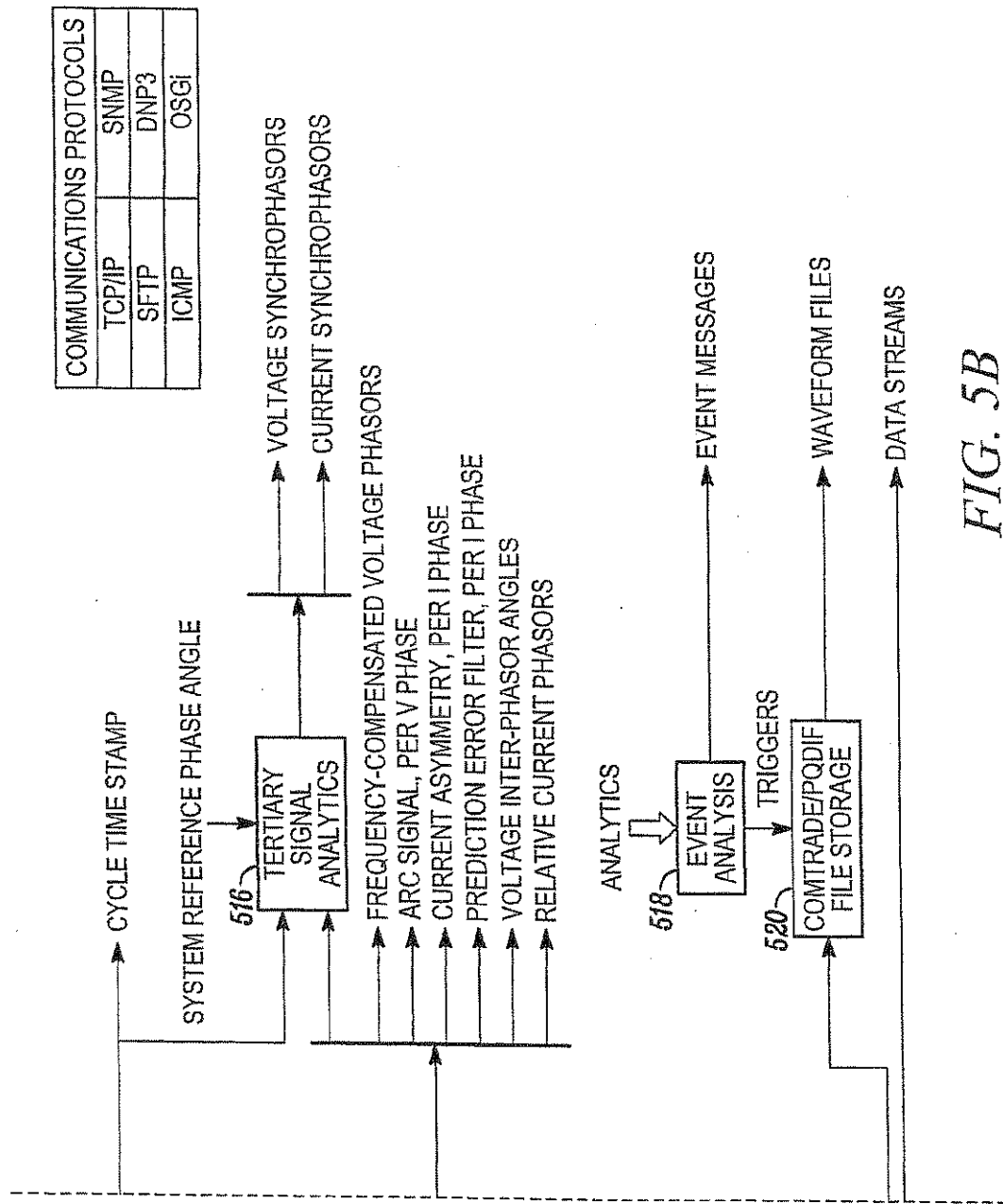
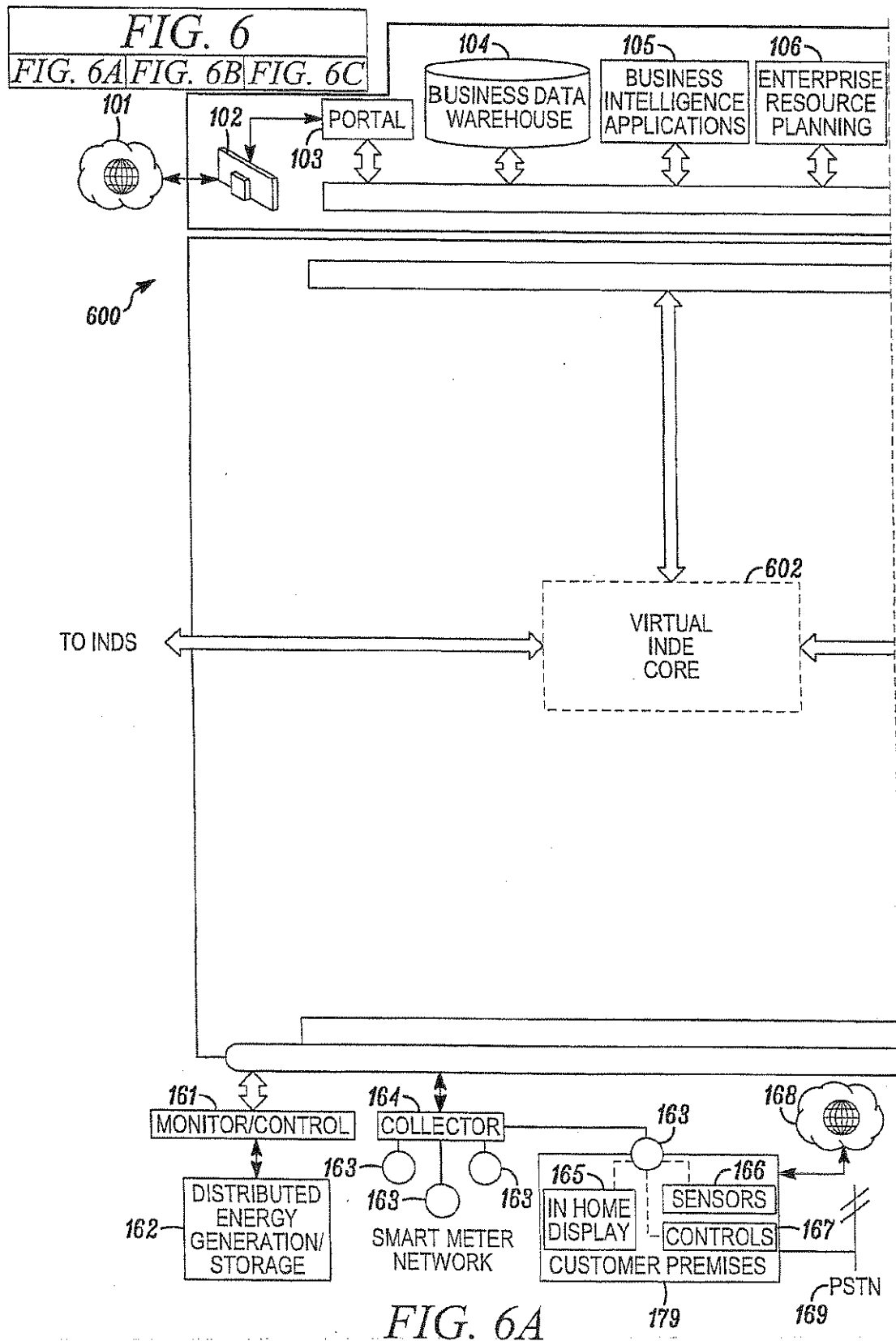
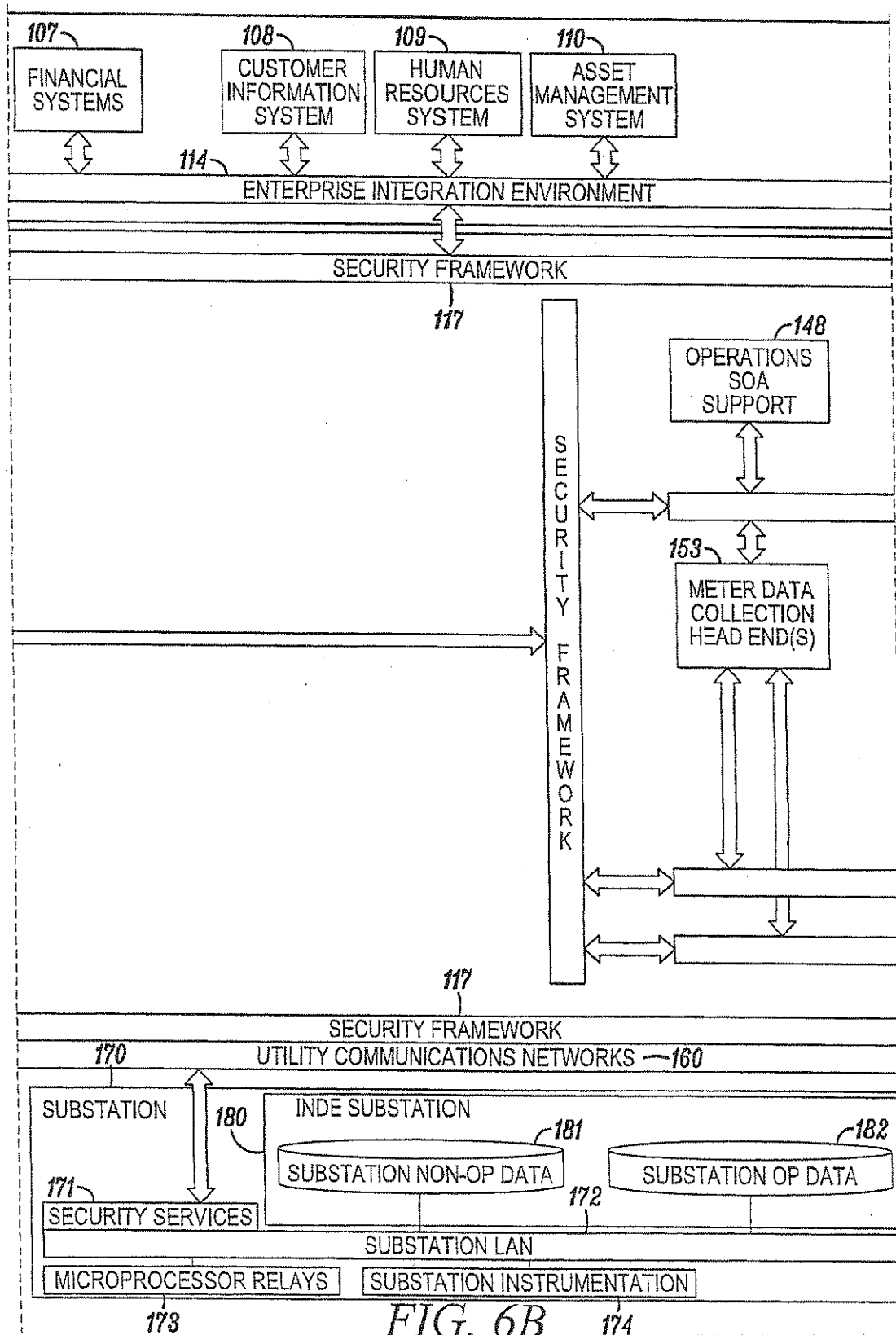


FIG. 5B





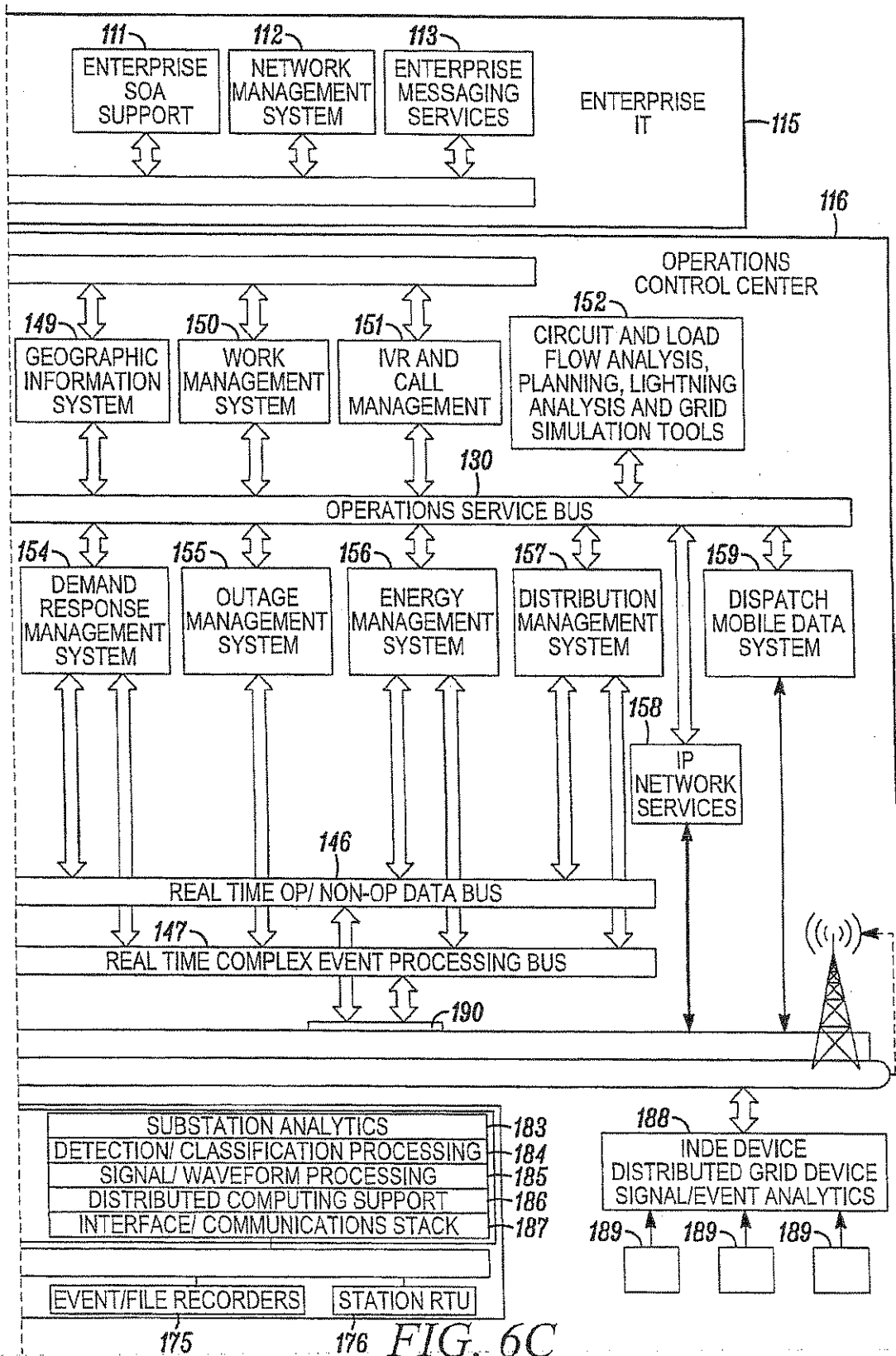


FIG. 6C

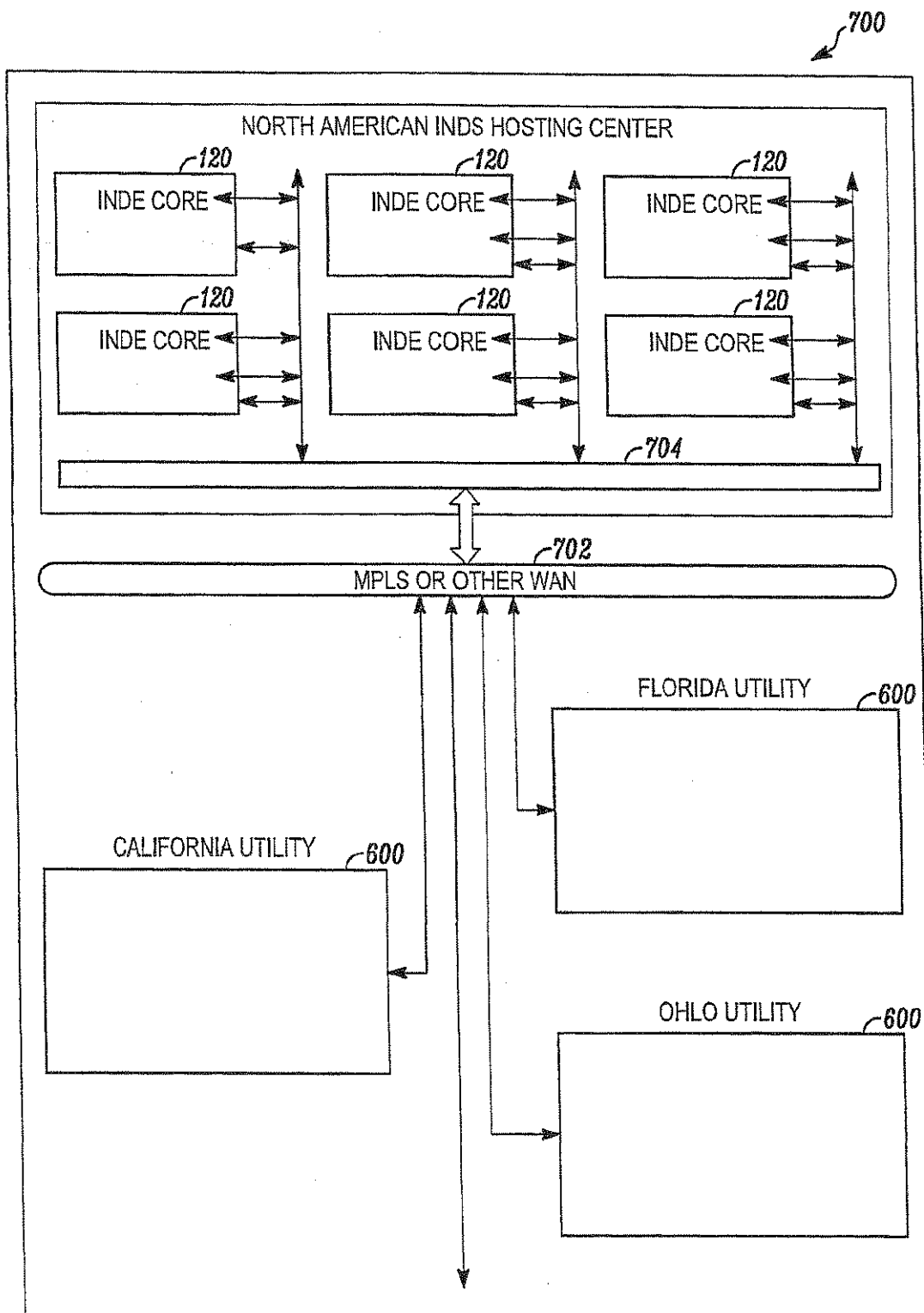


FIG. 7

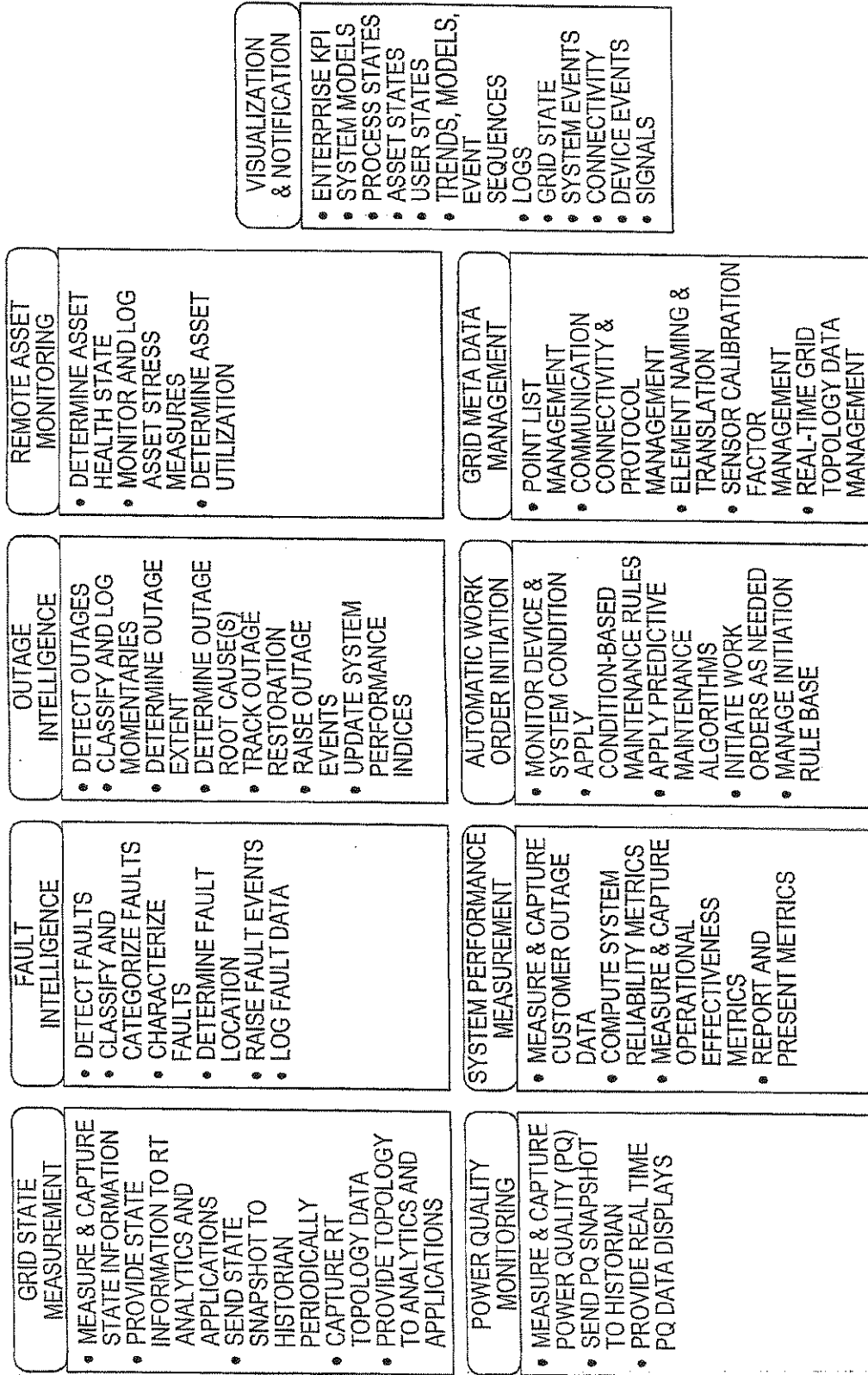
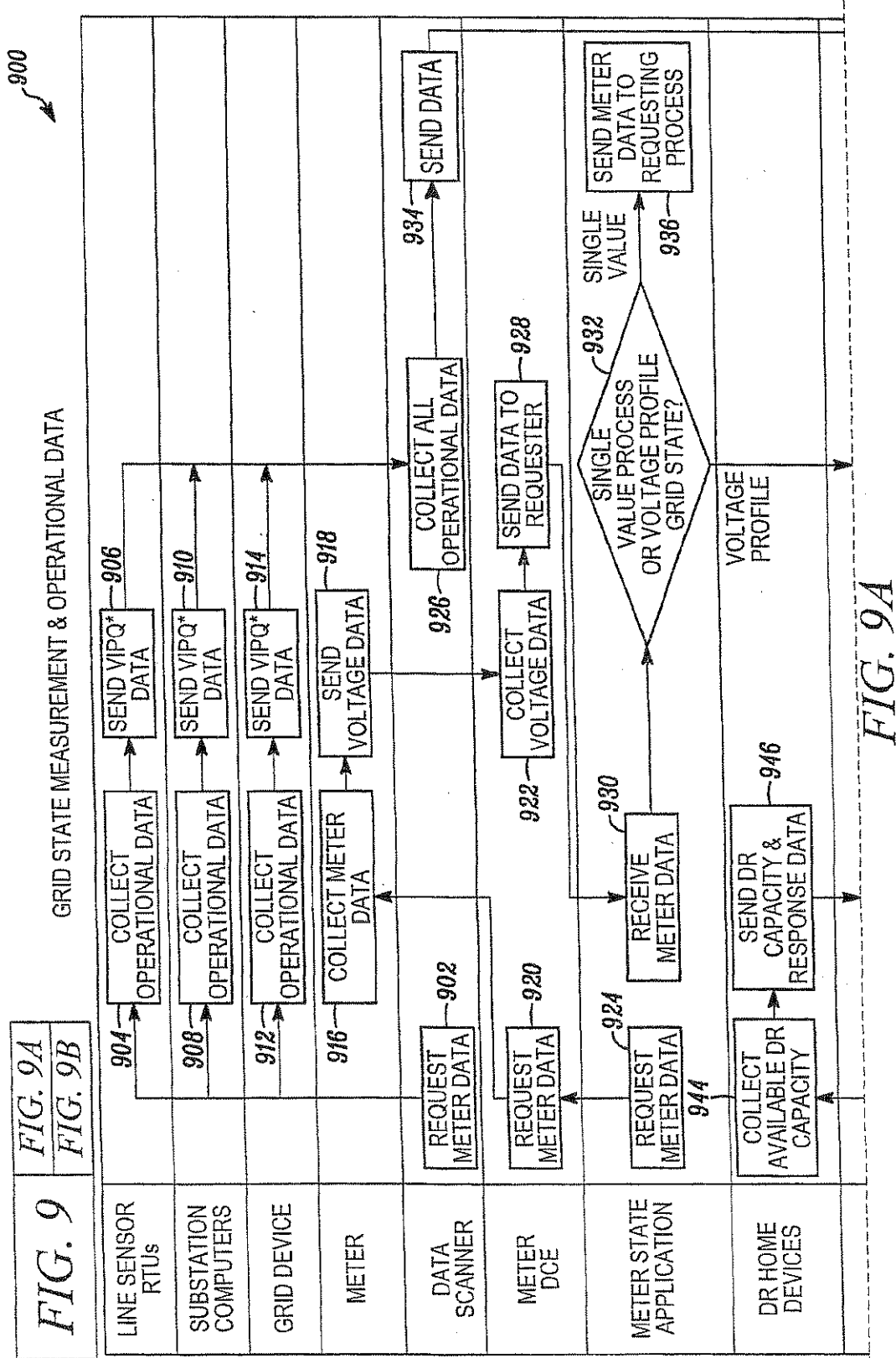
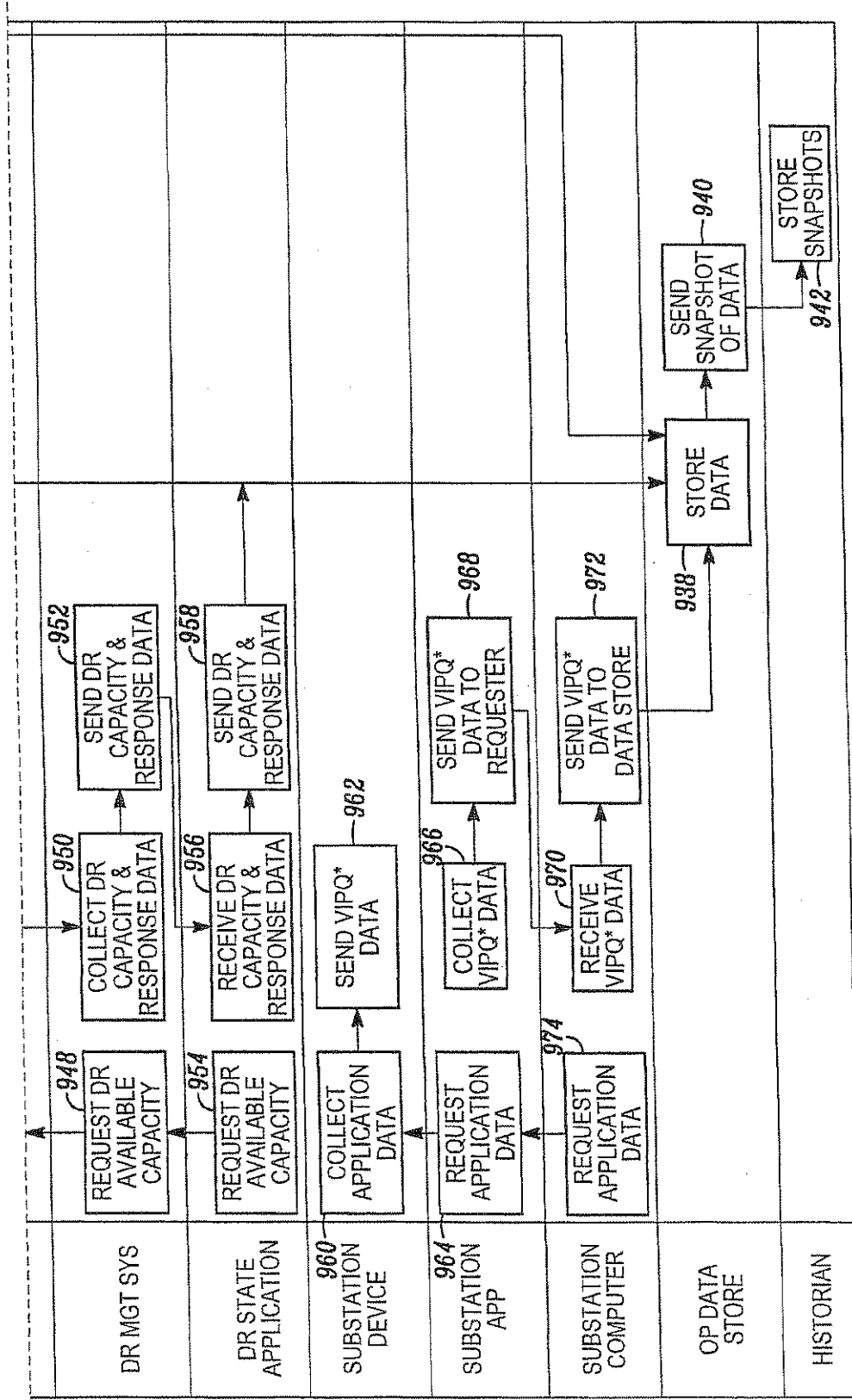


FIG. 8





* V = VOLTAGE, I = CURRENT, P = REAL POWER, Q = REACTIVE POWER

FIG. 9B

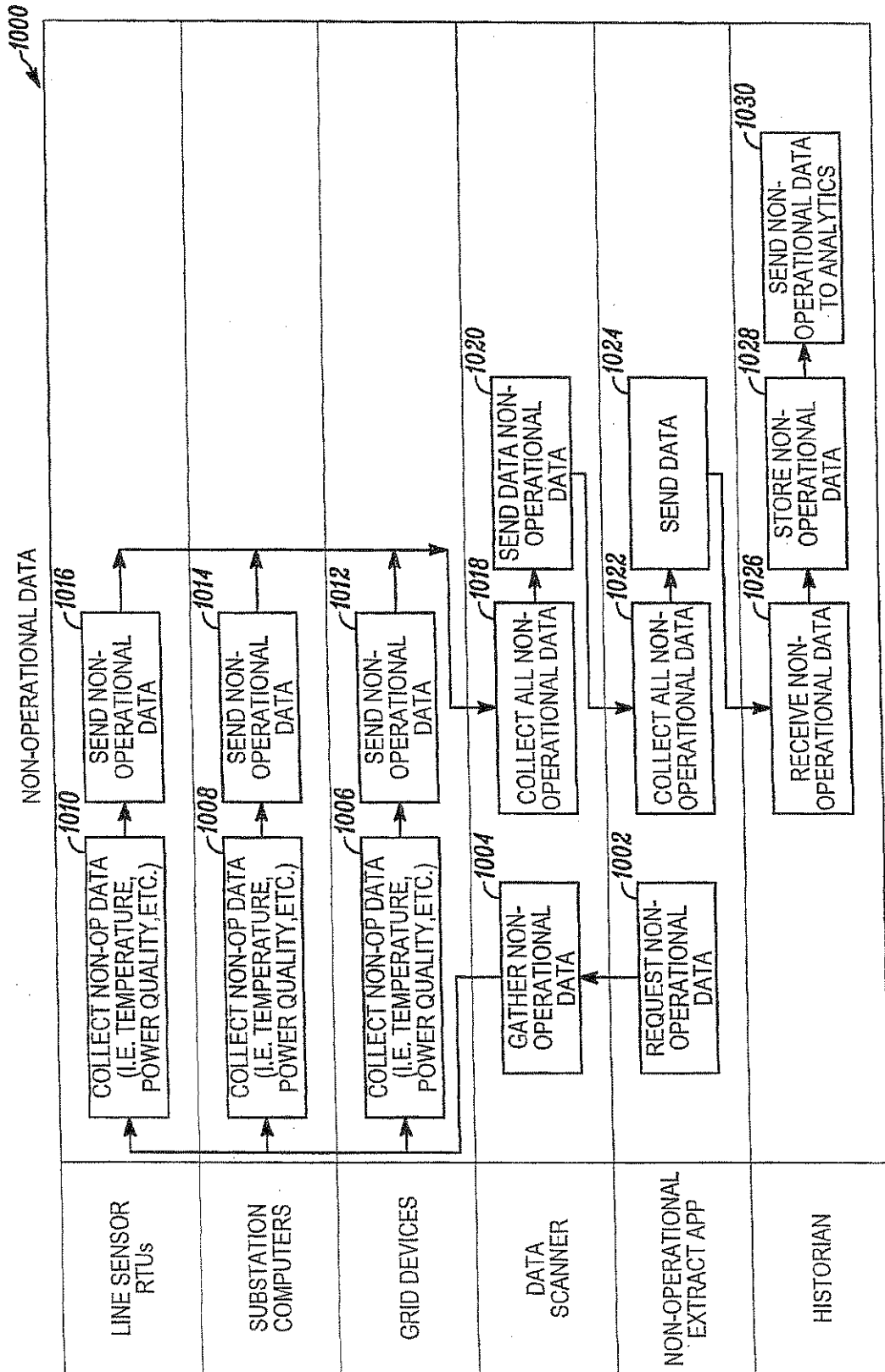


FIG. 10

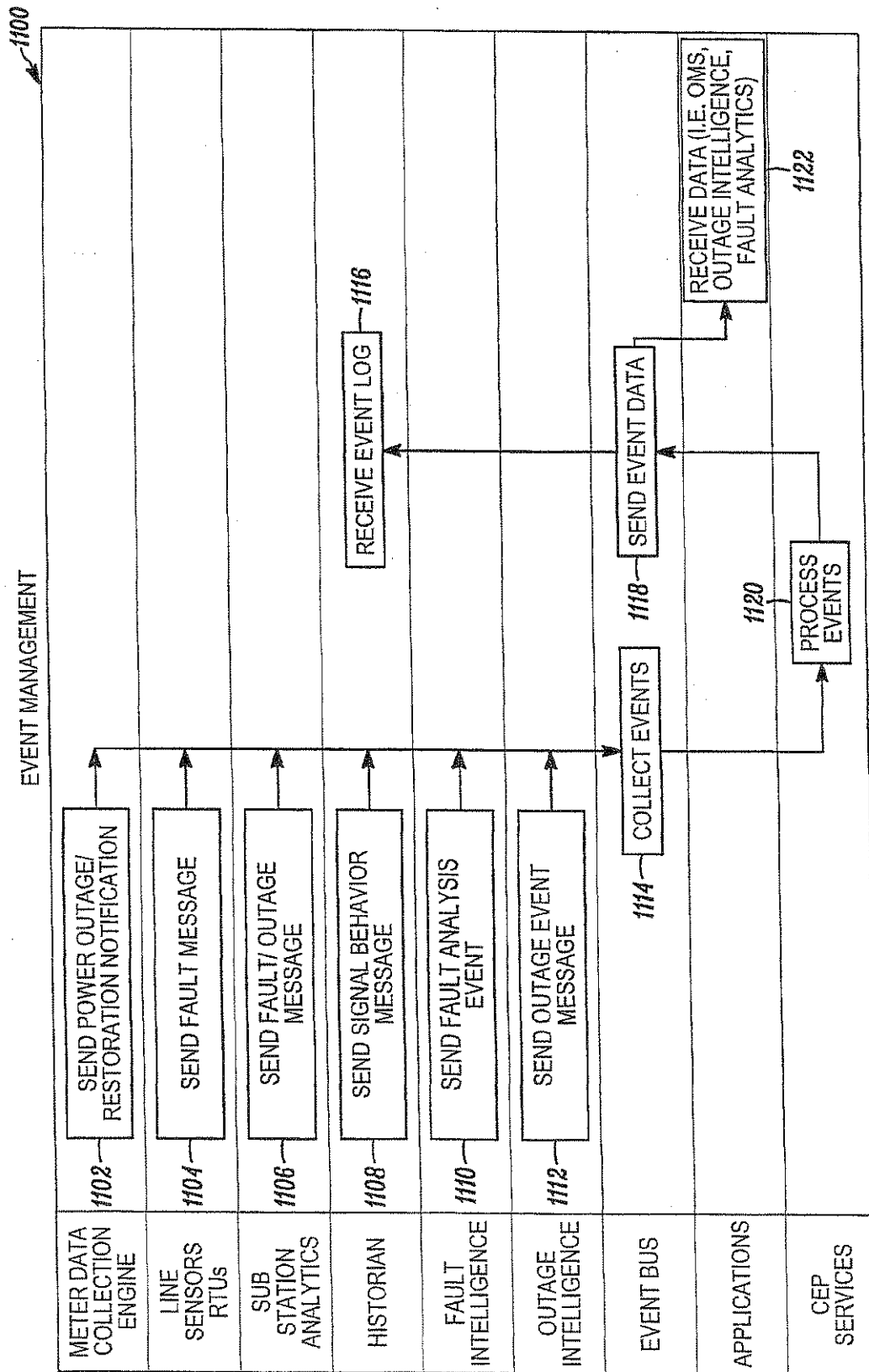


FIG. 11

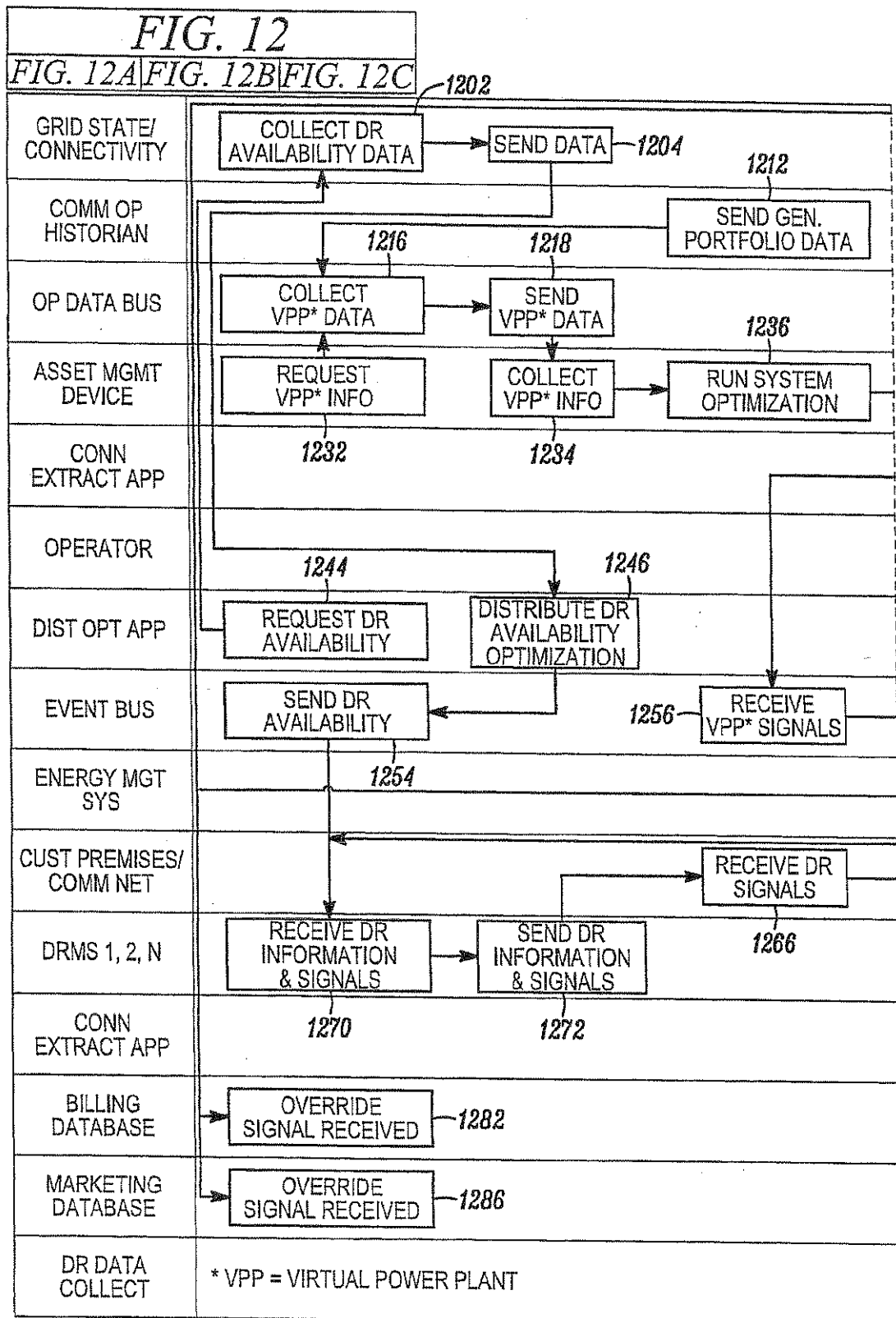


FIG. 12A

DR SIGNALING

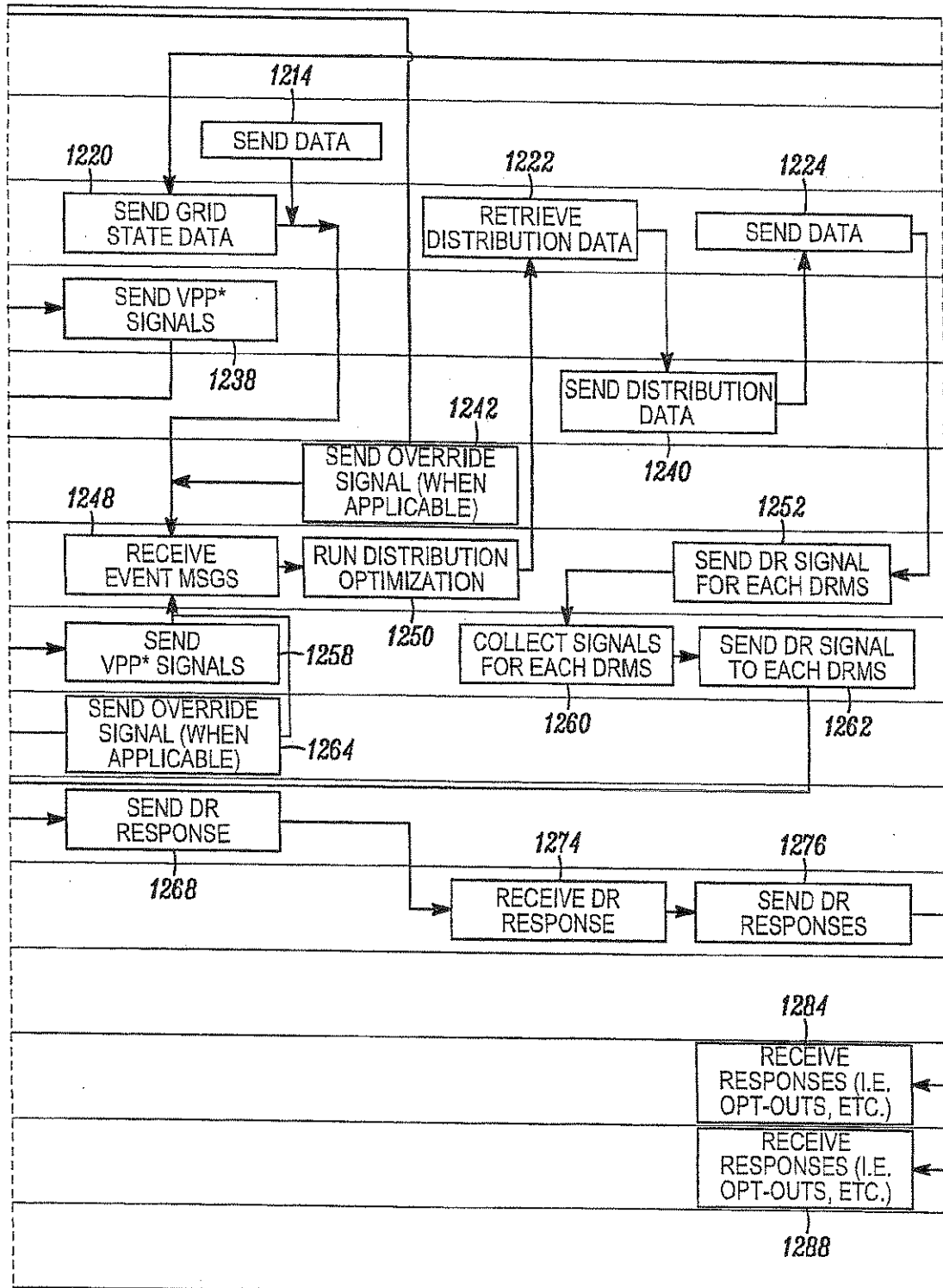


FIG. 12B

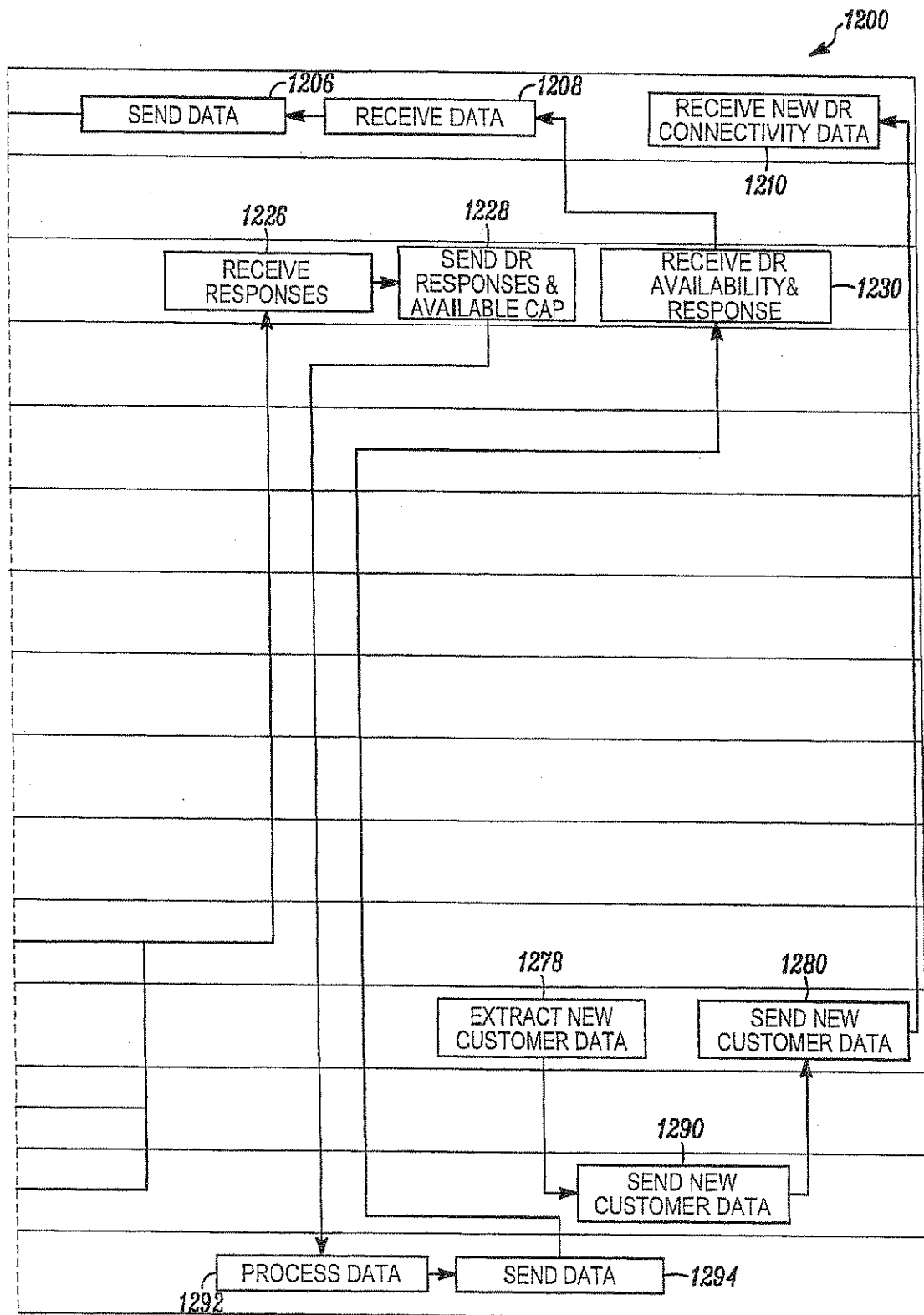
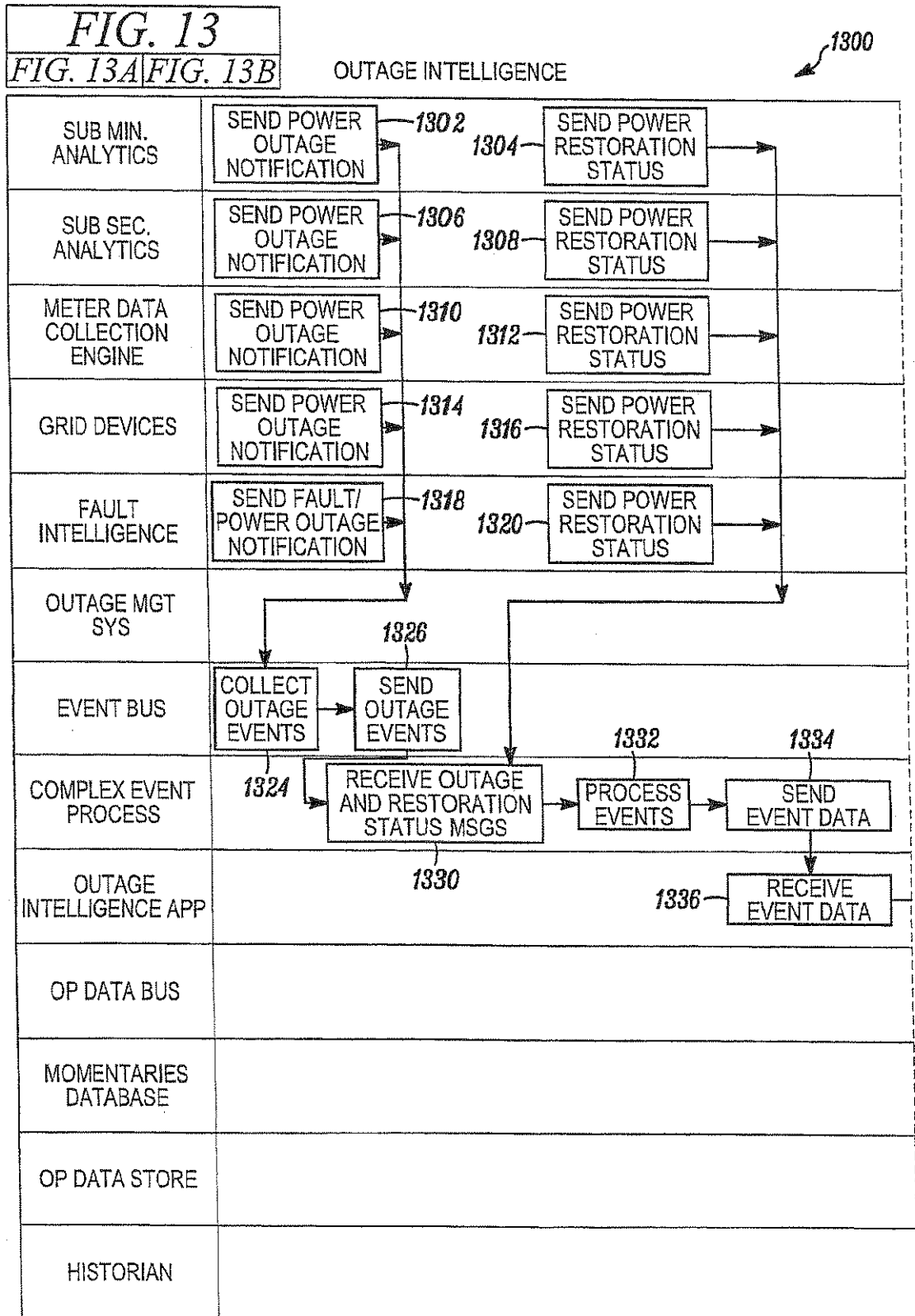


FIG. 12C

**FIG. 13A**

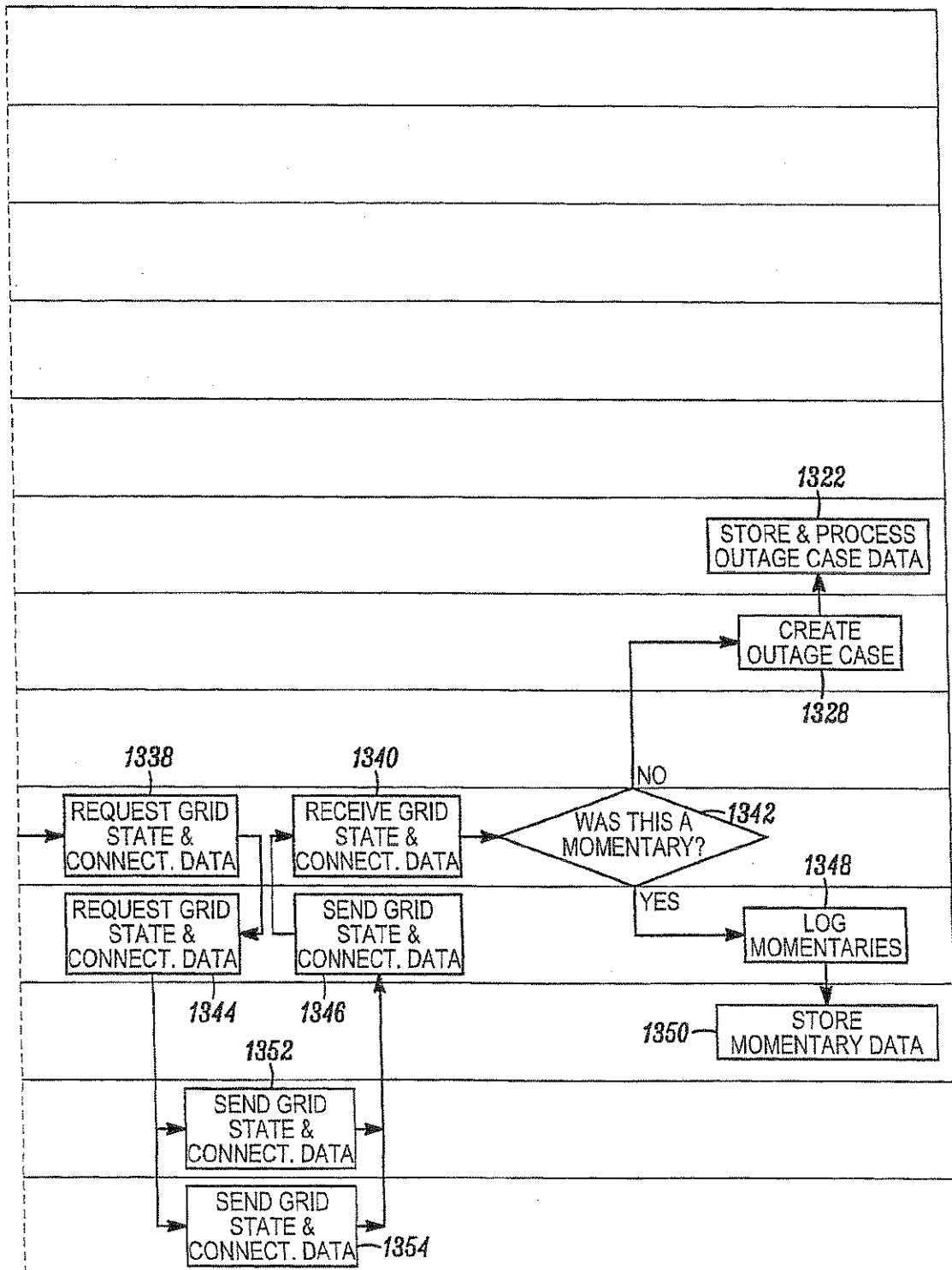
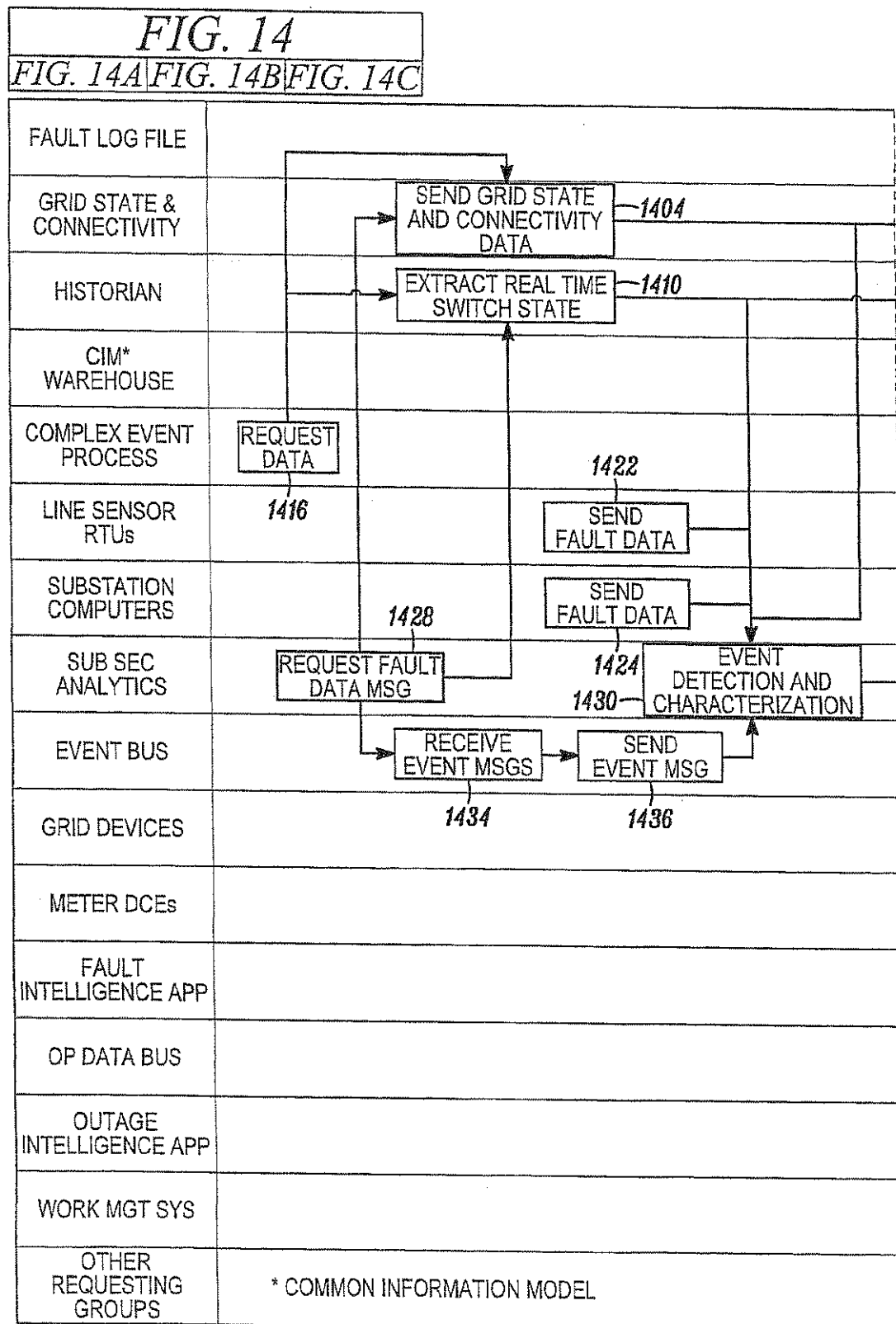


FIG. 13B



FAULT INTELLIGENCE

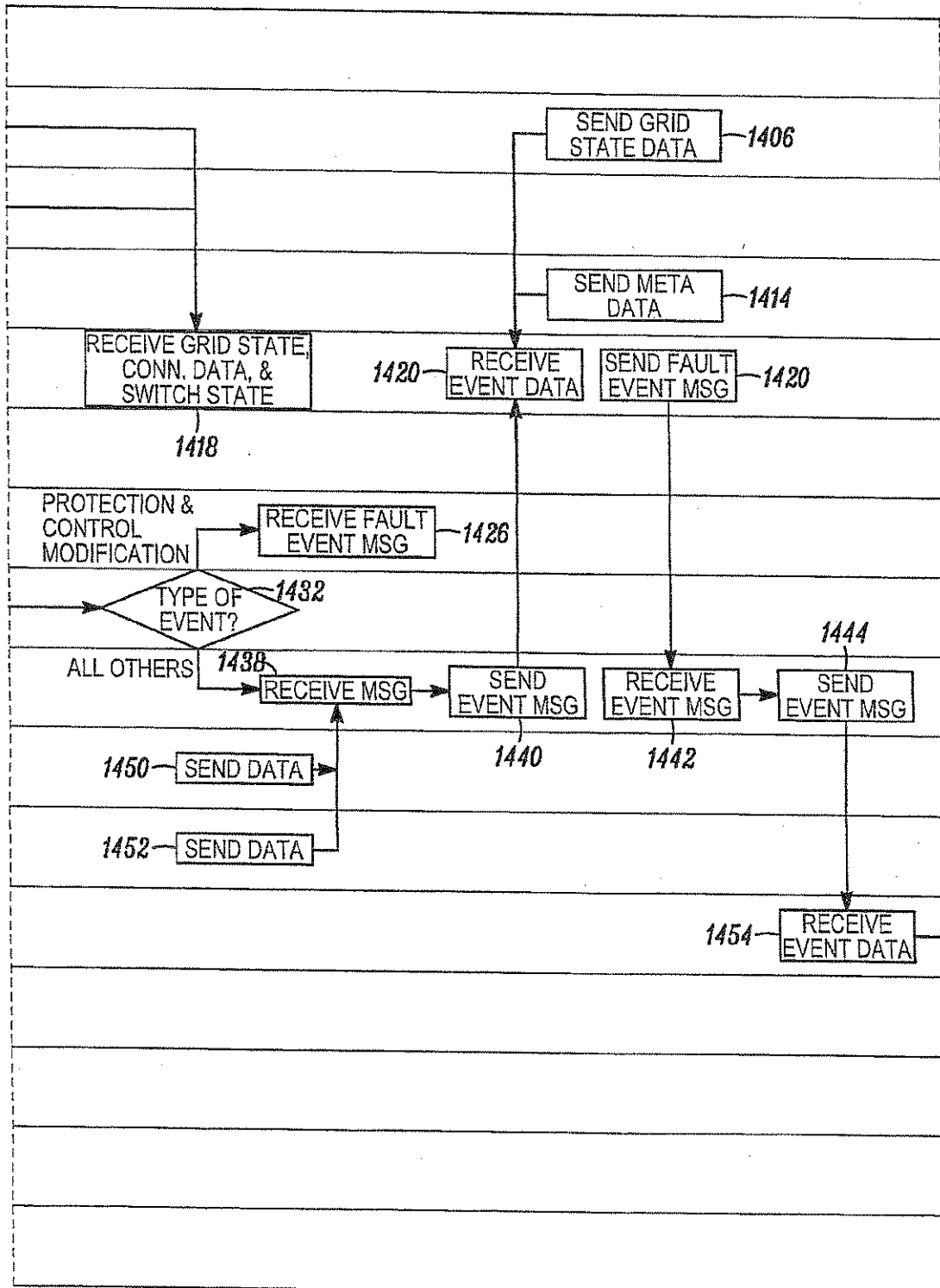


FIG. 14B

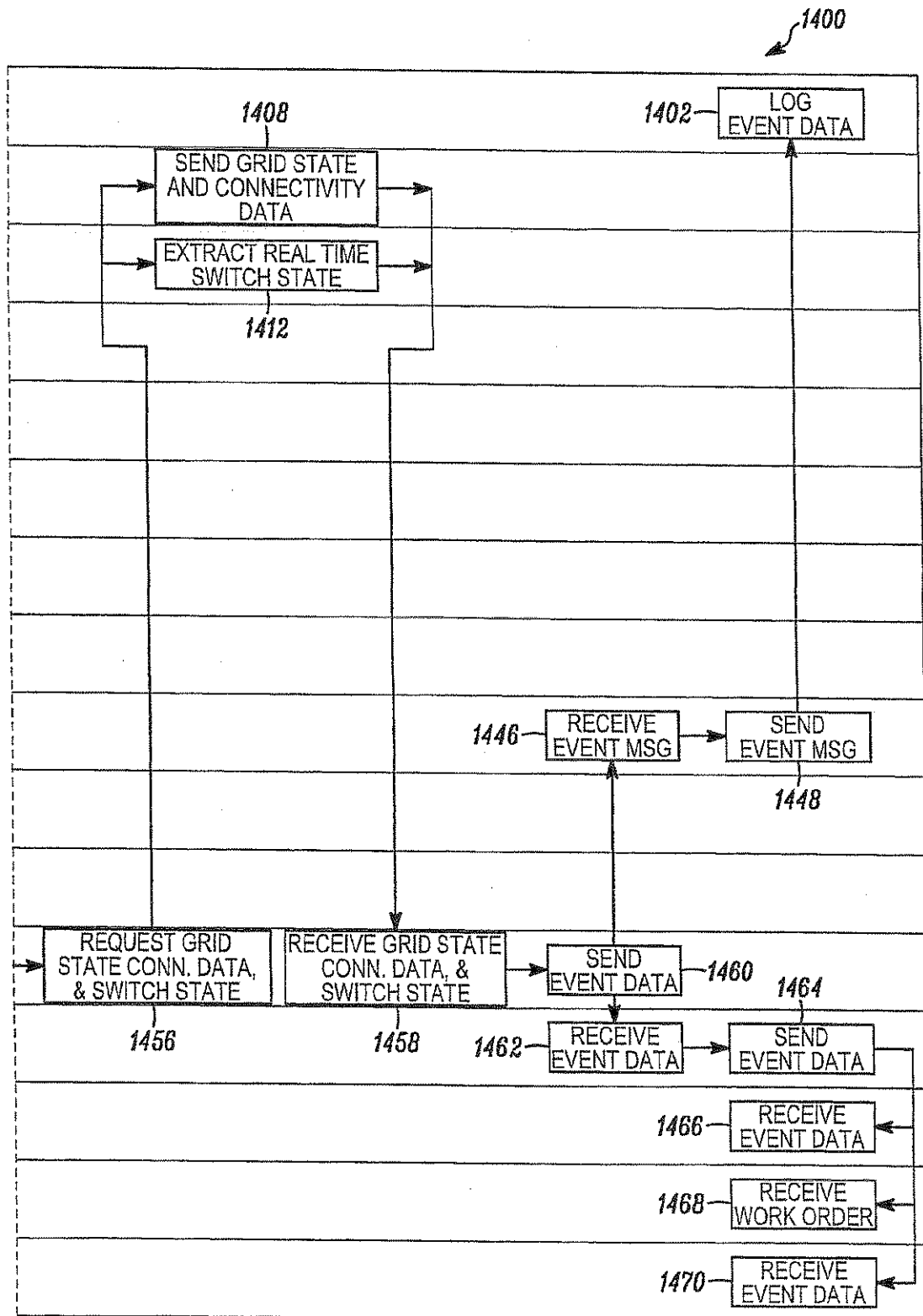


FIG. 14C

FIG. 15
FIG. 15A/FIG. 15B

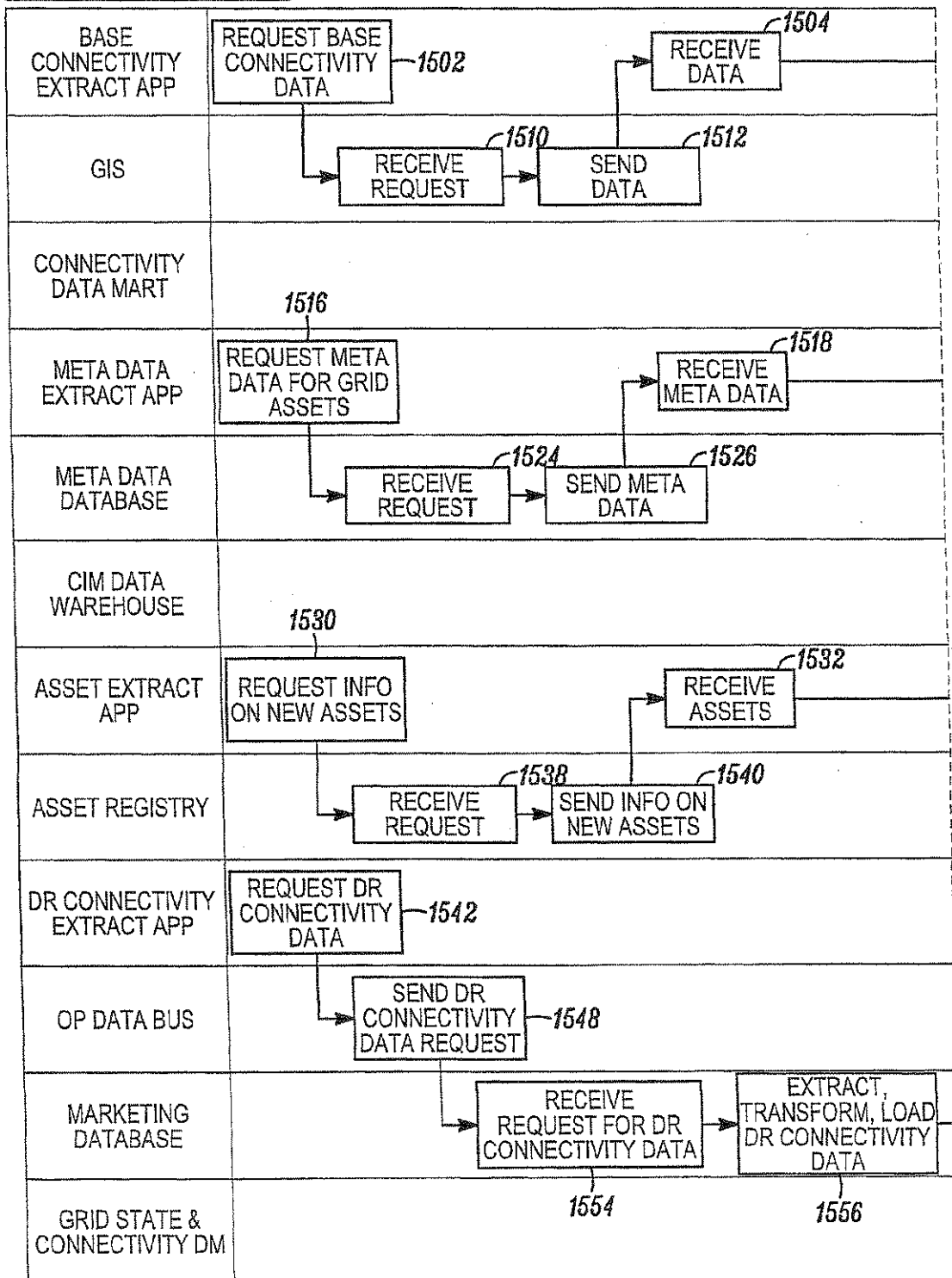


FIG. 15A

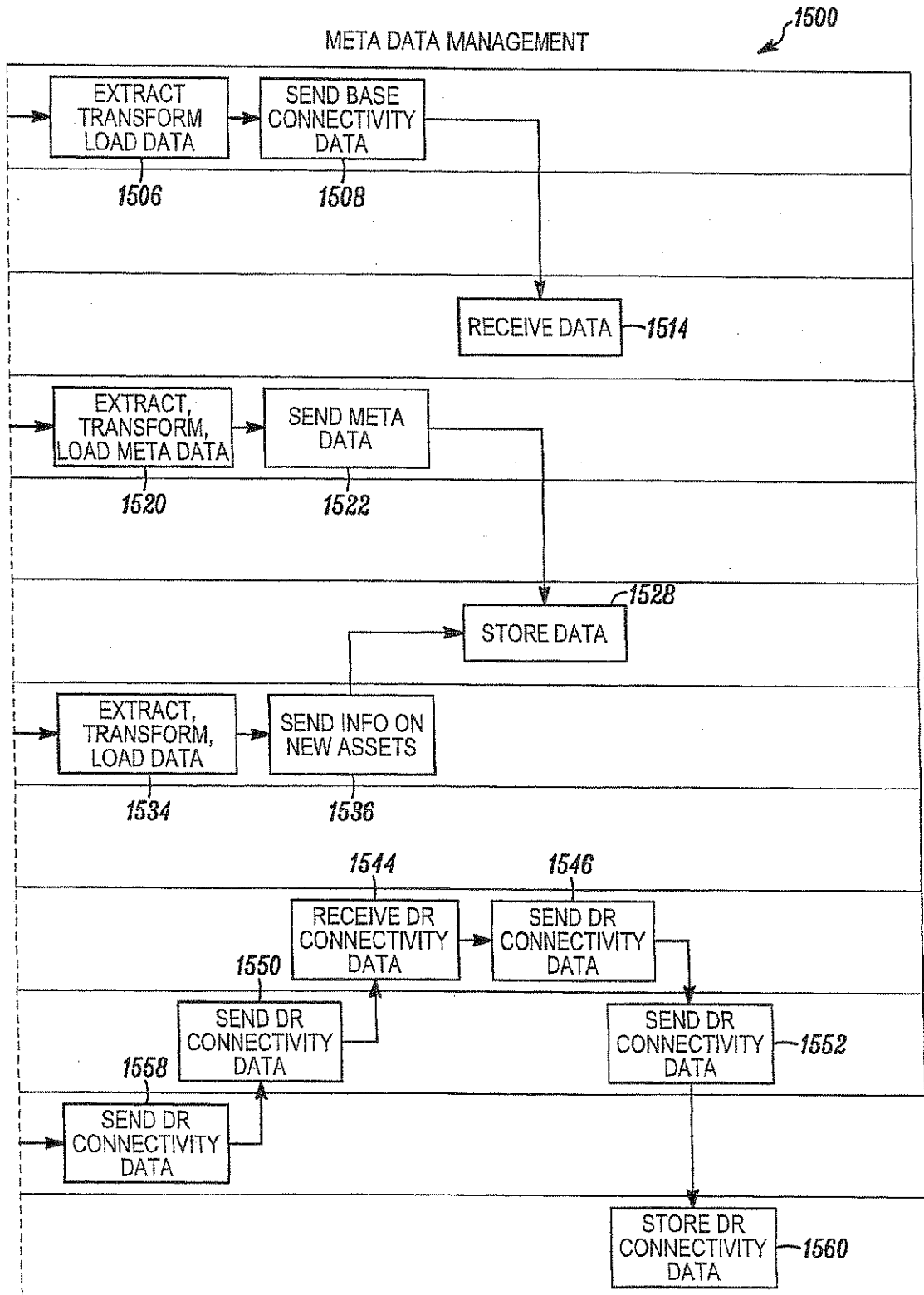


FIG. 15B

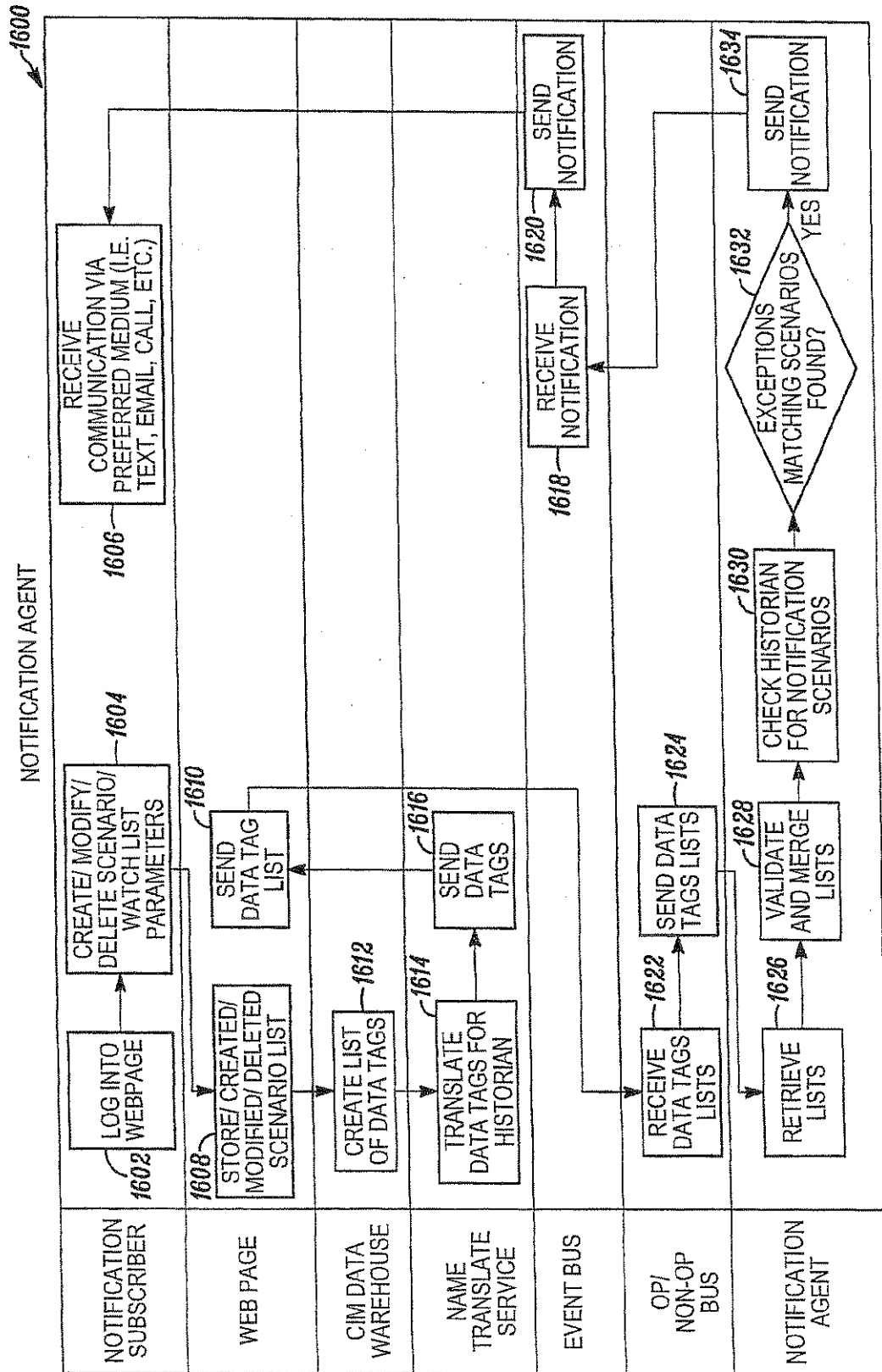


FIG. 16

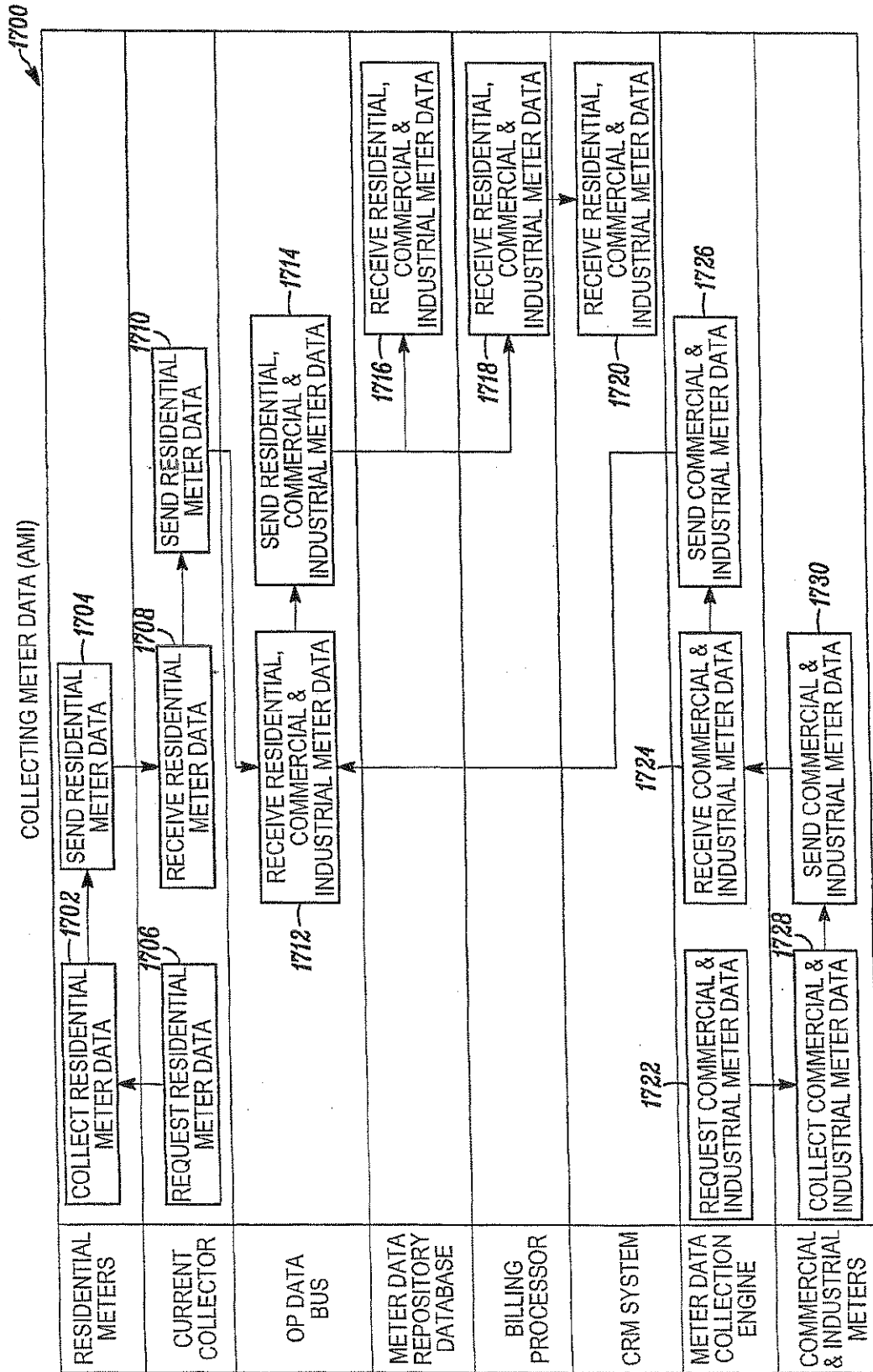
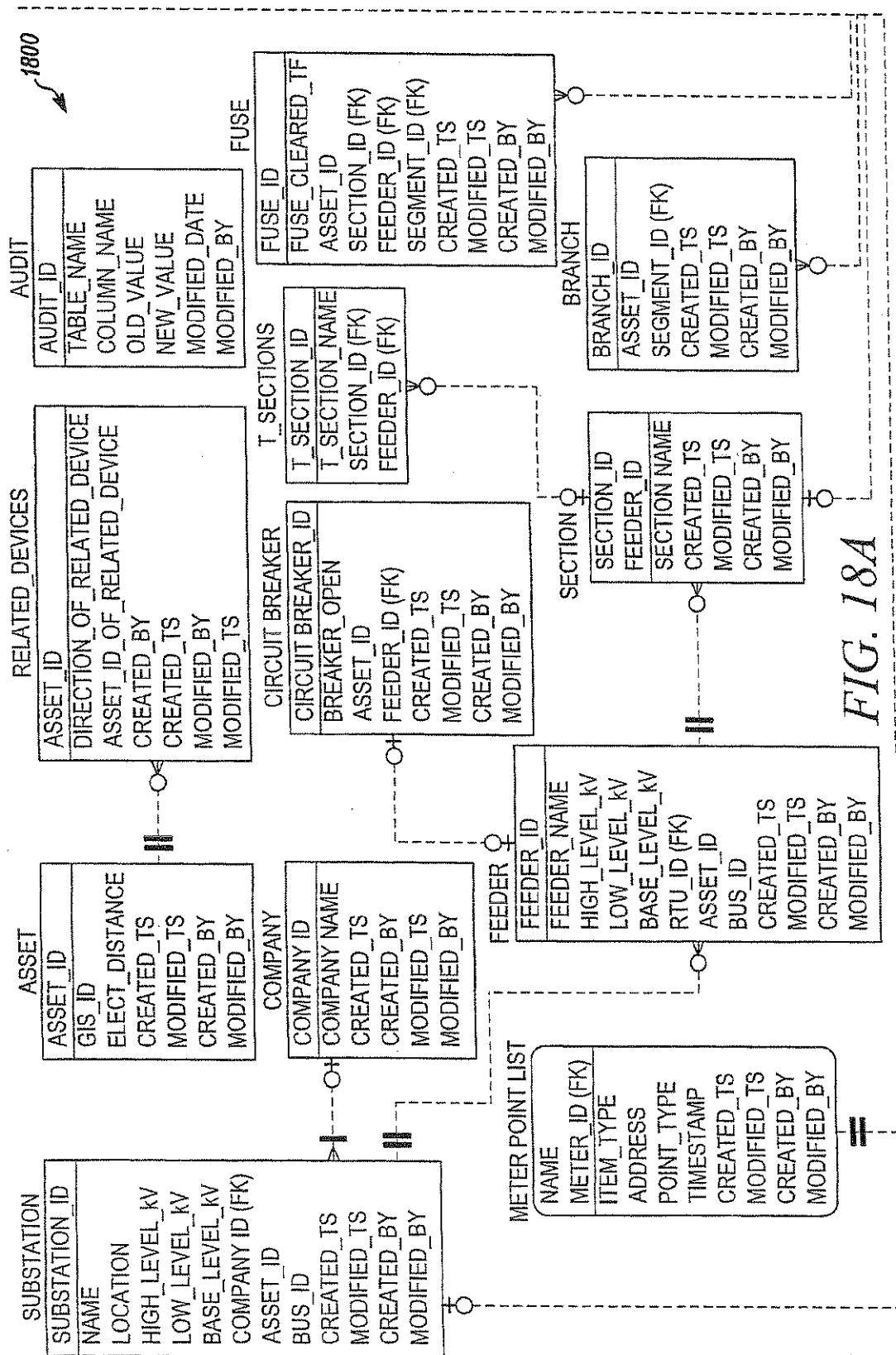
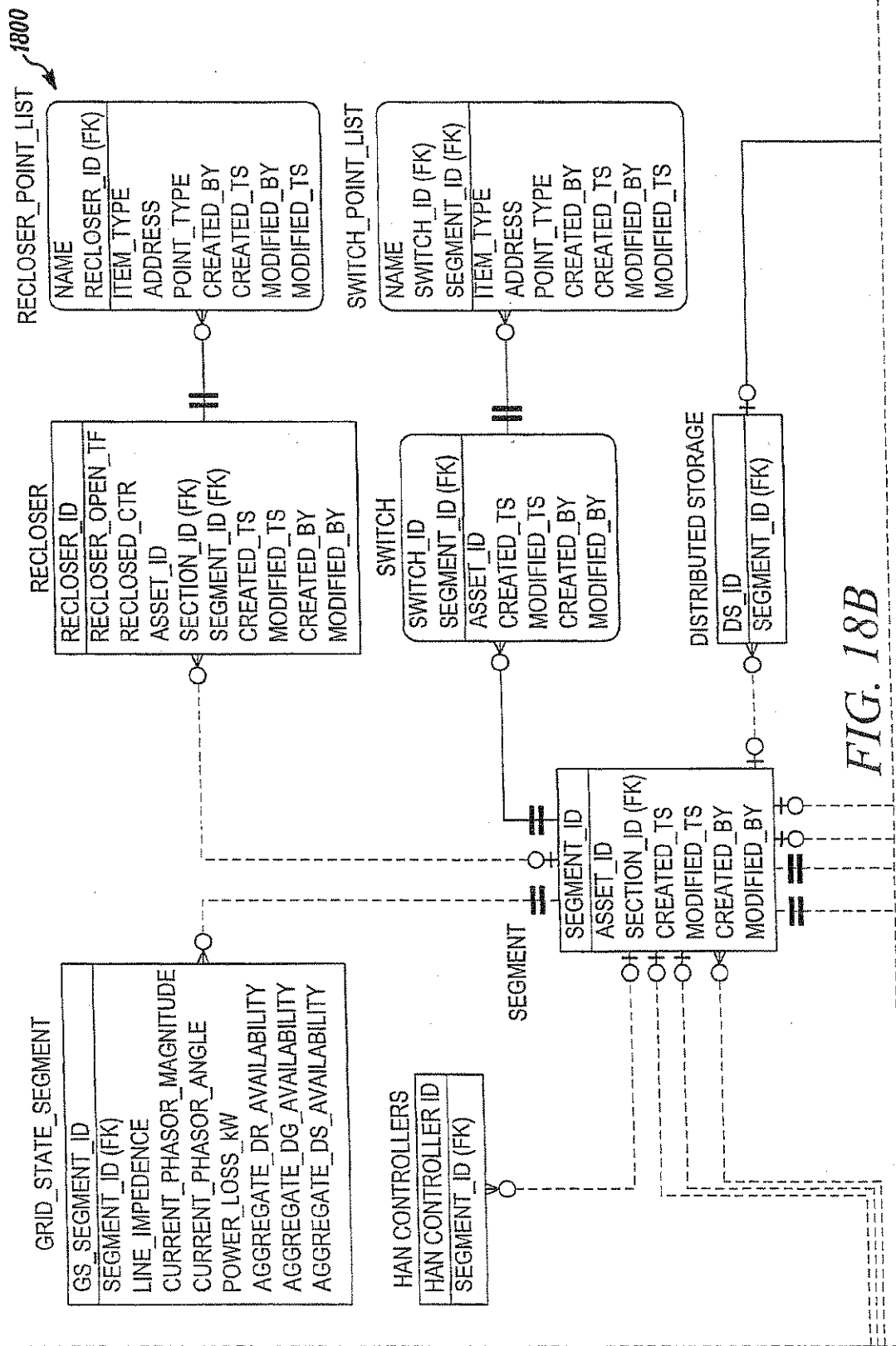


FIG. 17





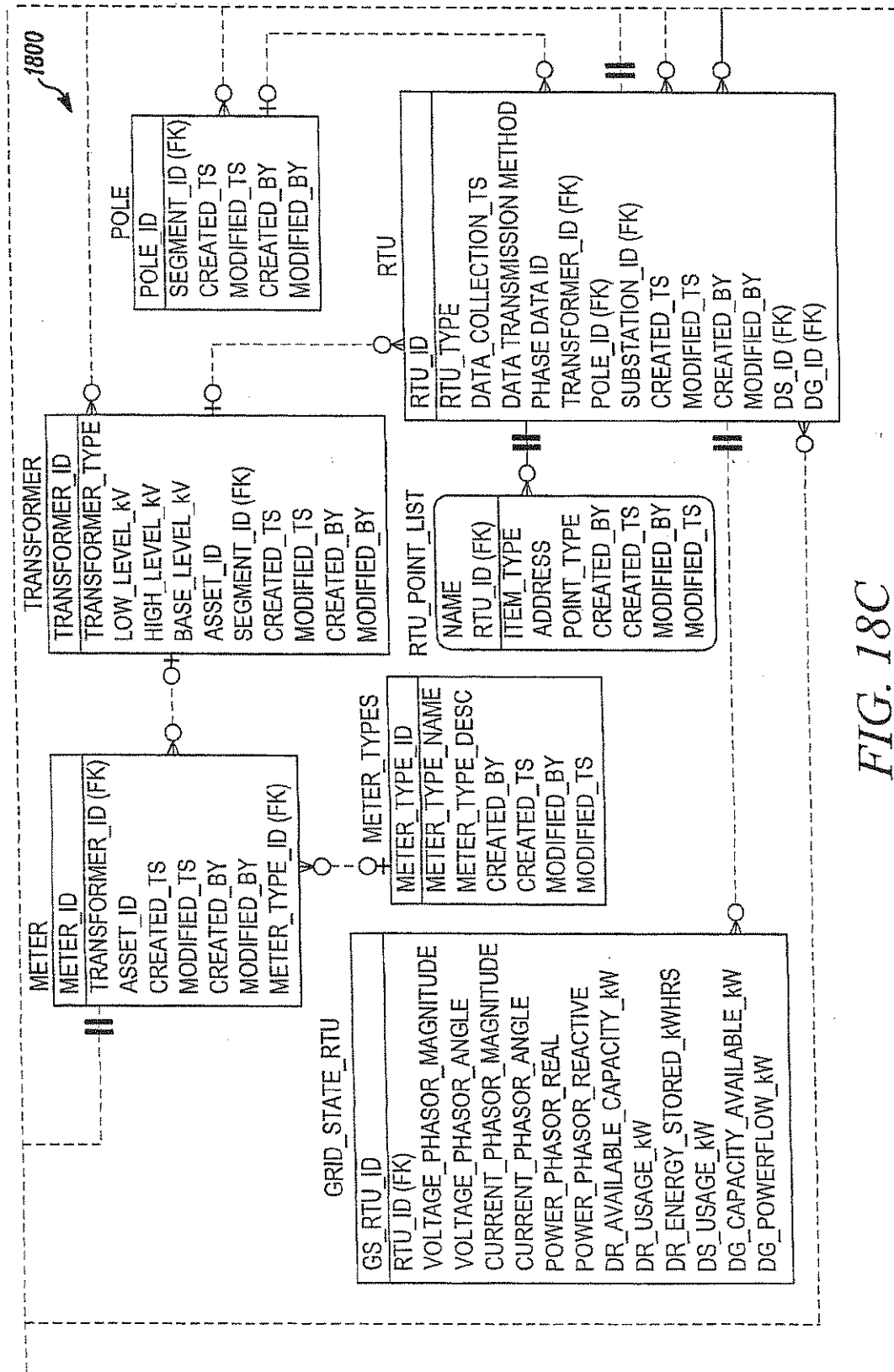


FIG. 18C

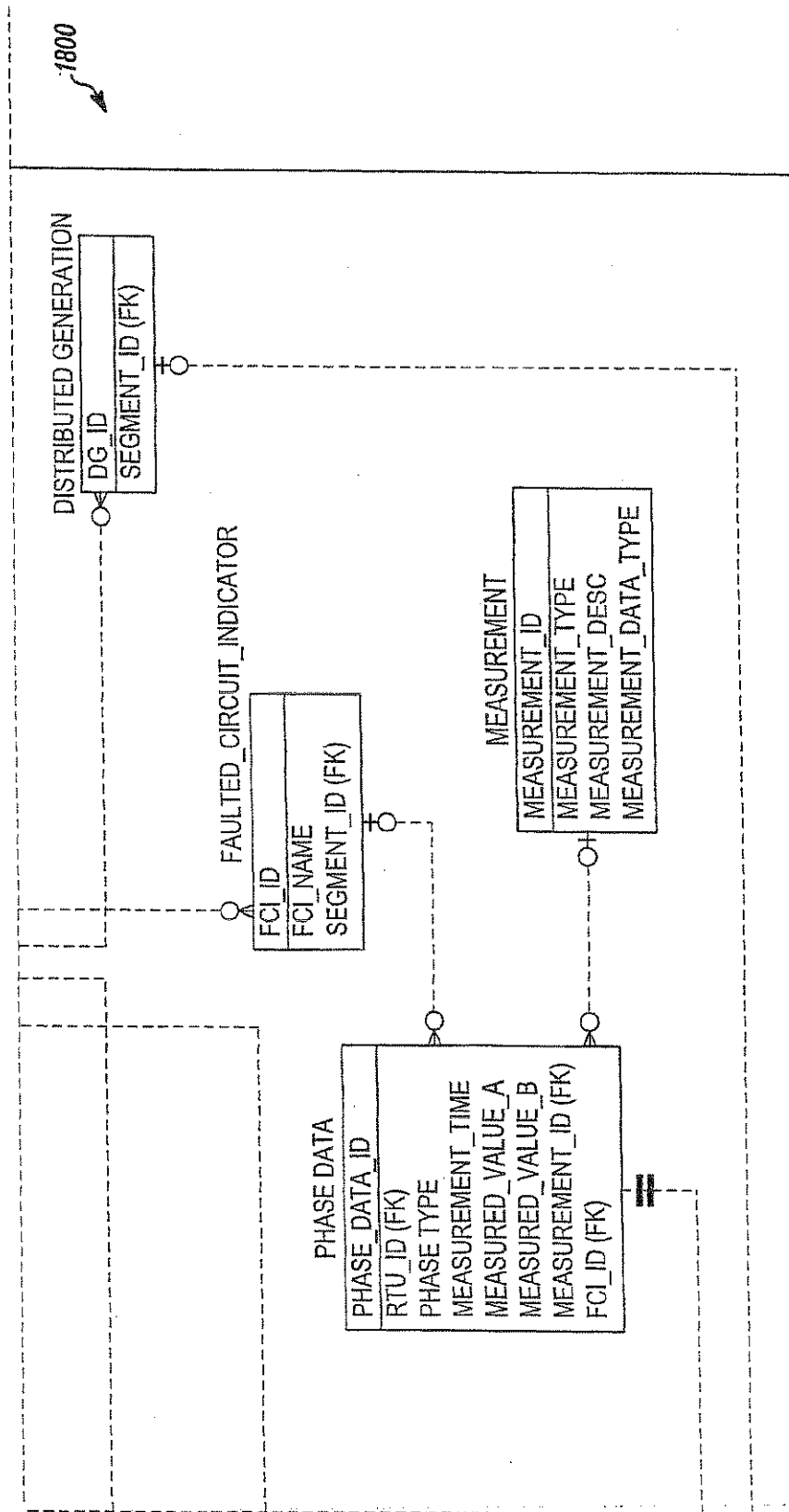


FIG. 18D

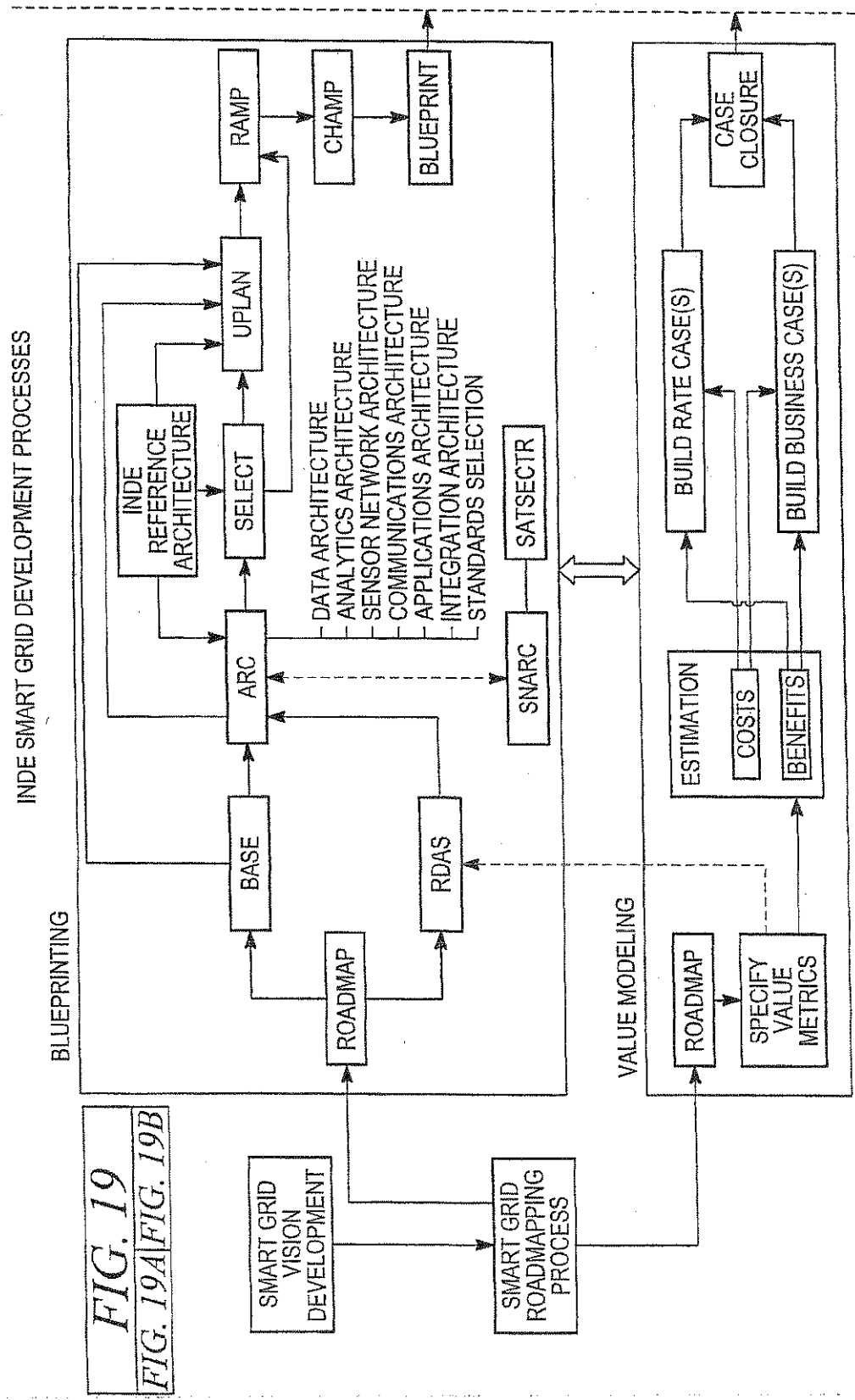


FIG. 19A

BLUEPRINT PROCESS LIST

BASE	BASELINE AND SYSTEMS EVALUATION
RDAS	REQUIREMENTS DEFINITION AND ANALYTICS SELECTION
ARC	ARCHITECTURE CONFIGURATION
SELECT	SOLUTION ELEMENT EVALUATION AND COMPONENTS TEMPLATE
UPLAN	UPGRADE PLANNING FOR APPLICATIONS AND NETWORKS
RAMP	RISK ASSESSMENT AND MANAGEMENT PLANNING
CHAMP	CHANGE ANALYSIS AND MANAGEMENT PLANNING
SNARC	SENSOR NETWORK ARCHITECTURE CONFIGURATION
SATSECTR	SENSOR ALLOCATION VIA T-SECTION RECURSION

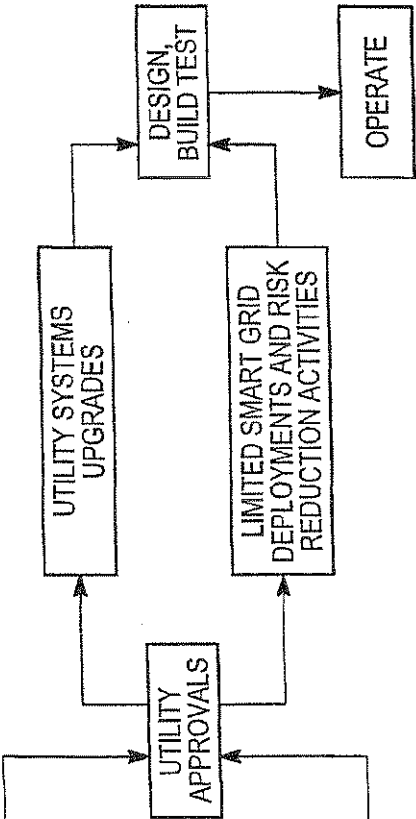


FIG. 19B

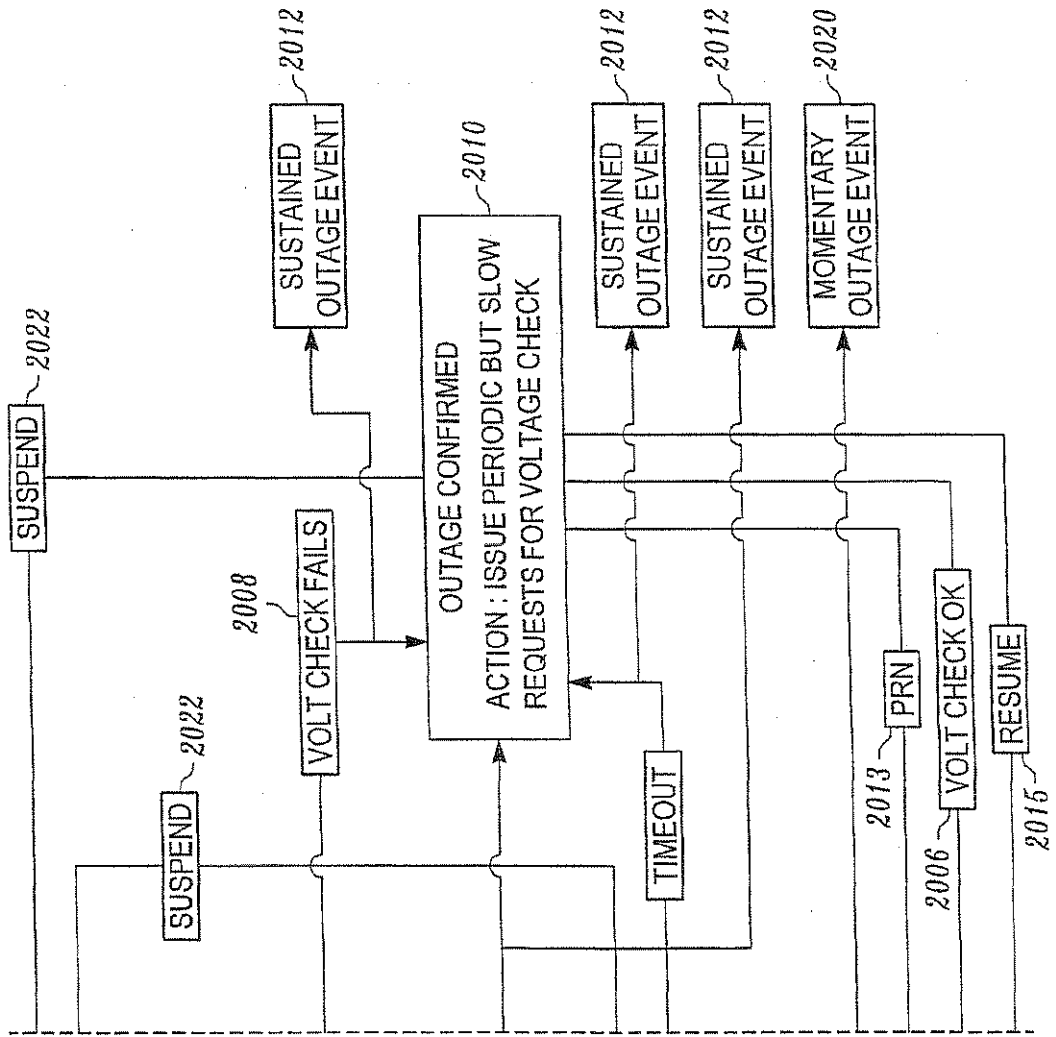
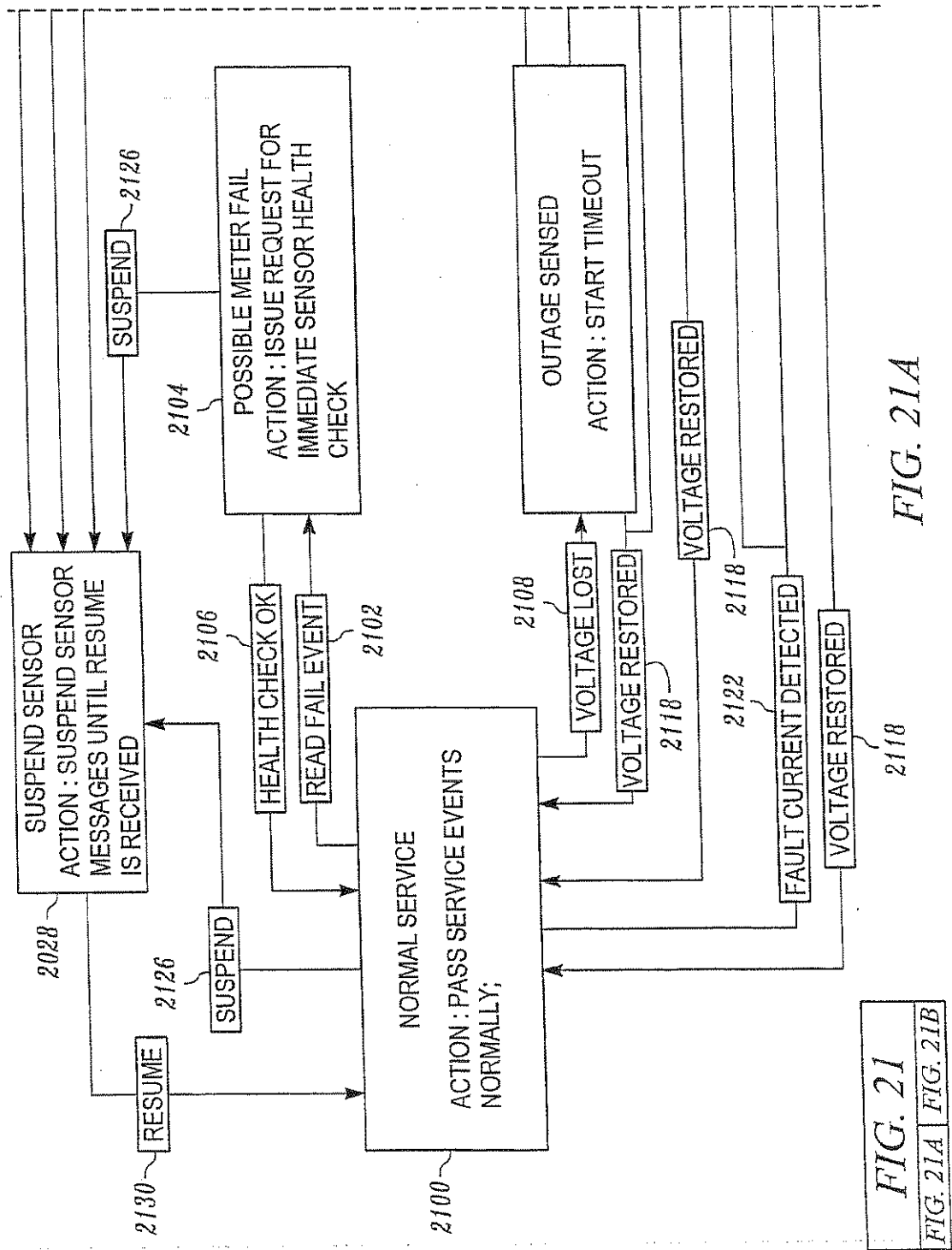
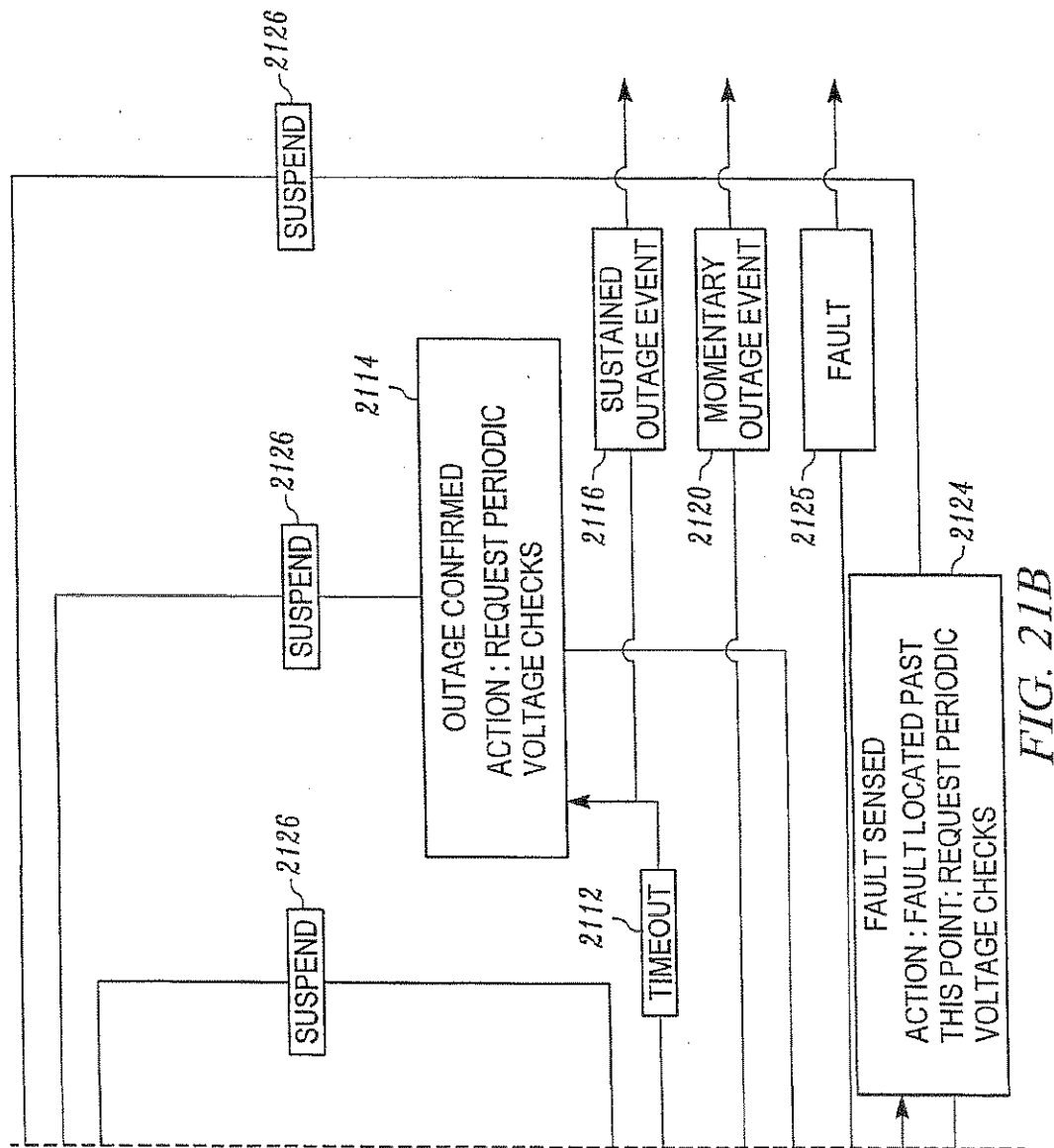


FIG. 20B





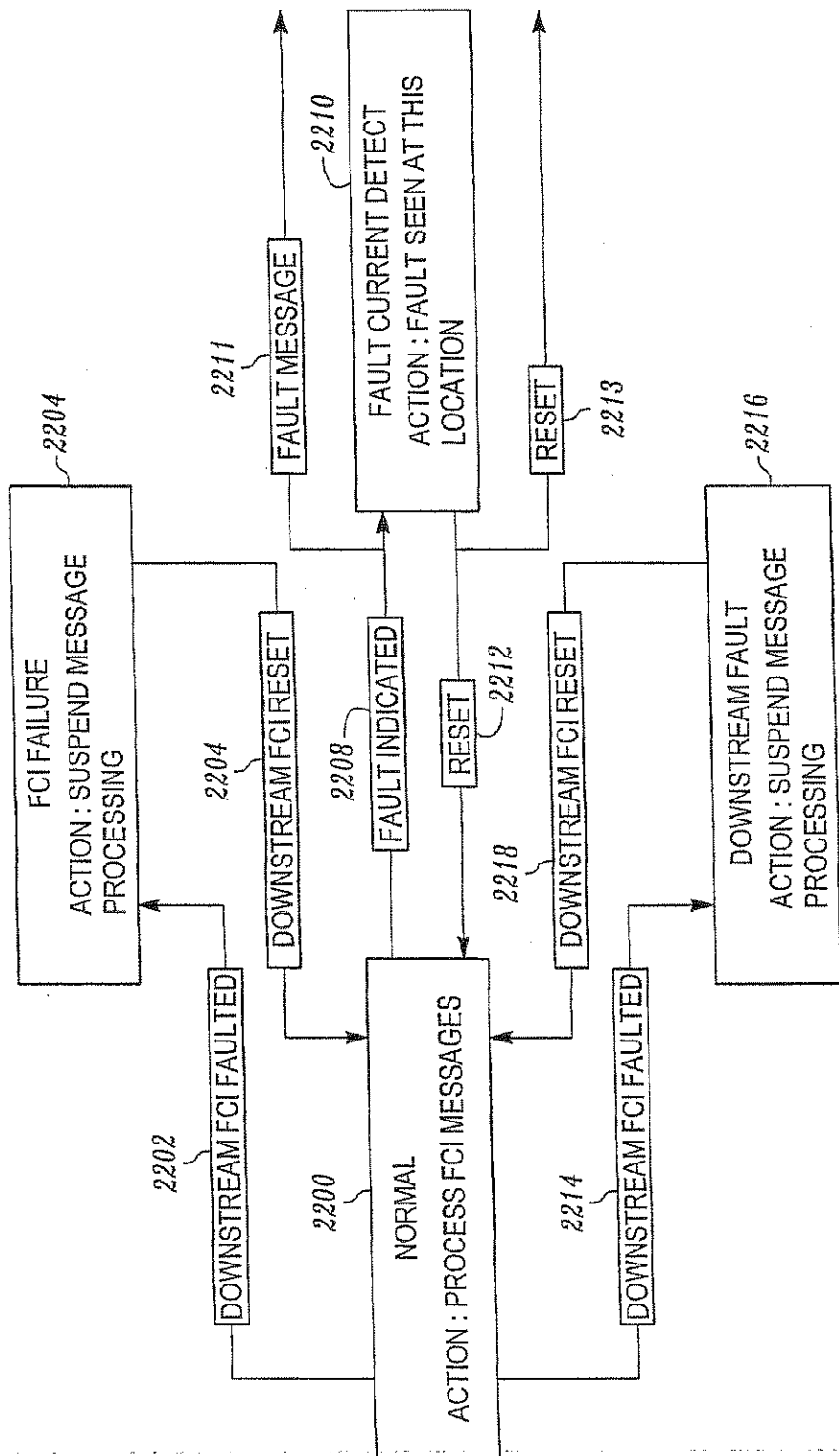


FIG. 22

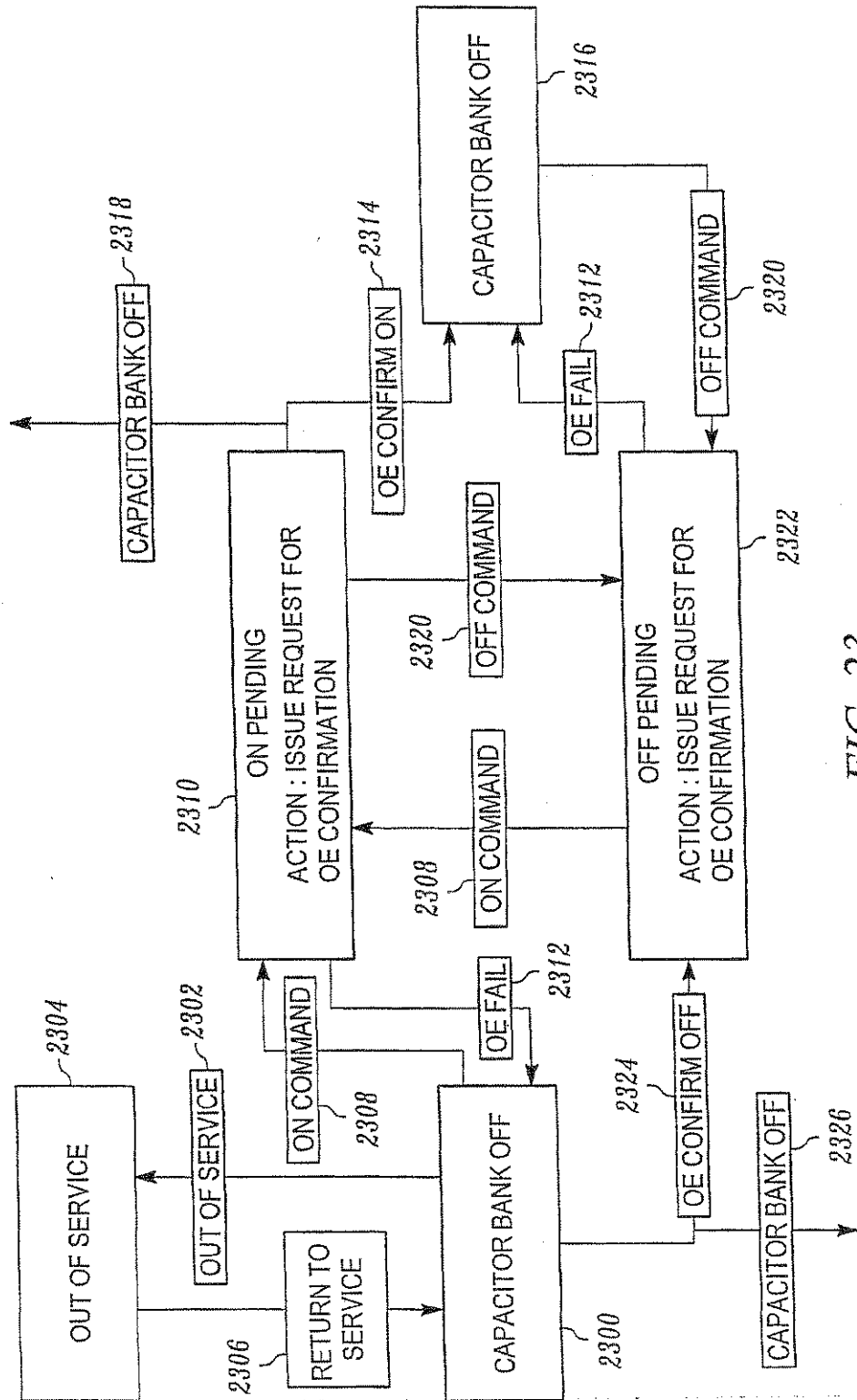


FIG. 23

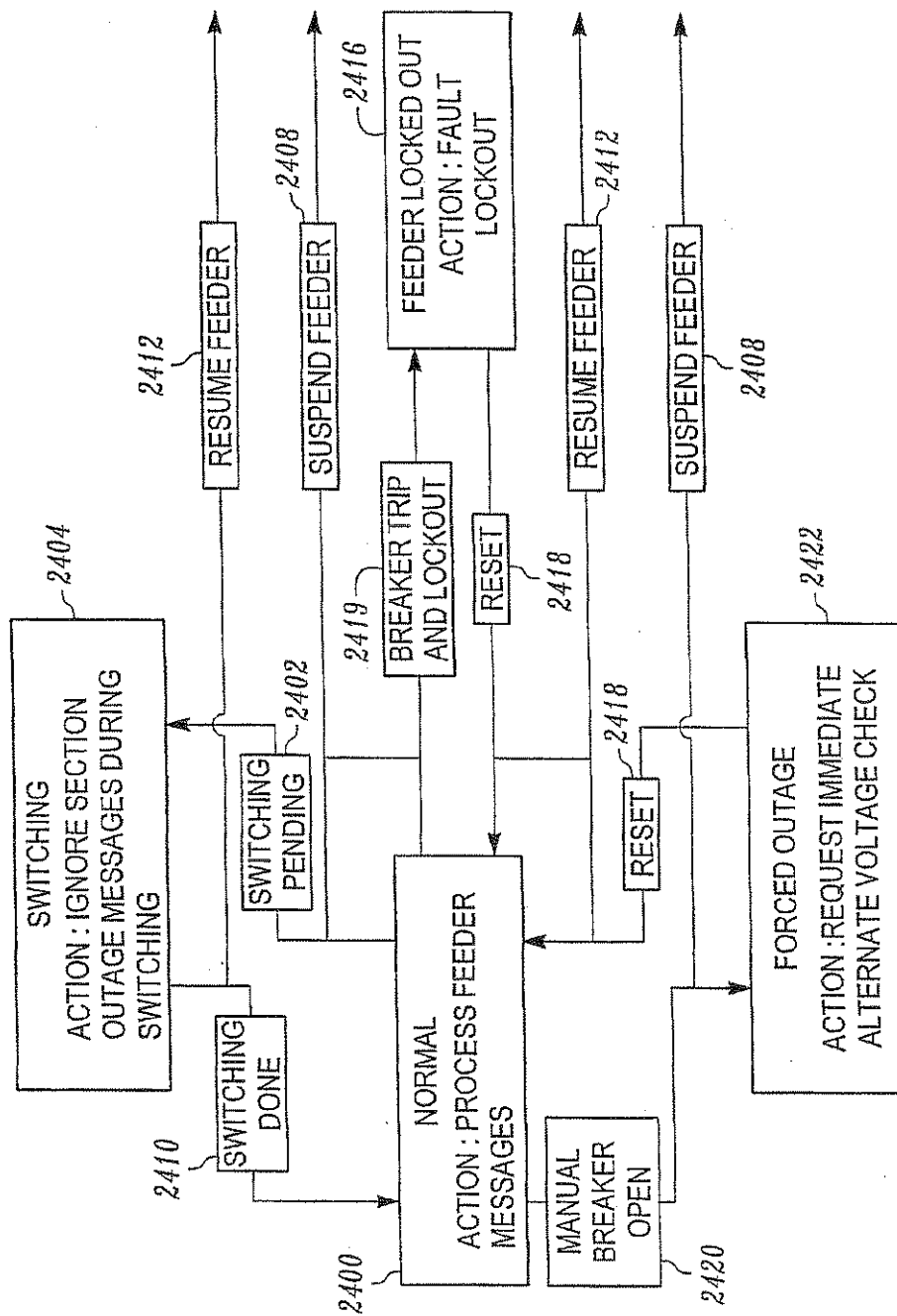


FIG. 24

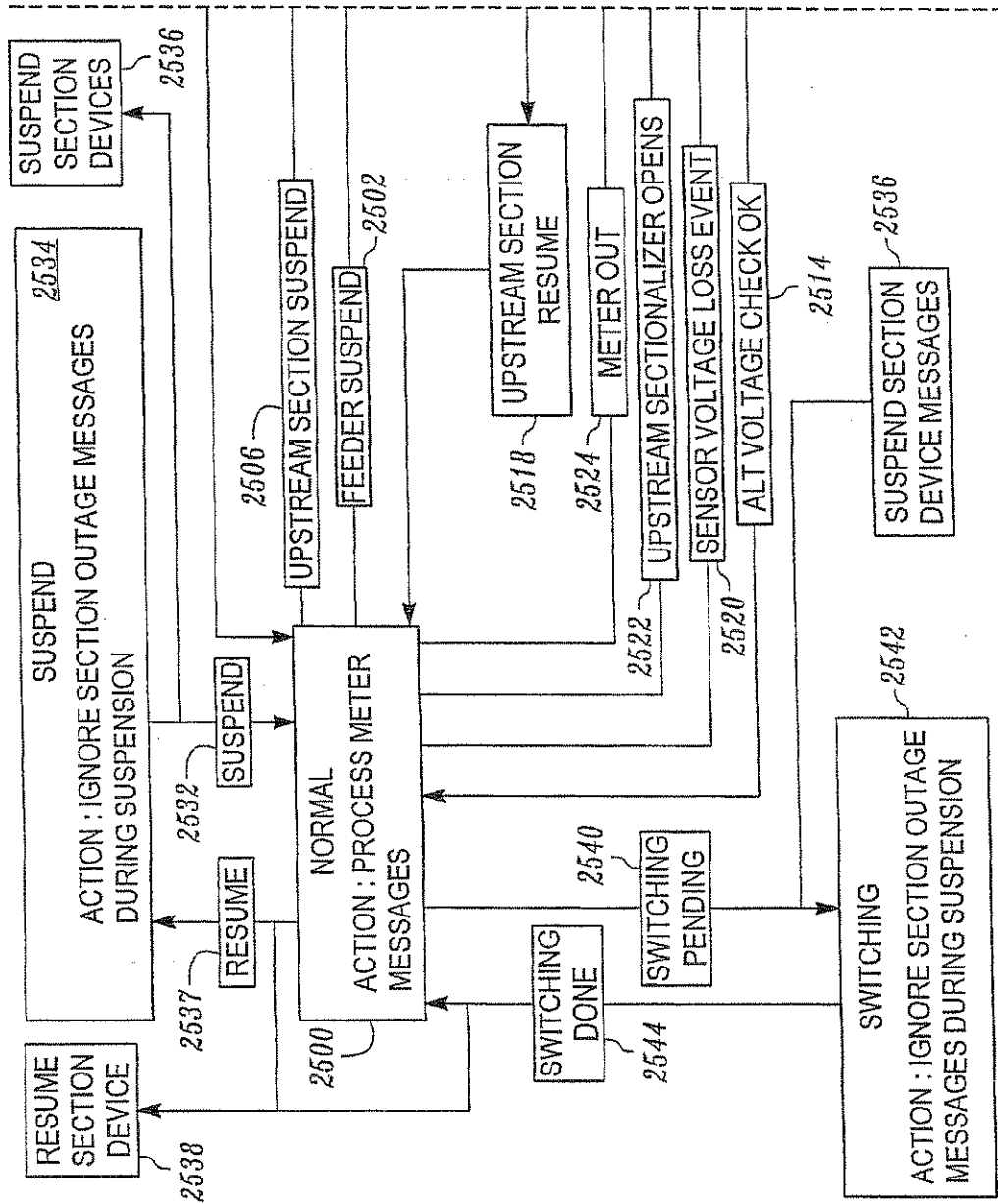


FIG. 25A

FIG. 25
FIG. 25A FIG. 25B

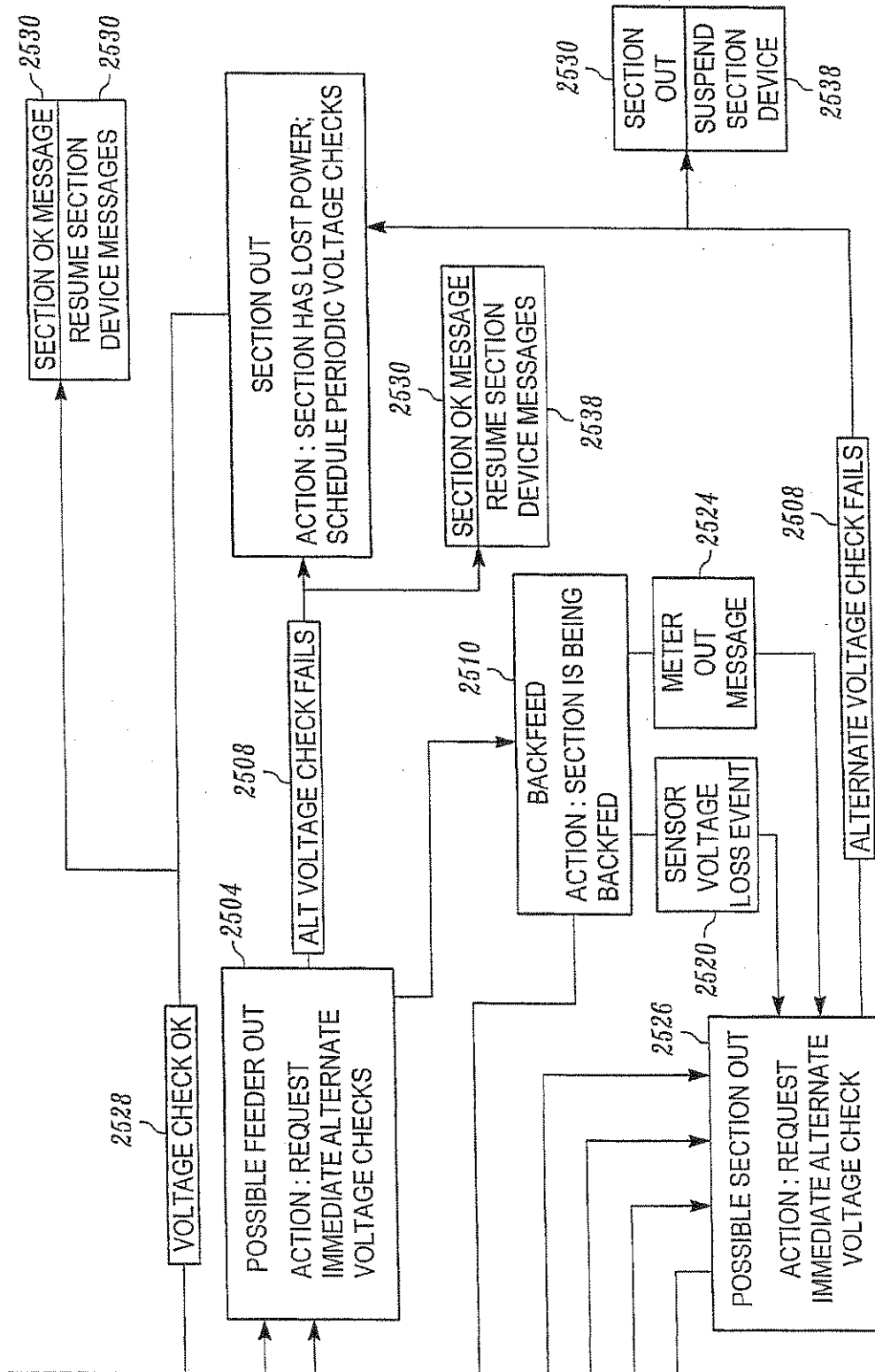


FIG. 25B

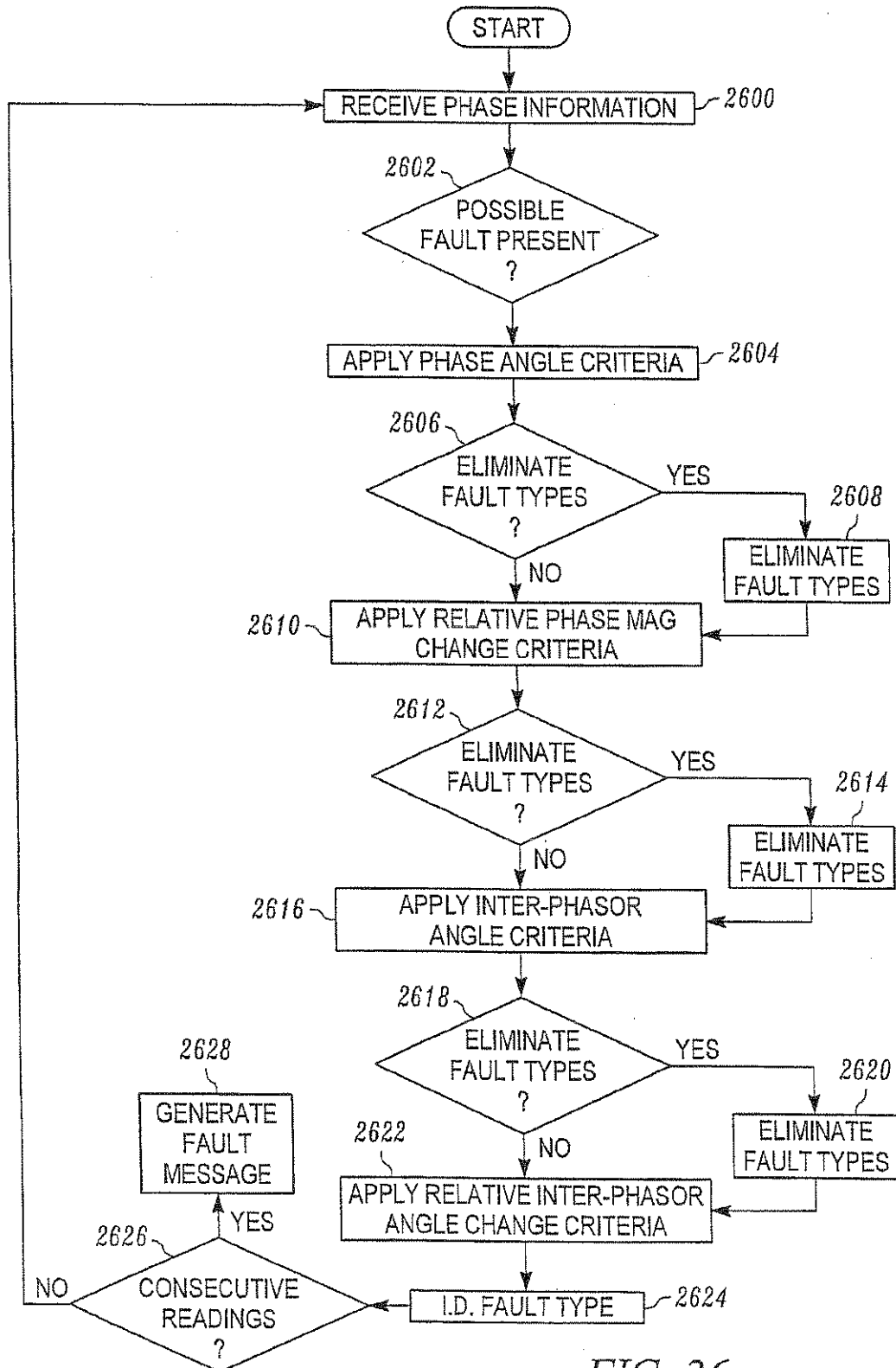


FIG. 26